

The diagram illustrates the power supply connections for three test points:

- TP1** is connected to **+5V**.
- TP2** is connected to **+3V3** and is also labeled with **PWR\_FLAG**.
- TP3** is connected to **GND**.

IC1  
WLR089U0-LRM

Pin connections for IC1 (WLR089U0-LRM):

- 1: GND\_1
- 2: GND\_2
- 3: VCC\_1
- 4: VCC\_2
- 5: PA07
- 6: PA08
- 7: PA27
- 8: PA16
- 9: PA17
- 10: PA18
- 11: PA19
- 12: GND\_3
- 13: PA15
- 14: PA14
- 15: GND\_4
- 16: S5\_MOSI
- 17: S5\_SS
- 18: S5\_SCK
- 19: S5\_MISO
- 20: PA22
- 21: USB\_DP
- 22: PA24
- 23: GND\_5
- 24: GND\_6
- 25: RESET
- 26: VSW
- 27: VDDCORE
- 28: PA28
- 29: PA31
- 30: PA30
- 31: PA05
- 32: PA04
- 33: PB03
- 34: PA06
- 35: GND\_7
- 36: GND\_8
- 37: GND\_9
- 38: GND\_10
- 39: GND\_11
- 40: GND\_12
- 41: TPAD\_GND

Additional components and connections:

- +3V3 GND
- USB\_DM
- RESET
- VSW
- VDDCORE
- PA28
- PA31
- PA30
- PA05
- PA04
- PB03
- PA06
- GND\_7
- GND\_8
- GND\_9
- GND\_10
- GND\_11
- GND\_12
- TPAD\_GND
- +3V3
- R1 100k
- RESET
- SW1 SW\_Push
- GND

The image shows two pin connection diagrams for headers J3 and J5. Header J3 is a 10-pin female header with pins numbered 1 to 10. Pin 1 is connected to +5V, and pin 10 is connected to GND. The pins are populated with the following components: PA07 (pin 2), PA08 (pin 3), PA27 (pin 4), PA18 (pin 5), PA19 (pin 6), PA15 (pin 7), PA14 (pin 8), and an empty pin 9. Header J5 is a 10-pin female header with pins numbered 1 to 10. Pin 10 is connected to +3V3, and pin 1 is connected to GND. The pins are populated with the following components: PA06 (pin 9), PB03 (pin 8), S0\_UART\_TX (pin 7), S0\_UART\_RX (pin 6), PA28 (pin 5), VSW (pin 4), VDDCORE (pin 3), PA22 (pin 2), and an empty pin 1.

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