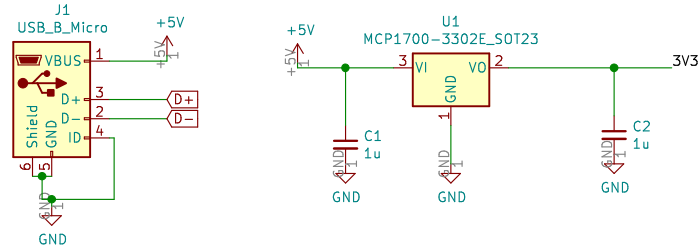
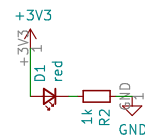


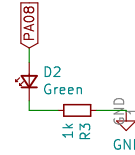
POWER CIRCUITRY



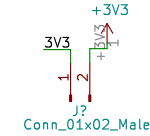
POWER LED



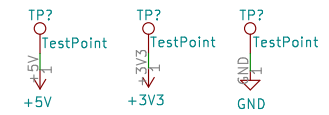
GPIO STATUS LED



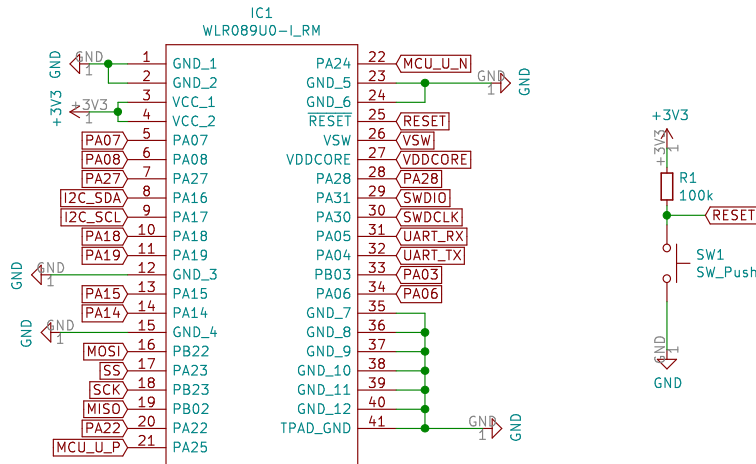
IDD MEASUREMENT



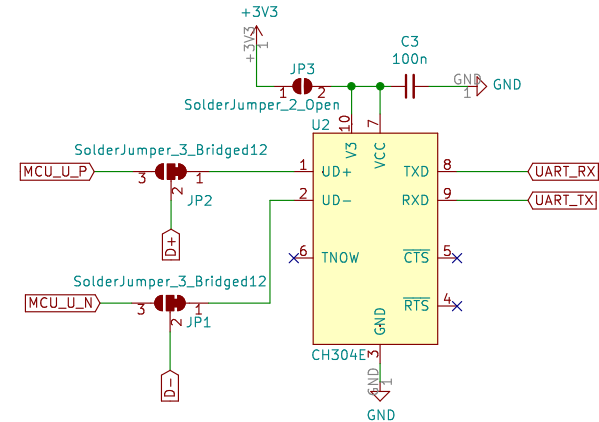
TEST POINTS



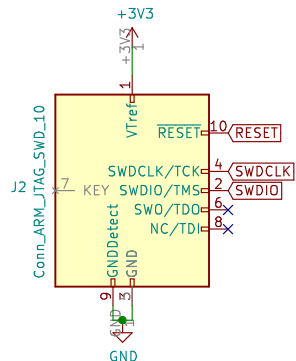
Microchip WLR089U0-SAMR34 Module



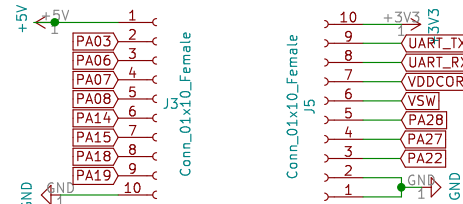
USB-UART USING CH340E via JP3 short (OR) USB-MCU_USB via JP1,JP2 short



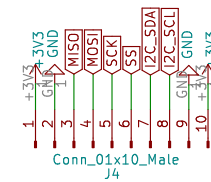
ARM CORTEX SWD PROGRAMMING 10 PIN HEADER



GPIO PIN HEADERS



I2C & SPI HEADER



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File: WLR089U0-DevBoard.sch

Title: WLR089U0-SAMR34 LoRaWAN Development Board

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Rev: V1
Id: 1/1