Jithin Saji Isaac

Assistant Professor

Himalaya Society, Ghatkopar West, Mumbai 400084. jithin at the rate dbit dot in 99 202 8 &(50+8) & (60+1)



SKILLS

Embedded System

Internet of Things

Signal Processing

Image Processing

EXPERIENCE

Don Bosco Institute of Technology, Mumbai- Assistant Professor, Department of Electronics & Telecommunication Engineering

JUNE 2012 - PRESENT

Subjects taught:

- Analog Electronics
- Control Systems
- Analog Communication
- Discrete & Digital Signal Processing
- Signals & Systems
- Microprocessors

Tech Mahindra, Pune - Technical Associate

NOV 2010 -SEPT 2011

- Software Testing
- Analysis & formation of Test cases

Biotronic System, Mumbai - Embedded Programmer

JUNE 2010 - SEPT 2010

- Post B.E. degree Internship
- ATMEL AVR Programming
- Project: Keithley DMM data extraction

EDUCATION

Shah & Anchor Kutchhi Engineering College, Mumbai - Bachelor of Engineering (Electronics Engineering)

AUGUST 2006 - JUNE 2010

Vivekanand Education Society's Institute of Technology, Mumbai - *Master of Engineering (Electronics & Telecommunication Engineering)*AUGUST 2013 - AUGUST 2015

For more detailed information, kindly visit my website which I update regularly.

Personal portfolio page @ https://jithinisaac.dblabs.in