

ETCS204-COMPUTER ORGANIZATION AND ARCHITECTURE

Semester - 4

Unit 1 Notes



UNIT - 1 Syllabus

Computer Arithmetic and Register transfer language: Unsigned notation, signed notation, binary coded decimal, floating point numbers, IEEE 754 floating point standard, Micro-operation, Bus and Memory Transfers, Bus Architecture, Bus Arbitration, Arithmetic Logic, Shift Micro operation, Arithmetic Logic Shift Unit.

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