ETCS204-COMPUTER ORGANIZATION AND ARCHITECTURE

Semester - 4



UNIT - 3 Syllabus

Control Design: Instruction sequencing & interpretation, Hardwired & Micro Programmed (Control Unit), Microrogrammed computers, Microcoded CPU: Pentium processor. Specifying a CPU, Design & implementation of simple CPU, General Register Organization, Stack Organization, Instruction Formats, Addressing Modes, Internal architecture of 8085 microprocessor.