

Getting Started with Freescale MQX™ RTOS and MDK-ARM Keil uVision4

PRODUCT:	Freescale MQX™ RTOS
PRODUCT VERSION:	Freescale MQX 3.8.0 (or later)
DESCRIPTION:	Using ARM-MDK Keil uVision4 Tools with Freescale MQX™ RTOS
RELEASE DATE:	Dec 15 th , 2011

How to Reach Us:

Home Page:

www.freescale.com

Web Support:

<http://www.freescale.com/support>

USA/Europe or Locations Not Listed:

Freescale Semiconductor, Inc.
Technical Information Center, EL516
2100 East Elliot Road
Tempe, Arizona 85284
1-800-521-6274 or +1-480-768-2130
www.freescale.com/support

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH
Technical Information Center
Schatzbogen 7
81829 Muenchen, Germany
+44 1296 380 456 (English)
+46 8 52200080 (English)
+49 89 92103 559 (German)
+33 1 69 35 48 48 (French)
www.freescale.com/support

Japan:

Freescale Semiconductor Japan Ltd.
Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku,
Tokyo 153-0064
Japan
0120 191014 or +81 3 5437 9125
support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor China Ltd.
Exchange Building 23F
No. 118 Jianguo Road
Chaoyang District
Beijing 100022
China
+86 10 5879 8000
support.asia@freescale.com

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.



Freescale™ and the Freescale logo are trademarks of Freescale Semiconductor, Inc. ARC, the ARC logo, ARCCore, ARCform, ARChitect, ARCCompact, ARCTangent, BlueForm, CASSEIA, High C/C++, High C++, iCon186, MetaDeveloper, MQX, Precise Solution, Precise/BlazeNet, Precise/EDS, Precise/MFS, Precise/MQX, Precise/MQX Test Suites, Precise/RTCS, RTCS, SeeCode, TotalCore, Turbo186, Turbo86, V8 μ RISC, V8 microRISC, and VAutomation are trademarks of ARC International. High C and MetaWare are registered under ARC International.

All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc. 2010. All rights reserved.

Rev. 01
12/2011

Table of Contents

1 Read Me First	2
2 Building the MQX Libraries.....	3
2.1 Compile-time Configuration.....	3
2.2 Build Configurations	4
2.3 Batch Build in uVision4 IDE.....	4
3 MQX Task Aware Debugging	6
3.1 Debugging MQX Applications in uVision4.....	6
3.2 MQX-Viewer TAD Debugger Plug-in.....	11
4 Using the MQX DebugIO Driver with uVision4 IDE.....	15

1 Read Me First

This document describes steps required to configure the ARM-MDK Keil uVision4 development tools and use it to build, run and debug applications of the Freescale MQX™ RTOS operating system. Refer to “Getting Started” and other user documentation included within the latest Freescale MQX™ RTOS installation for more details not specifically related to ARM development tools.

Get the latest Freescale MQX™ RTOS at <http://www.freescale.com/mqx>.

Find also more information related to Freescale Kinetis platform support in uVision4 tools in the http://www.arm.com/files/pdf/Kinetis_LAB.pdf document.

2 Building the MQX Libraries

2.1 Compile-time Configuration

Major compile-time configuration options are centralized in a single user configuration file located in

```
<install_dir>/config/<board>/user_config.h
```

This user configuration file is included internally by private configuration files in MQX PSP and BSP.

To share configuration settings between different boards, the user_config.h file may include other header files with common settings. The header files may only be located in the same <board> directory or in the “common” directory:

```
<install_dir>/config/common
```

All MQX configuration files are also *indirectly* used by other core components like RTCS, MFS, Shell etc. “Indirectly” means that the MQX PSP and BSP must be build first, which causes the configuration file being copied into the output (lib) directory. The other components then include the configuration file from the /lib output directory.

Caution: Until the PSP or BSP libraries are rebuilt, configuration changes made in the user_config.h file are not used by any other MQX component. On the other hand, after the PSP and BSP libraries are re-compiled with a new configuration, it is important to recompile the other libraries so the compiled code is consistent with the configuration file. See the next section for more details.

2.1.1 Build Process

After any change to the compile-time user configuration file or MQX kernel source files, the MQX libraries need to be re-built. The build process is similar with all core components:

- The output directory for any MQX library component is <install_dir>/lib/<board>.<compiler>/<component>
- For example the MQX PSP and BSP libraries for the TWR-K60N512 board are copied into the /lib/twrk60n512.uv4/psp and /lib/twrk60n512.uv4/bsp directories after successful build process.
- All public header files needed by an application to make use of the library are also copied from internal include folders to the same output directory.
- During PSP or BSP build process, also the user_config.h file and other header files from the config/<board> and config/common directories are copied into the lib/<board>.uv4 output directory.
- Other components like RTCS, MFS, Shell or USB use the copied configuration files only.
- Applications which make use of any MQX library do not need to make any reference to the internal source and include paths of the MQX components. Applications use solely the paths in the /lib/<board>.<compiler> as the search paths for header files or libraries.

To summarize the points above, there are simple **rules to obey** when re-building the MQX libraries.

- After any change to the /config/common/user_config.h file, all MQX libraries should be re-built.

- The PSP and BSP libraries must be build first, before the MFS, RTCS, USB, Shell and other libraries.

Important: No changes should be made to header files in the output build directory (`/lib`). The files get overwritten any time the libraries are built.

2.2 Build Configurations

Each uVision4 project in Freescale MQX™ RTOS contains multiple compiler/linker configurations (so called build „targets“).

Two different types of build targets exist for different compiler optimization settings:

- **Debug** – the compiler optimizations are turned off or set to low. The compiled code is easy to debug but may be less effective and much larger than the Release build. All output libraries have `_d` postfix in the file name (e.g. `rtcs_<board>_d.a`).
- **Release** – the compiler optimizations are set to maximum. The compiled code is very hard to debug and should be used for final applications only. There is no postfix in the output file name (e.g. `rtcs_<board>.a`).

Build target name of any MQX application project makes a reference either to **Debug** or **Release** builds of the core libraries. On top of that the target names also specify board memory configuration which gets built. For example:

Devices with internal Flash memory (e.g. TWR-K60N512):

- **Int. Flash Release** – this target is suitable for final application deployment. When programmed to Flash, the application starts immediately after reset. Variables are allocated in internal SRAM memory.
- **Int. Flash Debug** – same as above, only the Debug-compiled libraries are used. This target is suitable for debugging before deployment. On boards without external memory, this is the only target suitable for debugging larger applications.

Boards and devices with internal Flash memory and additional external RAM for data (TWR-K70F120M):

- **Int Flash <mem>Data Debug** – The name of each target additionally defines a memory used as the default data storage. For example the application built with target named “Int Flash DDRData Debug” will execute code out of internal Flash memory and will use the DDR memory for data storage.

Boards with external RAM memory:

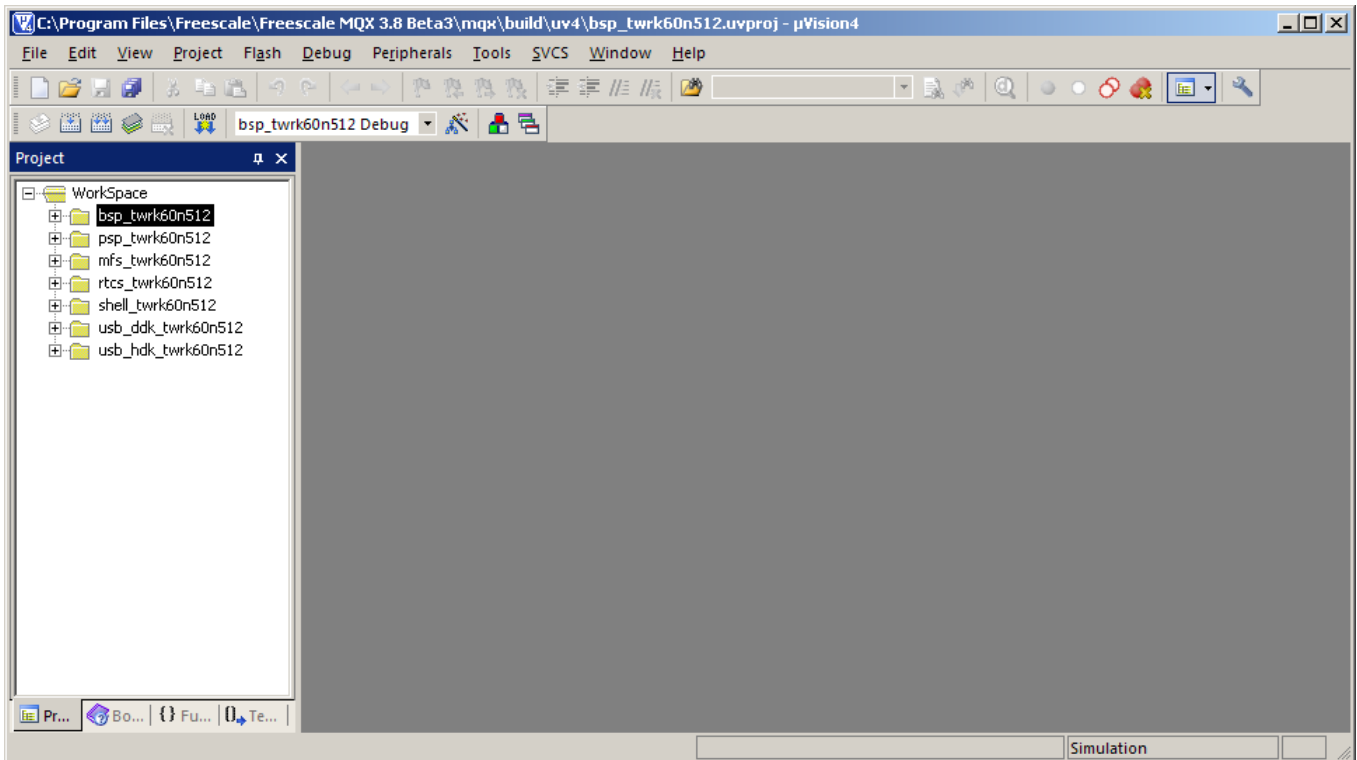
- **Ext. Ram Debug** – solely for debugging purposes with code located in external RAM memory. Both code and variables are located in this external memory. Application executable is loaded to RAM automatically by the debugger.

Refer to BSP-specific information included in the latest MQX installation for a description of build targets specific to particular board.

2.3 Batch Build in uVision4 IDE

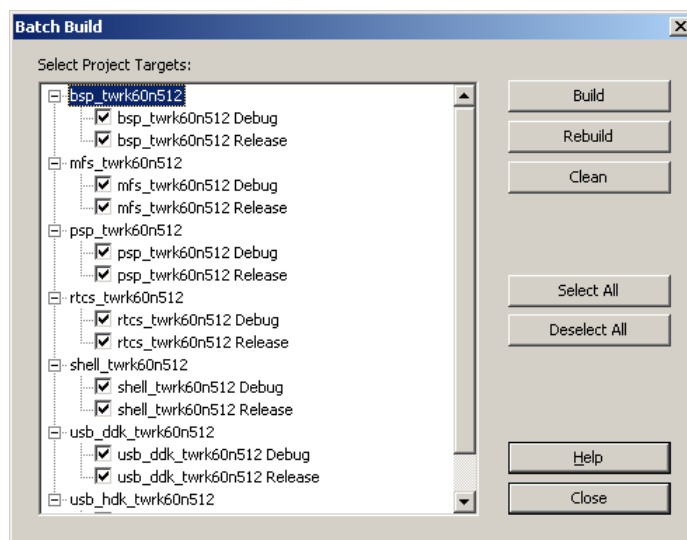
With uVision4, the MQX build process can be simplified by using Batch Build feature. For each supported board, there is a Multi-Project Workspace file which includes build projects for all related MQX libraries:

<install_dir>/config/<board>/uv4/build_libs.uvmpw



The Workspace file contains Batch Build configurations which can be used to build all MQX libraries at once.

- Go to menu “Project / Batch Build...” in the uVision4 IDE.
- Select libraries and targets to build. Note that the projects are sorted in alphabetical order but this does not affect the build order. The build order is set properly in the Multi-Project Workspace definition.
- Select the libraries and targets you want to build in a batch. It is recommended to rebuild at least all Debug or all Release targets at once.
- Press the “Rebuild” button to start the batch build process.



3 MQX Task Aware Debugging

MQX Task Aware Debugging plug-in (TAD) is an optional extension to a debugger tool which helps to visualize internal MQX data structures, task-specific information, I/O device drivers and other MQX context data.

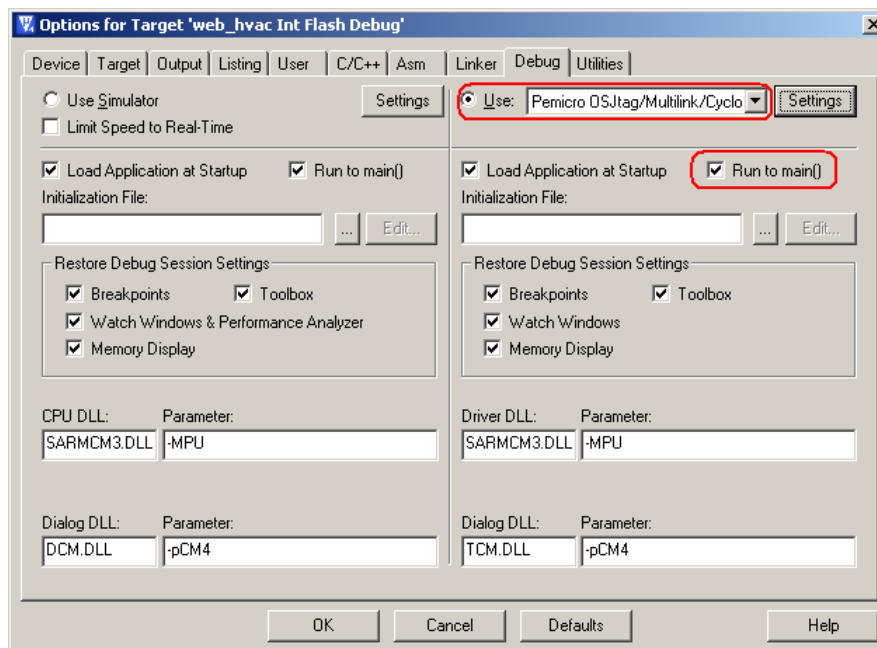
The MQX TAD plugin for uVision4 is called MQX-Viewer and is installed by an add-on installer included with the MQX installation.

3.1 Debugging MQX Applications in uVision4

Loading and debugging MQX applications is an easy task with ARM Keil uVision4 tool and it is not really different from debugging classic non-OS applications. Make sure the correct debugger interface is selected in the project options and correct processor configuration is selected.


3.1.1 Using OSJTAG Debugger Connection

By default, the MQX example projects are configured to use on-board OSJTAG debugger connection. You can double check the debugger connection settings in project options, the “Debug” tab:

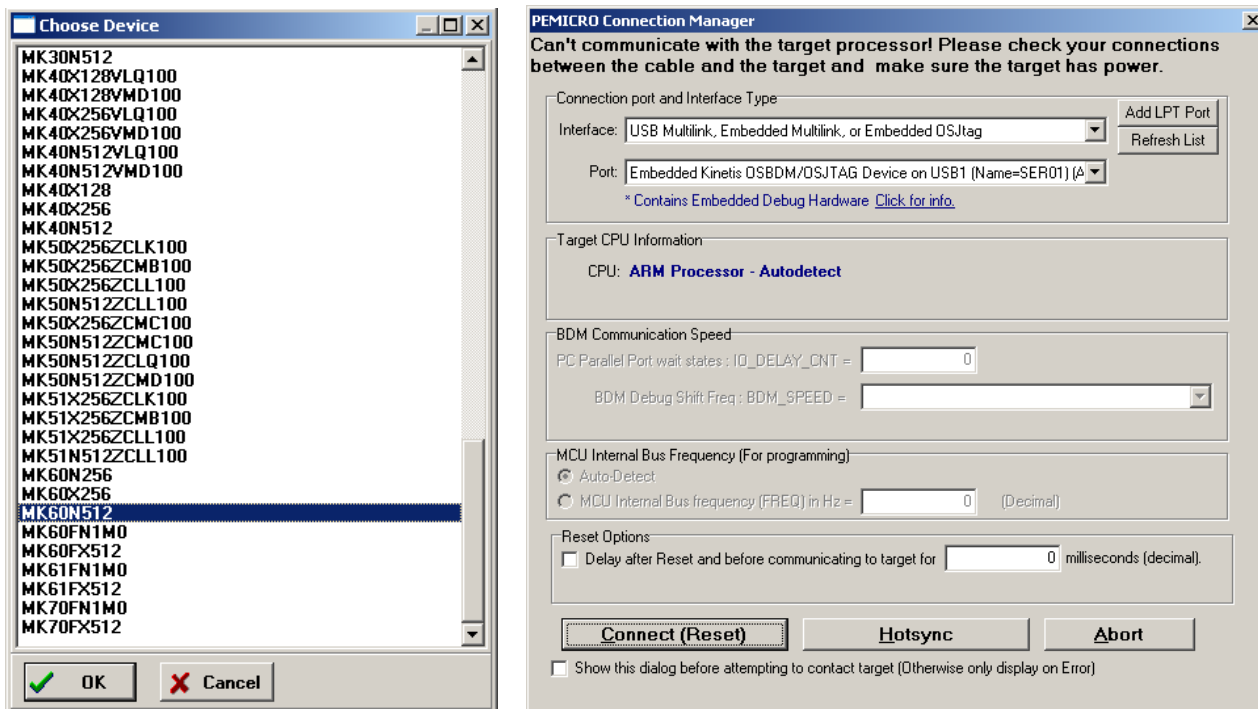


You may also want to select the “Run to main()” option which forces the debugger to execute the startup code until the main C entry point function is reached. When this option remains unchecked, the debugger stops execution at the first executed instruction at the reset vector.

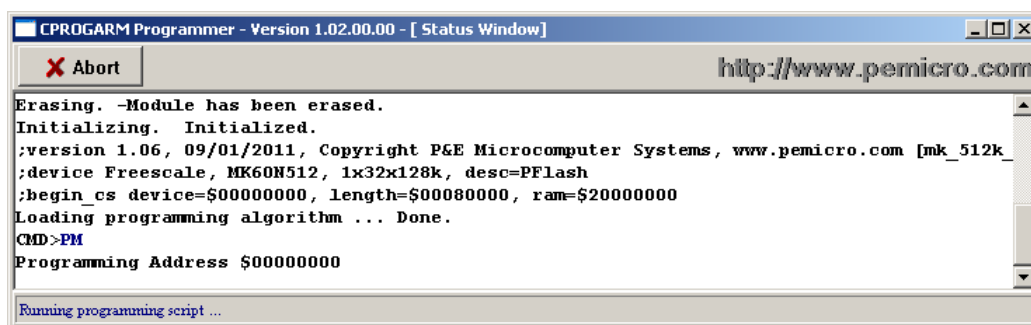
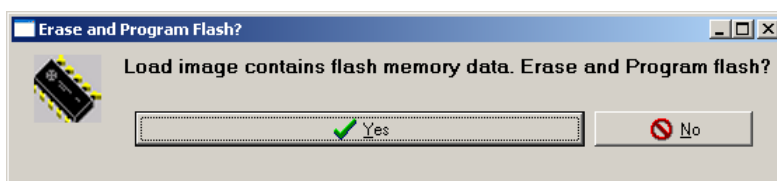
In either case, be aware that at such a breakpoint, the MQX Operating System is not yet fully running so use of TAD plugin features (as described in later sections) is limited.

When an MQX application is compiled and linked with all MQX libraries, press the “Start/Stop Debug Session...”  button on the toolbar to initiate the debugger session.

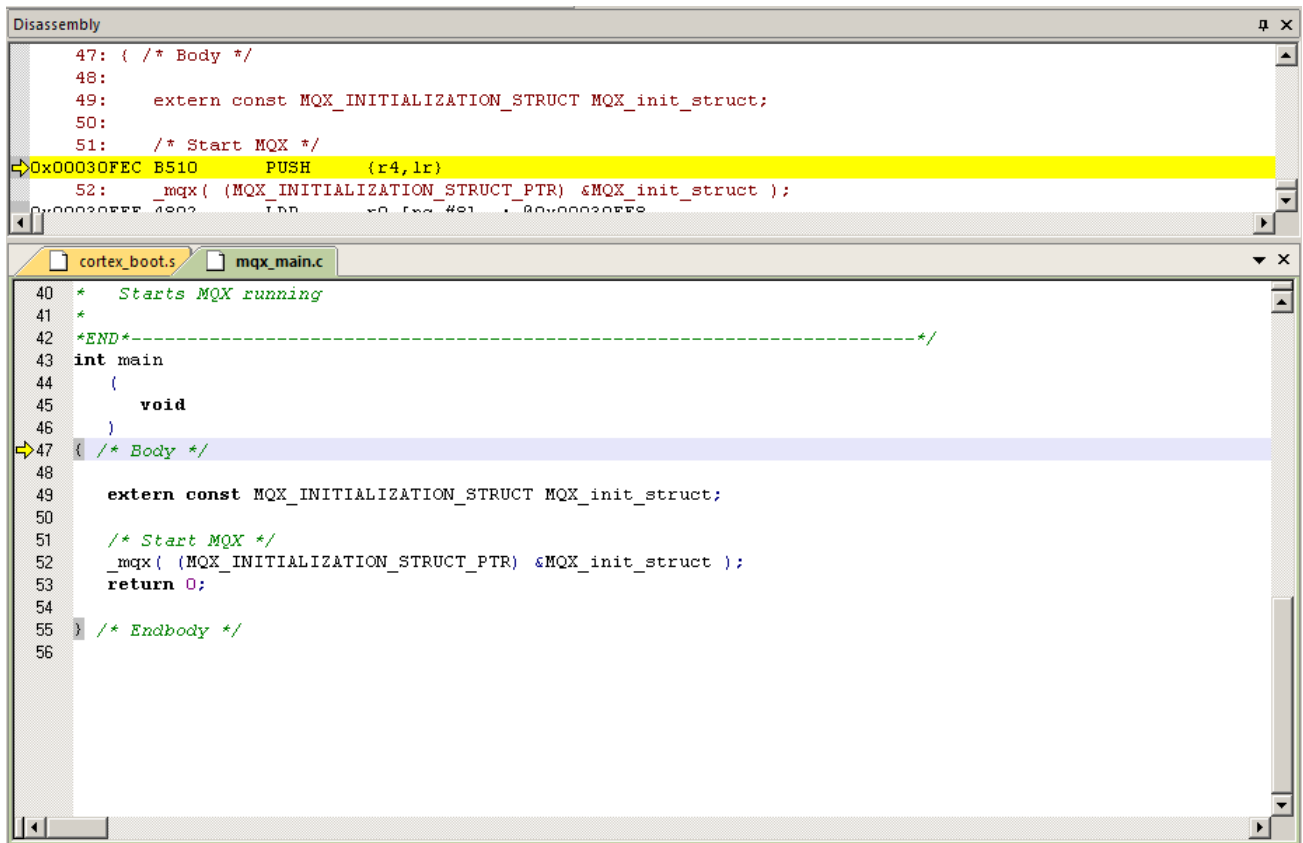
With the OSJTAG debugger connection, the first time you will try to execute the application you will be prompted to select the target processor. Then in the connection manager, press the Connect (Reset) button to establish connection between the PC Host and target board.



...and use the “Yes” button to load application to target Flash memory and wait until the Flashing process finishes:



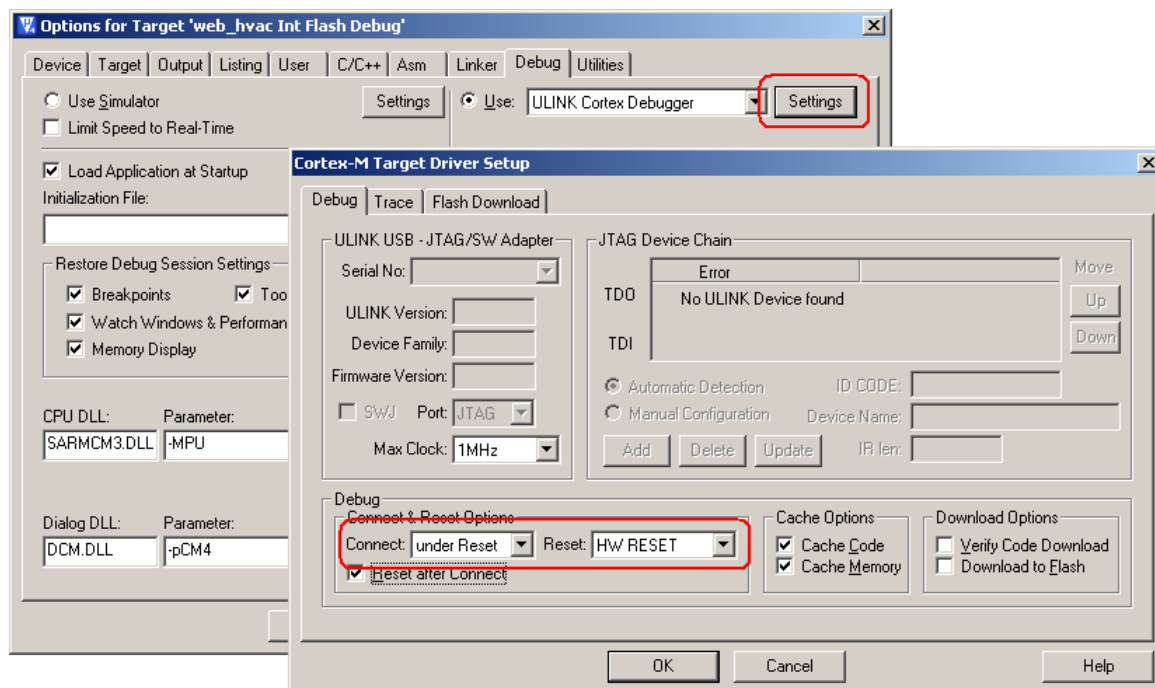
When the application gets loaded, it is executed under the debugger and stops at the initial breakpoint:



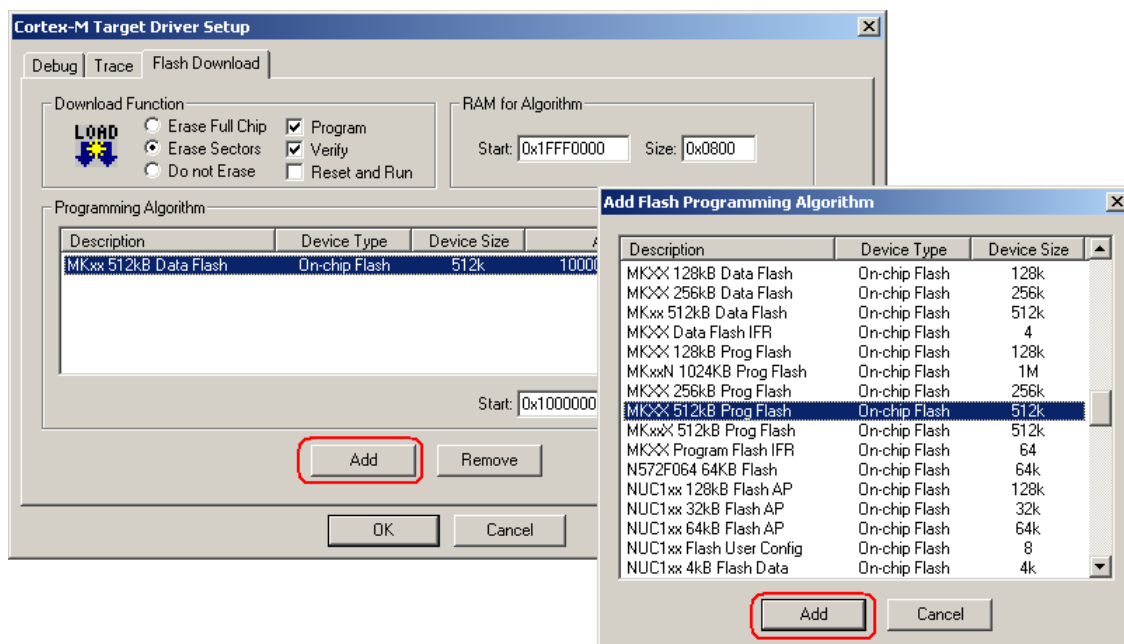
3.1.2 Using ARM Keil ULink Debugger Connection

Use of ULINK or ULINKpro debugger interface requires additional configuration steps as described below.

In the project options, the “Debug” tab, select the ULINK connection, press the Settings button and make sure the Connection options are set as shown in the picture:



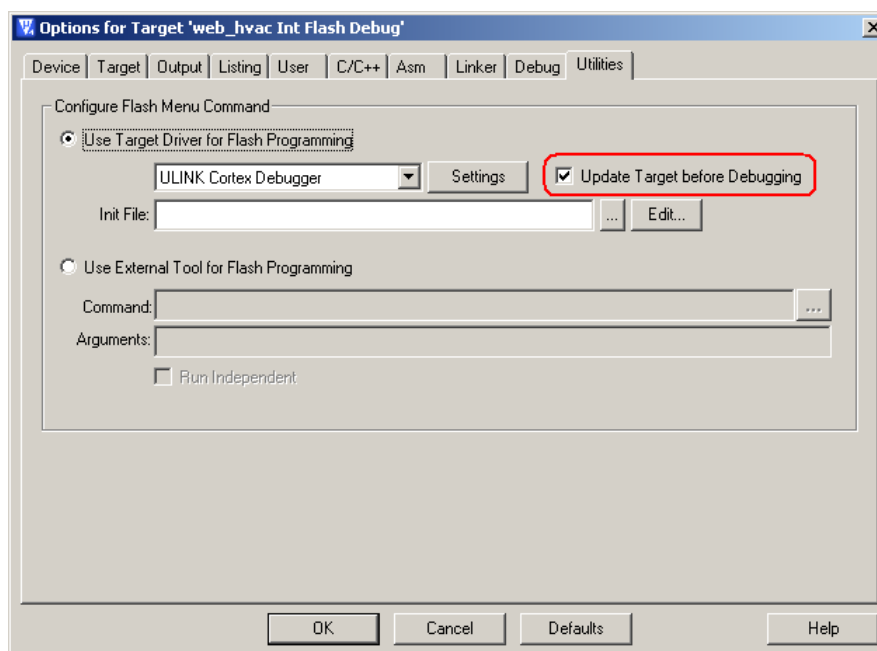
In the same setup dialog, select the “Flash Download” tab and add programming algorithm suitable for the target processor:




Select the Flash algorithm according to the following table:

Description	Target processor
MKXX 256kB Prog Flash	TWR-K40X256 Program Flash (0x00000000 – 0x00003FFFF)
MKXX 256kB Data Flash	TWR-K40X256 Data Flash (0x10000000 – 0x10003FFFF)
MKXX 512kB Prog Flash	TWR-K60N512 Program Flash (0x00000000 – 0x00007FFFF)
MKxxN 1024KB Prog Flash	TWR-K70FN1M Program Flash (0x00000000 – 0x0000FFFFF)

Back in the Options dialog, select the “Utilities” tab and make sure the “Update Target before Debugging” option is selected:

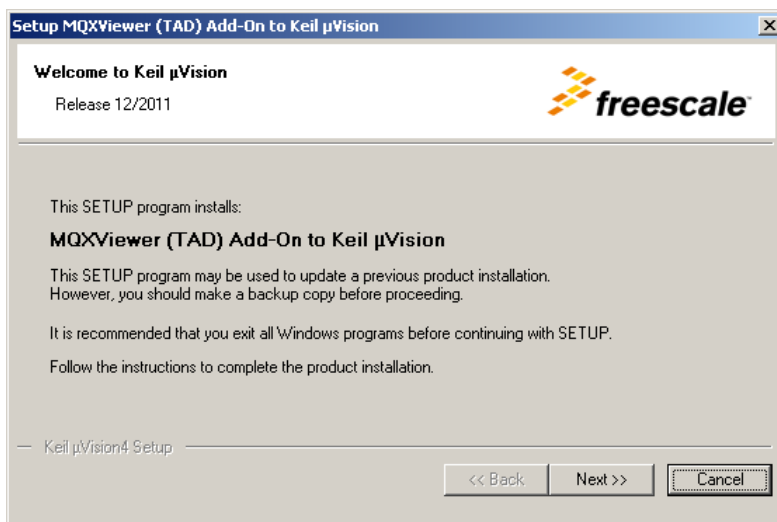


With the settings described above, press the “Start/Stop Debug Session...”  button to initiate the debugger session. It automatically loads the application to the target Flash memory and executes the application until it reaches the initial breakpoint.

3.2 MQX-Viewer TAD Debugger Plug-in

3.2.1 Installing uVision4 TAD

The MQX-Viewer TAD plug-in is installed with add-on installer distributed inside the MQX installation package. The add-on installer is executed automatically during the MQX installation if uVision4 tool exists and if the Keil extensions are selected in MQX setup.

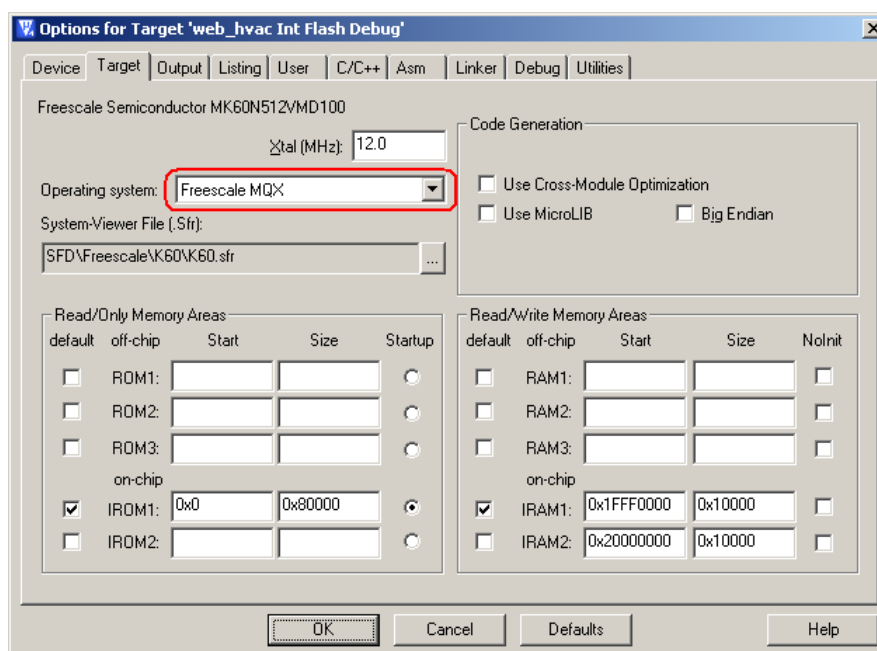


In case you need to install the MQX-Viewer TAD plug-in manually after the MQX has been installed, simply start the add-on installer by running the

```
<MQX Installation>\tools\keil_extensions\uVision4\MDK_MQX-Viewer_AddOn.exe
```

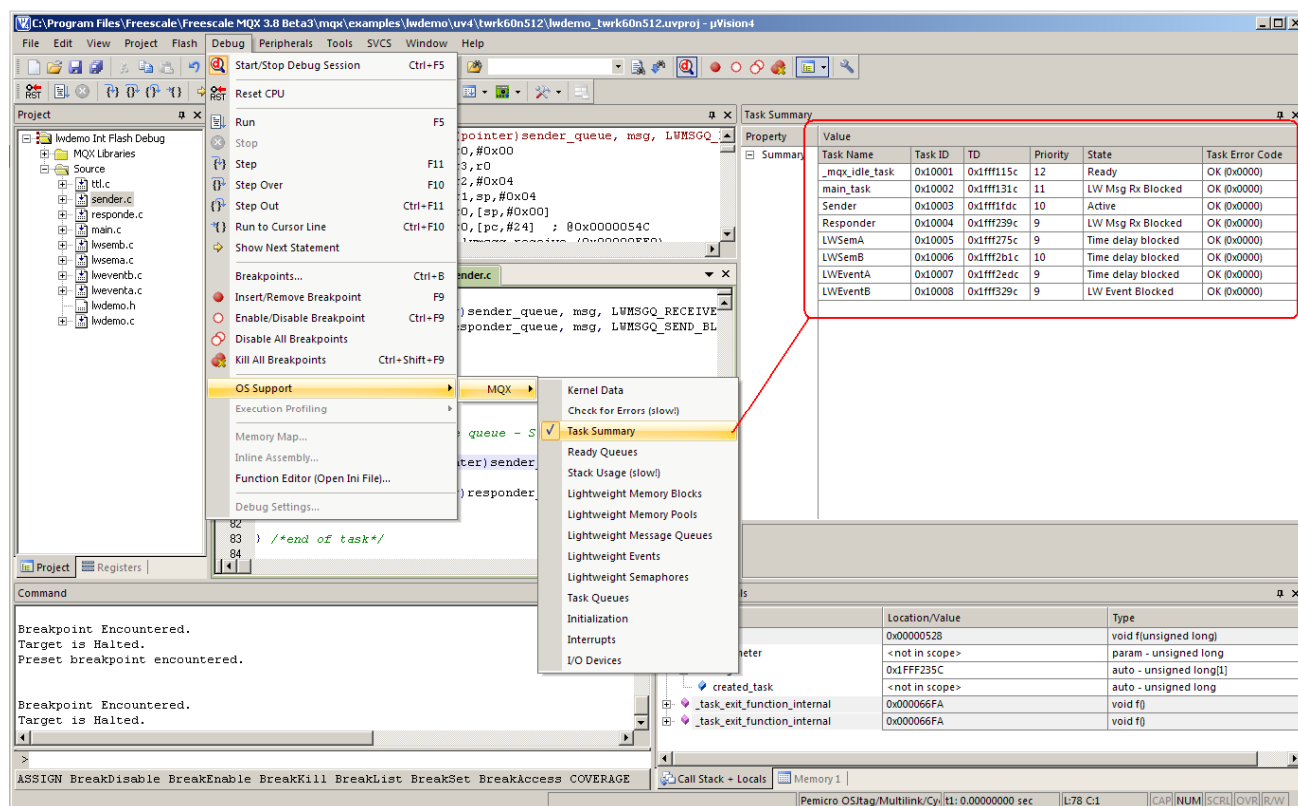
The add-on installer installs the plug-in as the `<uVision4>\ARM\BIN\MQX-Viewer.dll` file and registers it in the main `TOOLS.ini` file as `RTOSx=MQX-Viewer.dll ("Freescale MQX")` in the `[ARM]` and `[ARMADS]` sections.

When MQX-Viewer is installed, the MQX application projects should be set to use this plug-in during debugger sessions. In project options, select the “Target” tab and select the “Freescale MQX” operating system:



3.2.2 Using MQX-Viewer TAD Screens

Using the MQX or RTCS menu in the uVision “OS Support” menu, several TAD “screens” may be opened during the debugging session.



The most helpful and frequently used screens are shown in the pictures below:

- *Task Summary* – overview about all tasks created in the MQX application.

Task Summary						
Property	Value					
Summary	Task Name	Task ID	TD	Priority	State	Task Error Code
	_mqx_idle_task	0x10001	0x1fff115c	12	Ready	OK (0x0000)
	main_task	0x10002	0x1fff131c	11	LW Msg Rx Blocked	OK (0x0000)
	Sender	0x10003	0x1fff1fdc	10	Active	OK (0x0000)
	Responder	0x10004	0x1fff239c	9	LW Msg Rx Blocked	OK (0x0000)
	LWSemA	0x10005	0x1fff275c	9	Time delay blocked	OK (0x0000)
	LWSemB	0x10006	0x1fff2b1c	10	Time delay blocked	OK (0x0000)
	LWEventA	0x10007	0x1fff2edc	9	Time delay blocked	OK (0x0000)
	LWEventB	0x10008	0x1fff329c	9	LW Event Blocked	OK (0x0000)

- *Stack Usage Summary* – displays information about interrupt and task stacks. Typically, stack overflow is a root cause of vast majority of problems in MQX user applications.

Property	Value					
[-] Summary	Task	Stack Base	Stack Limit	Stack Used	% Used	Overflow?
	_mqx_idle_task	0x1fff1300	0x1fff1230	0x1fff129c	48 %	No
	main_task	0x1fff1fc0	0x1fff13d0	0x1fff1e5c	11 %	No
	Sender	0x1fff2380	0x1fff2090	0x1fff221c	47 %	No
	Responder	0x1fff2740	0x1fff2450	0x1fff2694	22 %	No
	LWSemA	0x1fff2b00	0x1fff2810	0x1fff2a6c	19 %	No
	LWSemB	0x1fff2ec0	0x1fff2bd0	0x1fff2e2c	19 %	No
	LWEventA	0x1fff3280	0x1fff2f90	0x1fff31ec	19 %	No
	LWEventB	0x1fff3640	0x1fff3350	0x1fff358c	23 %	No
	Interrupt	0x1fff0b60	0x1fff0760	0x1fff0b18	7 %	No

- *Lightweight Memory Block Summary* – displays address, size and type information about each memory block allocated in the default memory pool by the MQX system or applications. Additional memory pools (if used) may be displayed using the “*Lightweight Memory Pools*” screen.

Property	Value				
Start:	0x1fff0750				
End:	0x2000fff0				
Size:	0x0001f8a0 (126.0K)				
Highest:	0x1fff3340 (10.0K, 8%)				
Pool valid?	Yes				
[-] Summary	Address	Size (hex)	Size (dec)	Owner	Type
	0x1fff0750	0x420	1056	System	Interrupt Stack
	0x1fff0b70	0xf0	240	System	System Stack
	0x1fff0c60	0xe0	224	System	Ready Qs
	0x1fff0d40	0x90	144	System	Interrupt Table
	0x1fff0dd0	0x20	32	System	Interrupt Vector
	0x1fff0df0	0x20	32	System	Interrupt Vector
	0x1fff0e10	0x40	64	System	I/O Serial polled device struct
	0x1fff0e50	0x40	64	System	I/O Device
	0x1fff0e90	0x40	64	System	I/O Serial polled device struct
	0x1fff0ed0	0x40	64	System	I/O Device
	0x1fff0f10	0x30	48	System	I/O I2C polled device struct
	0x1fff0f40	0x40	64	System	I/O Device
	0x1fff0f80	0x30	48	System	I/O I2C int. device struct
	0x1fff0fb0	0x40	64	System	I/O Device
	0x1fff0ff0	0x30	48	System	
	0x1fff1020	0x40	64	System	I/O Device
	0x1fff1060	0x30	48	System	File
	0x1fff1090	0x60	96	System	I/O Serial charq
	0x1fff10f0	0x60	96	System	
	0x1fff1150	0xb0	176	0x10001	Task Descriptor
	0x1fff1200	0x110	272	0x10001	Task Stack
	0x1fff1310	0xb0	176	0x10002	Task Descriptor
	0x1fff13c0	0xc10	3088	0x10002	Task Stack
	0x1fff1fd0	0xb0	176	0x10003	Task Descriptor
	0x1fff2080	0x310	784	0x10003	Task Stack
	0x1fff2390	0xb0	176	0x10004	Task Descriptor
	0x1fff2440	0x310	784	0x10004	Task Stack
	0x1fff2750	0xb0	176	0x10005	Task Descriptor
	0x1fff2800	0x310	784	0x10005	Task Stack

- *Lightweight Semaphores, Lightweight Events* – displays address and status of synchronization objects created by the MQX system or application. When a synchronization object is allocated as a global or static variable in the system, as an array element or as a structure member allocated as global or static variable, the TAD plug-in also displays the symbolic name of the object.

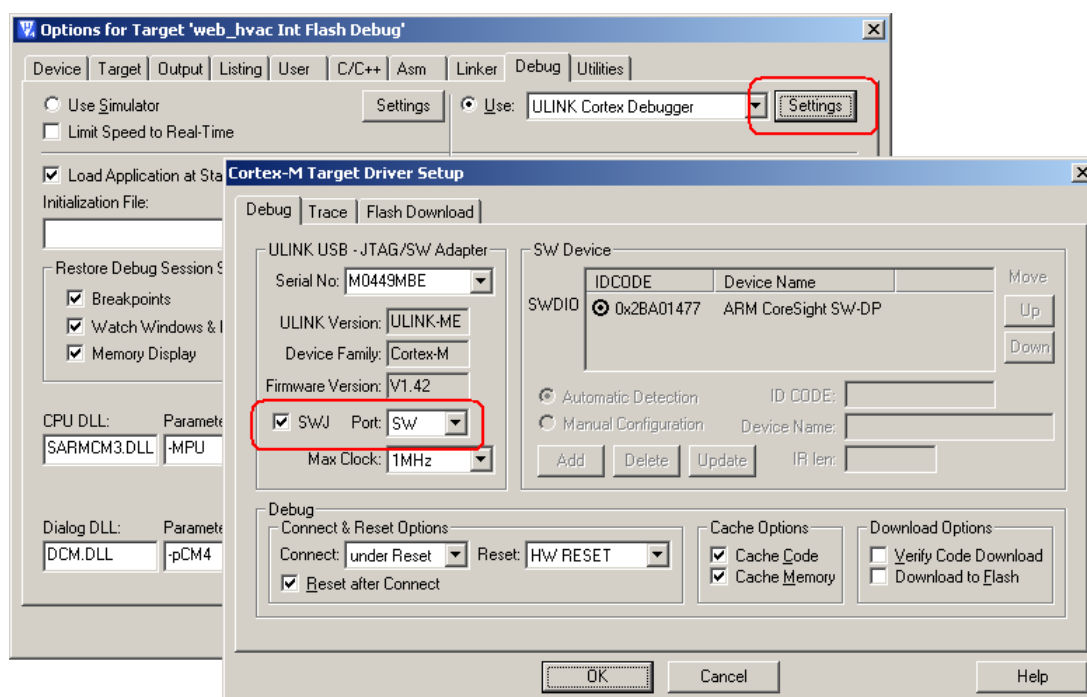
Lightweight Semaphores							🔍	✕		
Property		Value								
[-] Summary		LWSem	Valid	Value	Waiting#	Symbol				
		0x1fff05bc	Yes	1	0	mqx_kernel_data->COMPONENT_CREATE_LWSEM				
		0x1fff0530	Yes	1	0	mqx_kernel_data->TASK_CREATE_LWSEM				
		0x1fff0658	Yes	1	0	mqx_kernel_data->IO_LWSEM				
		0x1fff007c	Yes	8	0	lwsem				
Lightweight Events									🔍	✕
Property		Value								
[-] Summary		LWEvent	Valid	BitMask	Auto Clear	Waiting#	Symbol			
		0x1fff0058	Yes	0x00000000	No	1	lwevent			

4 Using the MQX DebugIO Driver with uVision4 IDE

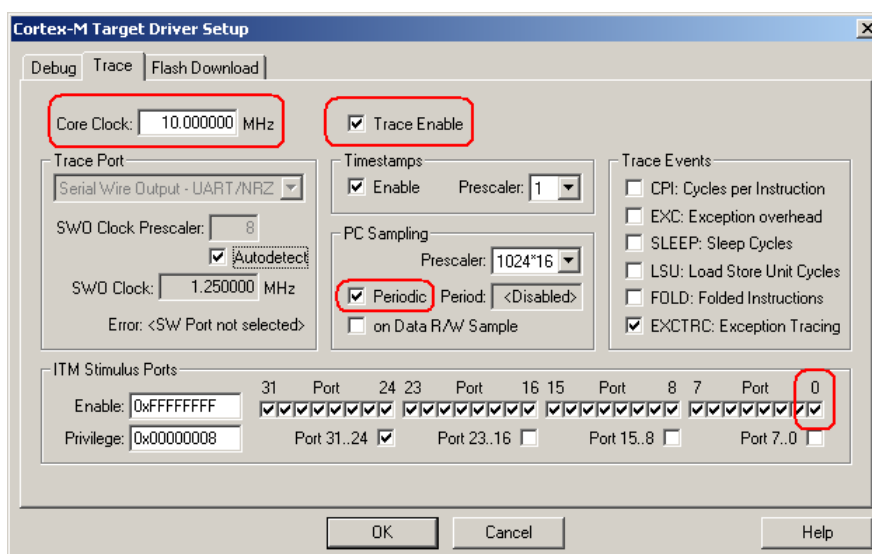
The MQX provides the DebugIO driver allowing the processor to communicate with PC host computer via a debugger probe. The DebugIO channel can also be used as a default console for standard input and output operations. See more details about this driver in the “Getting Started with Freescale MQX™ RTOS” document.

The MQX RTOS currently supports ARM CortexM Semihost and ITM technologies. The uVision4 IDE supports the ITM communication channel over the ULINK and ULINKpro debugger interfaces for both input and output directions.

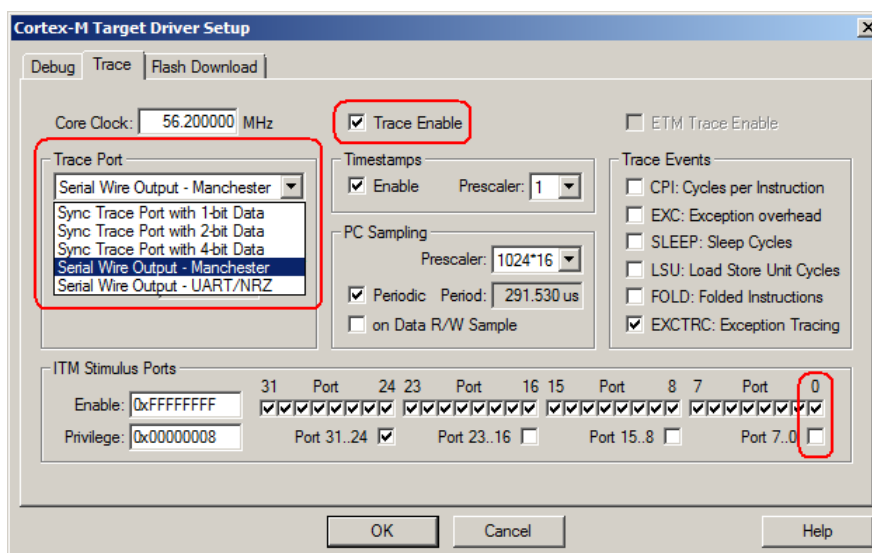
To set up the project for DebugIO communication, open the project options at the “Debug” tab, select the ULINK connection, press the Settings button and make sure the SWJ and Port options are set as shown in the picture:



Switch to Trace tab and check “Trace enable”, ITM Port 0 and other settings as shown in the picture below. The Core Clock should be set according to the real CPU setting:



For the ULINKpro debugger interface, also select the Manchester coding of the Trace port.



The console window can be opened during a debug session using the “View / Serial Windows / Debug (printf) Viewer” menu.

