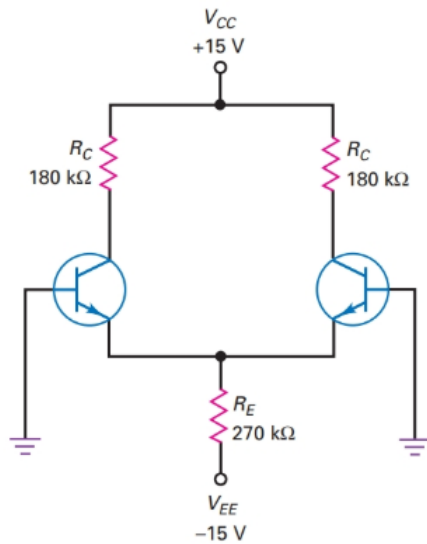


Differential Amplifier

Jiwook Kim

1 What are the ideal currents and voltages in Fig. 15-33?



Tail current

$$I_T = V_{EE}/R_E = 15\text{V}/270\text{k}\Omega = 55.6\mu\text{A}$$

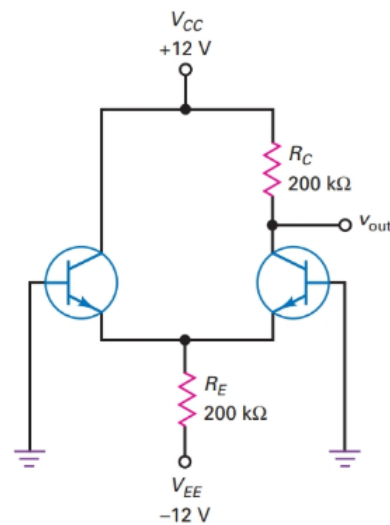
Each emitter current is half of the tail current

$$I_E = I_T/2 = 27.8\mu\text{A}$$

For ideal voltage

$$V_C = V_{CC} - I_C * R_C = 15\text{V} - 27.8\mu\text{A} * 180\text{k}\Omega = 10\text{V}$$

2 What are the ideal currents and voltages in Fig. 15-34?



Tail current

$$I_T = V_{EE}/R_E = 12\text{V}/200\text{k}\Omega = 60\mu\text{A}$$

Each emitter current is half of the tail current

$$I_E = I_T/2 = 30\mu\text{A}$$

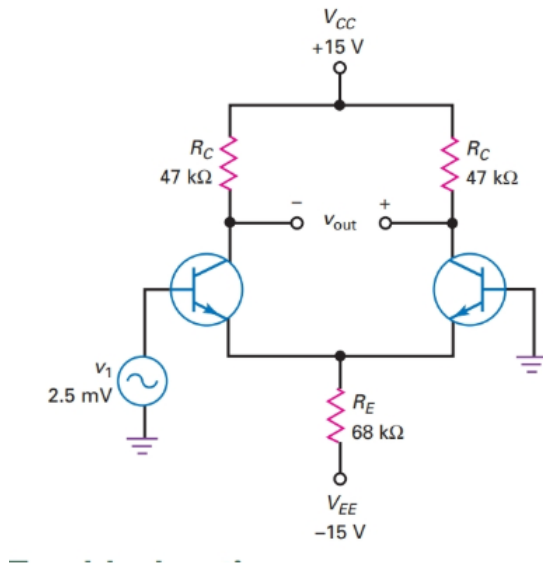
For ideal voltage

$$V_C = V_{CC} - I_C * R_C = 12\text{V} - 30\mu\text{A} * 200\text{k}\Omega = 6\text{V}$$

V_C on the right transistor is 6V

V_C on the left transistor is 12V since R_{C1} is 0 on the left side

3 In Fig. 15-35, what is the ac output voltage? If $\beta = 275$, what is the input impedance of the diff amp? Use the ideal approximation to get the tail current.



This is non inverting input and differential output

Tail current

$$I_T = V_{EE}/R_E = 15V/68k\Omega = 220\mu A$$

Each emitter current is half of the tail current

$$I_E = I_T/2 = 110\mu A$$

$$r'_e = 25mV/110\mu A = 227\Omega$$

The voltage gain of differential output

$$A_v = R_C/r'_e = 47k\Omega/227\Omega = 206.8$$

The ac output voltage is

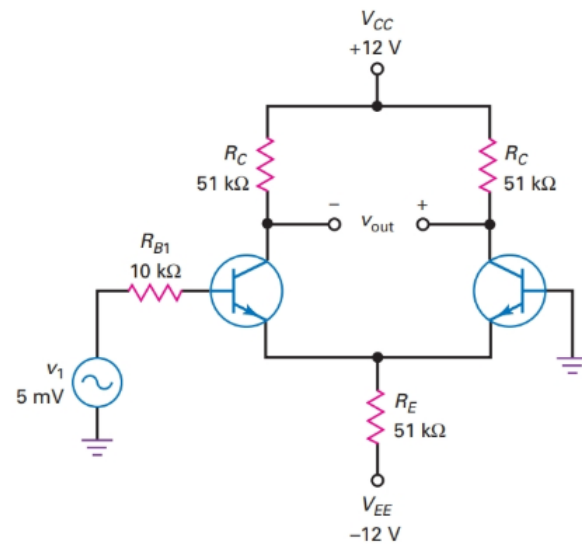
$$V_{out} = A_v(V_{in}) = 206.8 * 2.5mV = 518mV$$

In differential amplifier, the input impedance is twice as high

$$Z_{in(base)} = 2 * \beta$$

$$V_C = V_{CC} - I_C * R_C = 15V - 27.8\mu\Omega * 180 k\Omega = 10V$$

4 The differential amplifier of Fig. 15-36 has $A_v = 360$, $I_{in(bias)} = 600$ nA, $I_{in(off)} = 100$ nA, and $V_{in(off)} = 1$ mV. What is the output error voltage? If a matching base resistor is used, what is the output error voltage?



Known

Non inverting input, Differential output

$$A_v = 360$$

$$(I_{in(bias)}) = (I_{b1} + I_{b2})/2$$

$$I_{in(bias)} = 600 \text{ nA}$$

$$I_{in(off)} = (I_{b1} - I_{b2})$$

$$I_{in(off)} = 100 \text{ nA}$$

$$V_{in(off)} = V_{Error}/A_v$$

$$V_{in(off)} = 1 \text{ mV}$$

Drived formulas

$$V_{1err} = (R_{b1} - R_{b2}) * I_{in(bias)}$$

$$V_{2err} = [(R_{b1} + R_{b2}) * I_{in(off)}]/2$$

$$V_{3err} = V_{in(off)}$$

$$V_{err} = A_v (V_{err} + V_{2err} + V_{3err})$$

$$R_{b1} = 10k\Omega$$

$$R_{b2} = 0k\Omega$$

Three unwanted dc error inputs

$$V_{1err} = (R_{b1} - R_{b2}) * I_{in(bias)}$$

$$= (10k\Omega) * 600 \text{ nA} = 6mV$$

$$V_{2err} = [(R_{b1} + R_{b2}) * I_{in(off)}]/2$$

$$= (10k\Omega * 100 \text{ nA})/2 = 0.5mV$$

$$V_{3err} = V_{in(off)} = 1mV$$

The output error voltage

$$V_{err} = A_v (V_{err} + V_{2err} + V_{3err}) = 360(6\text{mV} + 0.5\text{mV} + 1\text{mV}) = 2.7\text{V}$$

The output error voltage is 2.7V

When matching base resistance is 10k is used on the inverting side

$$V_{1err} = 0$$

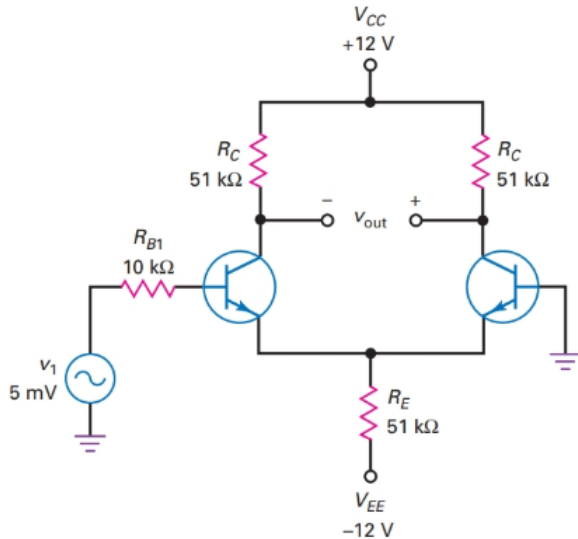
$$V_{2err} = R_B * I_{in(off)} = 10\text{k}\Omega * 100\text{nA} = 1\text{mV}$$

$$V_{3err} = V_{in(off)} = 1\text{mV}$$

$$V_{err} = A_v (V_{1err} + V_{2err} + V_{3err}) = 360(2\text{mV}) = 720\text{mV}$$

Error output voltage is 720mV when matching resistor is used on the based side

5 The diff amp of Fig. 15-36 has $A_v = 250$, $I_{in(bias)} = 1 \mu\text{A}$, $I_{in(off)} = 200 \text{ nA}$, and $V_{in(off)} = 5 \text{ mV}$. What is the output error voltage? If a matching base resistor is used, what is the output error voltage?



Known

non inverting input, differential output

$$A_v = 250$$

$$I_{in(bias)} = (I_{b1} + I_{b2})/2$$

$$I_{in(bias)} = 1 \mu\text{A}$$

$$I_{in(off)} = (I_{b1} - I_{b2})$$

$$I_{in(off)} = 200 \text{ nA}$$

$$V_{in(off)} = V_{Error}/A_v$$

$$V_{in(off)} = 1 \text{ mV}$$

Drived formulas

$$V_{1err} = (R_{b1} - R_{b2}) * I_{in(bias)}$$

$$V_{2err} = [(R_{b1} + R_{b2}) * I_{in(off)}]/2$$

$$V_{3err} = V_{in(off)}$$

$$V_{err} = A_v (V_{err} + V_{2err} + V_{3err})$$

$$R_{b1} = 10\text{k}\Omega$$

$$R_{b2} = 0\text{k}\Omega$$

Dc error inputs

$$V_{1err} = (R_{b1} - R_{b2}) * I_{in(bias)}$$

$$= (10\text{k}\Omega) * 1 \mu\text{A} = 10\text{mV}$$

$$V_{2err} = [(R_{b1} + R_{b2}) * I_{in(off)}]/2$$

$$= [(10\text{k}\Omega) * 200 \text{ nA}]/2 = 1\text{mV}$$

$$V_{3err} = V_{in(off)} = 5\text{mV}$$

The output error voltage

$$V_{err} = A_v (V_{err} + V_{2err} + V_{3err}) = 250(10\text{mV} + 1\text{mV} + 5\text{mV}) = 4\text{V}$$

The output error voltage is 4V

When matching base resistance is 10k is used on the inverting side

$$V_{1err} = 0$$

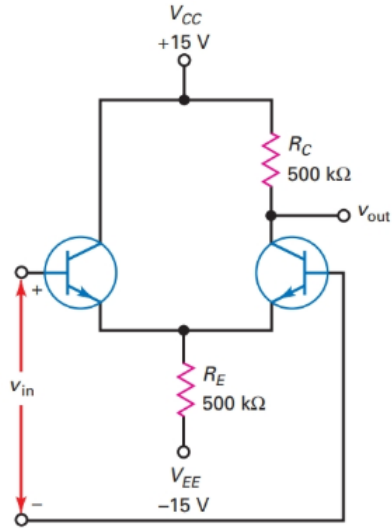
$$V_{2err} = R_B * I_{in(off)} = 10\text{k}\Omega * 200\text{nA} = 2\text{mV}$$

$$V_{3err} = V_{in(off)} = 5\text{mV}$$

$$V_{err} = A_v (V_{1err} + V_{2err} + V_{3err}) = 250(2\text{mV}) = 1.75\text{V}$$

Output error voltage 1.75V when matching resistor is used on the based side.

6 What is the common-mode voltage gain of Fig. 15-37? If a common-mode voltage of $20 \mu\text{V}$ exists on both bases, what is the common-mode output voltage?



The common mode gain

$$A_{V(cm)} = R_C / (2 * R_E) = 500\text{k}\Omega / (2 * 500\text{k}\Omega) = 0.5$$

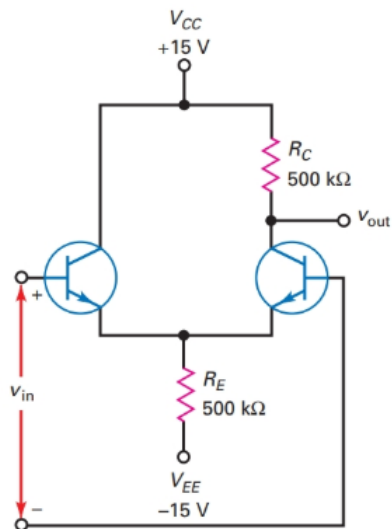
The output voltage is

$$V_{out} = A_{V(cm)} * V_{in(cm)} = 0.5 * 20\mu\text{V} = 10\mu\text{V}$$

The common mode output voltage

$$V_{out} = 10\mu\text{V}$$

7n Fig. 15-37, $v_{in} = 2 \text{ mV}$ and $v_{in(cm)} = 5 \text{ mV}$. What is the ac output voltage?



$$V_{in} = 2\text{mV}$$

$$V_{in(cm)} = 5\text{mV}$$

$$I_T = (15\text{V} - 0.7\text{V}) / (500\text{k}\Omega) = 28.6 \mu\text{A}$$

$$I_E = I_T / 2 = 14.3 \mu\text{A}$$

$$r'_e = 25\text{mV} / 14.3\mu\text{A} = 1748\Omega$$

$$A_v = r_C / (2 * r'_e) = 500\text{k}\Omega / (2 * 1748\Omega) = 143$$

$$V_{out} = A_v * V_{in} = 143 * 2\text{mV} = 286\text{mV}$$

Hence the output voltage is 286mV.

Common mode voltage gain

$$A_{V(cm)} = R_C / (2 * R_E) = 500\text{k}\Omega / (2 * 500\text{k}\Omega) = 0.5$$

The output voltage is

$$V_{out} = A_{V(cm)} * V_{in(cm)} = 0.5 * 5\text{mV} = 2.5 \text{ mV}$$

8 A 741C is an op amp with $A_v = 100,000$ and a minimum $CMRR_{dB} = 70 \text{ dB}$. What is the common-mode voltage gain? If a desired and common-mode signal each has a value of 5 V , what is the output voltage?

$$CMRR = A_v / A_{v(cm)}$$

$$A_v = 250$$

$$CMRR_{dB} = 70\text{dB}$$

$$V_{in} = 5\mu\text{V}$$

$$V_{in(cm)} = 5\mu\text{V}$$

$$CMRR = 10^{CMRR_{dB}/20} = 10^{70/20} = 3162.3$$

$$A_{v(cm)} = A_v / CMRR = 100000 / 3162.3 = 31.62$$

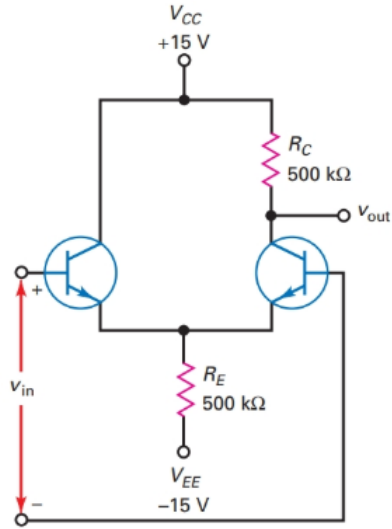
The desired output component

$$V_{out1} = A_v * V_{in} = 100000(5\mu\text{V}) = 0.5\text{V}$$

Common mode output

$$V_{out2} = A_{V(cm)} V_{in(cm)} = (31.62\text{V}) (5 \mu\text{V}) = 0.158\text{mV}$$

9 If the supply voltages are reduced to 110 V and 210 V, what is the common-mode rejection ratio of Fig. 15-37? Express the answer in decibels.



$$I_T = (10\text{V} - 0.7\text{V})/(500\text{k}\Omega) = 18.6 \mu\text{A}$$

$$I_E = I_T/2 = 9.3 \mu\text{A}$$

$$r'_e = 25\text{mV}/9.3\mu\text{A} = 2688\Omega$$

Since this amplifier is single ended output the formula is $A_v = r_c/(2*r'_e)$

$$A_v = r_c/(2*r'_e) = 500\text{k}\Omega/(2*2688\Omega) = 93$$

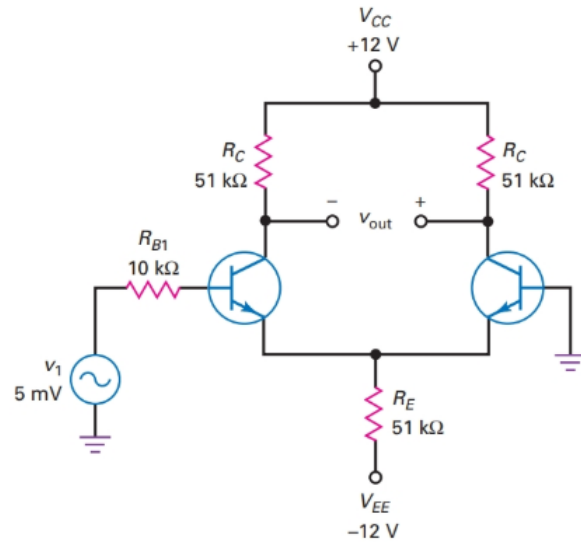
And the common mode voltage gain is

$$A_{v(CM)} = r_c/(2*r'_e) = 500\text{k}\Omega/(2*500\text{k}\Omega) = 0.5$$

$$\text{CMRR} = A_v/A_{v(CM)} = 93/0.5 = 186$$

$$\text{CMRR}_{db} = 20*\log(\text{CMRR}) = 20\log(186) = 45.4\text{dB}$$

10 A load resistance of 27 kΩ is connected across the differential output of Fig. 15-36. What is the load voltage?



$$I_T = (12\text{V} - 0.7\text{V})/(51\text{k}\Omega) = 0.2216 \text{ mA}$$

$$I_E = I_T/2 = 0.1108 \text{ mA}$$

$$r'_e = 25\text{mV}/0.1108 \text{ mA} = 225.63\Omega$$

Since it is differential output the formula is $A_v = r_c/r'_e$

$$A_v = r_c/(r'_e) = 51\text{k}\Omega/(225.63\Omega) = 226$$

$$v_{out} = A_v * v_1 = 226 * 5\text{mV} = 1.13\text{V}$$

$$\text{ac output voltage} = 1.13\text{V}$$

$$R_{th} = 2*R_C = 2*51\text{k}\Omega = 102\text{k}\Omega$$

$$V_L = V_{out}*(R_L)/(R_{th}+R_L) = 1.13\text{V} * (27\text{k}\Omega)/(102\text{k}\Omega+27\text{k}\Omega) = 237\text{mV}$$

Hence, the load voltage is 237mV