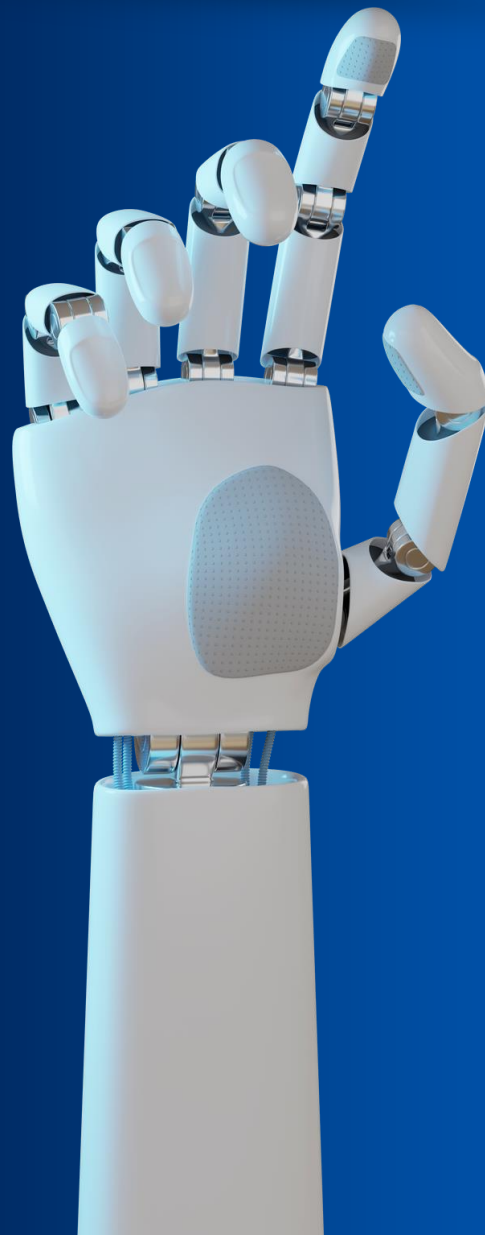# Compare Features

Quartus® Prime
Design Software

Pro

Design Flow
- Platform Designer
- Block Based Design
- Partial Reconfiguration

Design Entry/Planning
- Design Partition Planner
- Chip Planner
- Interface Planner
- Logic Lock Regions
- Multiprocessor Support (Faster Compile Time)
- IP Base Suite

Placement and Routing
- Fitter (Place and Route)
- Register Retiming

Timing and Power Verification
- Timing Analyzer
- Design Space Explorer II
- Power Analysis

Simulation and Debug
- Signal Tap Logic Analyzer
- Transceiver Toolkit
- Questa*-Intel® FPGA Edition Software

Standard

Design Flow
- Platform Designer
- Partial Reconfiguration

Design Entry/Planning
- Design Partition Planner
- Chip Planner
- Logic Lock Regions
- Multiprocessor Support (Faster Compile Time)
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Placement and Routing
- Fitter (Place and Route)
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Timing and Power Verification
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Lite (Free)

Design Flow
- Platform Designer

Design Entry/Planning
- Chip Planner
- IP Base Suite [i]

Placement and Routing
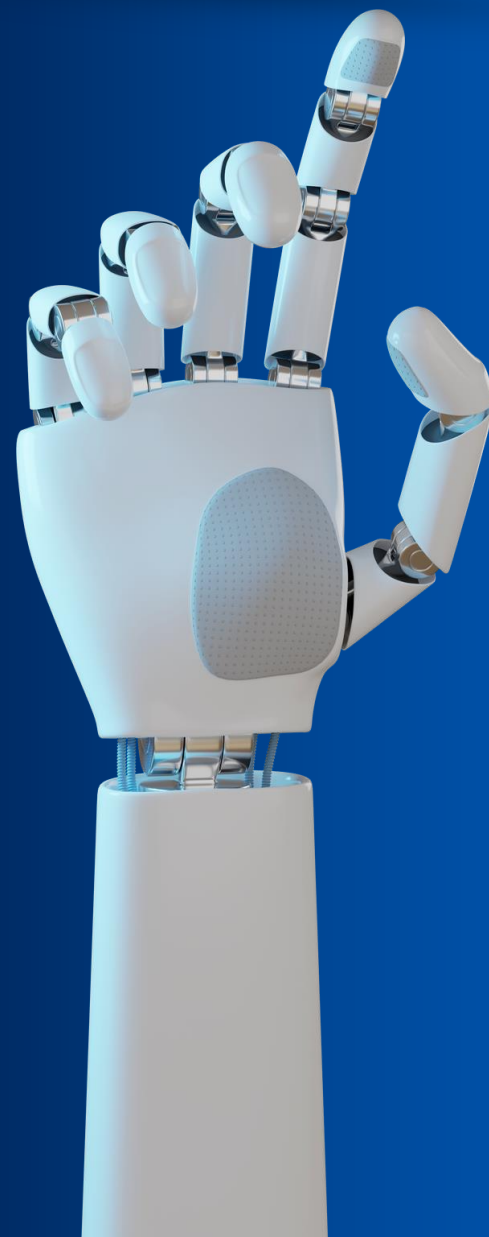- Fitter (Place and Route)

Timing and Power Verification
- Timing Analyzer
- Design Space Explorer II
- Power Analysis

Simulation and Debug
- Signal Tap Logic Analyzer
- Questa*-Intel® FPGA Edition Software

i = Available for Purchase

Compare Editions

Quartus® Prime
Design Software

Pro

Devices Supported:
- Agilex™ FPGA Portfolio
- Stratix® 10 devices
- Arria® 10 devices
- Cyclone® 10 GX devices

Cyclone® 10 GX devices are supported for free in Quartus® Prime Pro Edition Software.

Simulation Tools
- Questa*-Intel® FPGA Edition [1]
- Questa*-Intel® FPGA Starter Edition

Add-On
- Intel® FPGA Power and Thermal Calculator
- FPGA AI Suite

Additional Development Tools
- Intel® Advanced Link Analyzer Tool
- Intel® HLS compiler
- DSP Builder for Intel® FPGAs
- Intel® SoC FPGA Embedded Development Suite

Embedded Tools
- Nios® V Processor
- Arm* Development Studio for Intel® SoC FPGA
- Ashling* RiscFree* IDE for Intel® FPGAs
- Intel® Simics® Simulator for Intel® FPGAs

Standard

Devices Supported:
- Arria® 10 devices
- Arria® V FPGAs, Arria® V GZ FPGA
- Arria® II FPGAs
- MAX® 10 devices
- MAX® V CPLDs, MAX® II CPLDs
- Cyclone® 10 LP devices
- Cyclone® V devices, Cyclone® IV FPGAs
- Stratix® V FPGAs, Stratix® IV FPGAs

The Quartus® Prime Standard Edition software includes extensive support for earlier device families.

Simulation Tools
- Questa*-Intel® FPGA Edition [1]
- Questa*-Intel® FPGA Starter Edition

Additional Development Tools
- Intel® Advanced Link Analyzer Tool
- Intel® SoC FPGA Embedded Development Suite

Embedded Tools
- Nios® V Processor
- Arm* Development Studio for Intel® SoC FPGA
- Ashling* RiscFree* IDE for Intel® FPGAs

Lite (Free)

Devices Supported:
- Cyclone® 10 LP devices
- Cyclone® V devices, Cyclone® IV FPGAs,
- Arria® II FPGAs [2]
- MAX® 10 devices
- MAX® V CPLDs, MAX® II CPLDs

Simulation Tools
- Questa*-Intel® FPGA Starter Edition

Additional Development Tools
- Intel® SoC FPGA Embedded Development Suite

Embedded Tools
- Nios® V Processor
- Ashling* RiscFree* IDE for Intel® FPGAs

1. License for Questa*-Intel® FPGA Edition comes free with a paid license of Quartus® Prime Software
2. The only Arria II FPGA supported is the EP2AGX45 device.