

# AI-Designed Chips

## For AI by AI

- Chip floorplanning: designing physical layout of a computer chip.
- Despite decades of research, chip floorplanning has defied automation, **requiring months of intense effort** by physical design engineers
- Deep reinforcement learning approach to chip floorplanning
- **In under six hours**, automatically generates chip floorplans superior or comparable to those produced by humans in all key metrics, including power consumption, performance and chip area.

# Feedback Loops

## When AI Helps Improve Itself

“bringing us closer to a future in which chip designers are assisted by artificial agents with more experience than any human could ever gain”

“we believe that more powerful AI-designed hardware will fuel advances in AI, creating a symbiotic relationship”