

# Unificating Response Time Analysis Framework for Self-Suspending Tasks

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*Abstract—*

## 1 Introduction

The periodic/sporadic task model has been recognized as the basic model for real-time systems with recurring executions. A sporadic real-time task  $\tau_i$  is characterized by its *worst-case execution time*  $C_i$ , its *minimum inter-arrival time*  $T_i$  and its *relative deadline*  $D_i$ . A sporadic task defines an infinite sequence of task instances, also called *jobs*, that arrive with the minimum inter-arrival time constraint. When a job of task  $\tau_i$  arrives at time  $t$ , the job should finish no later than its *absolute deadline*  $t + D_i$ , and the next job of task  $\tau_i$  can only be released no earlier than  $t + T_i$ . For the periodic task model, the next job is released at time  $t + T_i$ , in which  $T_i$  is also referred to as the *period* of task  $\tau_i$ .

The seminal work by Liu and Layland [14] considered the scheduling of periodic tasks and presented the schedulability analyses based on utilization bounds to verify whether the deadlines are met or not. For over decades, researchers in real-time systems have devoted themselves to effective design and efficient analyses of different recurrent task models to ensure that tasks can meet their specified deadlines. In most of these studies, *a task usually does not suspend itself*. That is, after a job is released, the job is either executed or stays in the ready queue, but it is not moved to the suspension state. Such an assumption is valid only under the following conditions: (1) the latency of the memory accesses and I/O peripherals is considered to be part of the worst-case execution time of a job, (2) there is no external device for accelerating the computation, and (3) there is no synchronization between different tasks on different processors in a multiprocessor or distributed computing platform.

If a job can suspend itself before it finishes its computation, self-suspension behaviour has to be considered. Due to the interaction with other system components and synchronization, self-suspension behaviour has become more visible in designing real-time embedded systems. Typically, the resulting suspension delays range from a few microseconds (e.g., a write operation on a flash drive [9]) to a few hundreds of milliseconds (e.g., offloading computation to GPUs [10], [16]).

There are two typical models for self-suspending sporadic task systems: 1) the dynamic self-suspension task model, and 2) the segmented self-suspension task model. In the *dynamic* self-suspension task model, in addition the worst-case execution time  $C_i$  of sporadic task  $\tau_i$ , we have also the

worst-case self-suspension time  $S_i$  of task  $\tau_i$ . In the *segmented* self-suspension task model, the execution behaviour of a job of task  $\tau_i$  is specified by interleaved computation segments and self-suspension intervals. From the system designer's perspective, the dynamic self-suspension model provides a simple specification by ignoring the juncture of I/O access, computation offloading, or synchronization. However, if the suspending behaviour can be characterized by using a segmented pattern, the segmented self-suspension task model can be more appropriate.

In this paper, we focus on preemptive fixed-priority scheduling for the dynamic self-suspension task model on a uniprocessor platform. To verify the schedulability of a given task set, this problem has been specifically studied in [1], [2], [8], [12], [17]. The recent report by Chen et al. and the report by Bletsas et al. [3] have shown that the analysis by introducing the suspension time of a higher-priority task as its arrival jitter in [1], [2], [12], [17] is unsafe. This misconception was unfortunately adopted in [4], [5], [7], [11], [13], [20]–[22] to analyze the worst-case response time for partitioned multiprocessor real-time locking protocols.

Moreover, one concept to consider suspension-time as blocking time was used by Jane W. S. Liu in her book titled "Real-Time Systems" [15, Pages 164-165], and was also implicitly used by Rajkumar, Sha, and Lehoczky [19, Page 267] for analyzing the self-suspending behaviour due to synchronization protocols in multiprocessor systems. However, there is no proof in [15], [19] to support the correctness of the provided schedulability tests.

The contributions of this paper are as follows:

- We provide a general analysis framework in Theorem 1 for dynamic self-suspending sporadic real-time tasks on a uniprocessor platform. This theorem analytically dominates all the existing results in [3], [8] and [15, Pages 164-165], excluding the flawed ones. The key observation in the analysis framework is that the *interference from higher-priority self-suspending tasks can be arbitrarily modelled as jitter or carry-in terms*. Moreover, the proof of Theorem 1 also supports the correctness of the analysis in [15, Pages 164-165] and [19, Page 267].<sup>1</sup>
- We develop a few strategies to decide which higher-priority tasks should be classified to associate with jitter

<sup>1</sup> A simplified version of the proof of Theorem 1 to support the correctness of [15, Pages 164-165] and [19, Page 267] is provided in [6].

terms and which higher-priority tasks should be classified to associate with carry-in terms. The methods are presented in Section ??.

- utilization bounds..
- evaluation results...

## 2 Task Model

We assume a system  $\tau$  composed of  $n$  sporadic self-suspending tasks. A sporadic task  $\tau_i$  is released repeatedly, with each such invocation called a job. The  $j^{th}$  job of  $\tau_i$ , denoted by  $\tau_{i,j}$ , is released at time  $r_{i,j}$  and has an absolute deadline at time  $d_{i,j}$ . Each job of task  $\tau_i$  is assumed to have a worst-case execution time  $C_i$ . Furthermore, a job of task  $\tau_i$  may suspend itself for at most  $S_i$  time units (across all of its suspension phases). When a job suspends itself, it releases the processor and another job can be executed. The response time of a job is defined as its finishing time minus its release time. Successive jobs of the same task are required to execute in sequence.

Associated with each task  $\tau_i$  are a period (or minimum inter-arrival time)  $T_i$ , which specifies the minimum time between two consecutive job releases of  $\tau_i$ , and a relative deadline  $D_i$ , which specifies the maximum amount of time a job can take to complete its execution after its release. It results that for each job  $\tau_{i,j}$ , there is  $d_{i,j} = r_{i,j} + D_i$  and  $r_{i,j+1} \geq r_{i,j} + T_i$ . In this paper, we focus on constrained-deadline tasks, for which  $D_i \leq T_i$ . The utilization of a task  $\tau_i$  is defined as  $U_i = C_i/T_i$ .

The worst-case response time  $R_i$  of a task  $\tau_i$  is the maximum response time among all its jobs. A schedulability test for a task  $\tau_k$  is therefore to verify whether its worst-case response time is no more than its associated relative deadline  $D_k$ .

In this paper, we only consider *preemptive fixed-priority scheduling running on a single processor platform*, in which each task is assigned with a unique priority level. We assume that the priority assignment is given beforehand and that the tasks are numbered in a decreasing priority order. That is, a task with a smaller index has a higher priority than any task with a higher index, i.e., task  $\tau_i$  has a higher-priority than task  $\tau_j$  if  $i < j$ .

When performing the schedulability analysis of a specific task  $\tau_k$ , we will implicitly assume that all the higher priority tasks (i.e.,  $\tau_1, \tau_2, \dots, \tau_{k-1}$ ) are already verified to meet their deadlines, i.e., that  $R_i \leq D_i, \forall \tau_i \mid 1 \leq i \leq k-1$ .

## 3 Background

To analyze the worst-case response time (or the schedulability) of a task  $\tau_k$ , one usually needs to quantify the worst-case interference exerted by the higher-priority tasks on the execution of any job of task  $\tau_k$ . In the ordinary sequential sporadic real-time task model, i.e., when  $S_i = 0$  for every task  $\tau_i$ , the so-called critical instant theorem by Liu and Layland [14] is commonly adopted. That is, the worst-case response time of task  $\tau_k$  (if it is less than or equal to its period) happens for the first job of task  $\tau_k$  when (i)  $\tau_k$  and all the higher-priority tasks release their first job synchronously and (ii) all their subsequent jobs are released as early as possible (i.e.,

with a rate equal to their period). However, this definition of the critical instant does not hold for self-suspending sporadic tasks.

There exist three different approaches in the state-of-the-art that are potentially sound to perform the schedulability analysis of self-suspending tasks:

- modeling the suspension as execution, also known as the suspension-oblivious analysis (see Section 3.1);
- modeling the suspension as a release jitter (see Section 3.2);
- modeling the suspension as blocking time (see Section 3.3).

We later prove in Section 5 that all these approaches are analytically correct.

### 3.1 Suspension-Oblivious Analysis

The simplest analysis consists in converting the suspension time  $S_i$  of each task  $\tau_i$  as a part of its computation time. Therefore, a constrained-deadline task  $\tau_k$  can be feasibly scheduled by a fixed-priority scheduling algorithm if

$$\exists t \mid 0 < t \leq D_k, \quad C_k + S_k + \sum_{i=1}^{k-1} \left\lceil \frac{t}{T_i} \right\rceil (C_i + S_i) \leq t. \quad (1)$$

### 3.2 Modeling the Suspension as a Release Jitter

Another approach consists in modeling the impact of the self-suspension  $S_i$  of each higher priority task  $\tau_i$  as a release jitter  $J_i$ . Several works in the state-of-the-art [1], [2], [12], [17] upper bounded  $J_i$  with  $S_i$ . However, it has been recently shown in [3] that this upper bound is unsafe and  $J_i$  can in fact be larger than  $S_i$ .

Nevertheless, it was proven in the same document [3] that the jitter of a higher-priority task  $\tau_i$  can be safely upper bounded by  $R_i - C_i$ . It results that a task  $\tau_k$  with a constrained deadline can be feasibly scheduled under fixed-priority if

$$\exists t \mid 0 < t \leq D_k, \quad C_k + S_k + \sum_{i=1}^{k-1} \left\lceil \frac{t + R_i - C_i}{T_i} \right\rceil C_i \leq t. \quad (2)$$

### 3.3 Modeling the Suspension as Blocking Time

In [15, p. 164-165], Liu proposed a solution to study the schedulability of a self-suspending task  $\tau_k$  by modeling the extra delay suffered by  $\tau_k$  due to the self-suspension behavior of each task in  $\tau$  as a blocking time. This blocking time has been defined as follows:

- The blocking time contributed from task  $\tau_k$  is  $S_k$ .
- A higher-priority task  $\tau_i$  can block the execution of task  $\tau_k$  for at most  $\min(C_i, S_i)$  time units.

An upper bound on the blocking time is therefore given by:

$$B_k = S_k + \sum_{i=1}^{k-1} \min(C_i, S_i). \quad (3)$$

In [15], the blocking time is then used to derive a utilization-based schedulability test for rate-monotonic

scheduling. Namely, it is stated that, if  $T_i = D_i$  for every task  $\tau_i \in \tau$  and  $\frac{C_k + B_k}{T_k} + \sum_{i=1}^{k-1} U_i \leq k(2^{\frac{1}{k}} - 1)$ , then  $\tau_k$  can be feasibly scheduled with rate-monotonic.

The same concept was also implicitly used by Rajkumar, Sha, and Lehoczky in [19, p. 267] for analyzing the impact of the self-suspension of a task due to the utilization of synchronization protocols in multiprocessor systems. The statement in [19] reads as follows:<sup>2</sup>

*“For each higher priority job  $\tau_{i,j}$  that suspends on global semaphores or for other reasons, add the term  $\min(C_i, S_i)$  to  $B_k$ , where  $S_i$  is the maximum duration that  $\tau_{i,j}$  can suspend itself. [...] The sum [...] yields  $B_k$ , which in turn can be used in  $\frac{C_k + B_k}{T_k} + \sum_{i=1}^{k-1} U_i \leq k(2^{\frac{1}{k}} - 1)$  to determine whether the current task allocation to the processor is schedulable.”*

If the above argument is correct, we can further prove that a constrained-deadline task  $\tau_k$  can be feasibly scheduled under fixed-priority scheduling if

$$\exists t \mid 0 < t \leq D_k, \quad C_k + B_k + \sum_{i=1}^{k-1} \left\lceil \frac{t}{T_i} \right\rceil C_i \leq t. \quad (4)$$

However, there is no proof in [15] nor in [19] to support the correctness of those tests. Therefore, in Section 5, we provide a proof of the correctness of Equation (4).

## 4 Rationale

## 5 A Unificating Analysis Framework

### 5.1 Modeling the the Task Under Analysis

### 5.2 A new Response time Analysis

We can greedily convert the suspension time of task  $\tau_k$  to its computation time. For the sake of notational brevity, let  $C'_k$  be  $C_k + S_k$ . We call this converted version of task  $\tau_k$  as task  $\tau'_k$ . Suppose that  $R'_k$  is the worst-case response time in the task system  $\{\tau_1, \tau_2, \dots, \tau_{k-1}, \tau'_k\}$ . It was already shown in the previous works, e.g., Lemma 3 in [16] and Theorem 2 in [18], that  $R'_k$  is a safe upper bound on the worst-case response time of task  $\tau_k$  in the original task system.

Note that for the rest of this section we implicitly assume that  $R_i \leq D_i, \forall \tau_i \mid 1 \leq i \leq k-1$ . Our key result in this paper is the following theorem:

**Theorem 1.** *Suppose that  $R'_k \leq T_k$ . For any arbitrary vector assignment  $\vec{x} = (x_1, x_2, \dots, x_{k-1})$ , in which  $x_i$  is either 0 or 1, the worst-case response time  $R'_k$  is upper bounded by the minimum  $t$  (with  $t > 0$ ) that satisfies*

$$C'_k + \sum_{i=1}^{k-1} \left\lceil \frac{t + Q_i^{\vec{x}} + (1 - x_i)(D_i - C_i)}{T_i} \right\rceil C_i \leq t, \quad (5)$$

where  $Q_i^{\vec{x}}$  is  $\sum_{j=i}^{k-1} S_j \cdot x_j$ .

<sup>2</sup>We rephrased the wording and notations in order to be consistent with the rest of this paper.

We will explain the resulting properties from Theorem 1 first, by leaving the proof to Section 5.4 since it is pretty long. With Theorem 1, we can directly have the following corollary.

**Corollary 1.** *If there exists a vector assignment  $\vec{x} = (x_1, x_2, \dots, x_{k-1})$ , in which  $x_i$  is either 0 or 1, such that*

$$\exists t \mid 0 < t \leq D_k, C'_k + \sum_{i=1}^{k-1} \left\lceil \frac{t + Q_i^{\vec{x}} + (1 - x_i)(D_i - C_i)}{T_i} \right\rceil C_i \leq t, \quad (6)$$

where  $Q_i^{\vec{x}}$  is  $\sum_{j=i}^{k-1} S_j \cdot x_j$ , then a constrained-deadline task  $\tau_k$  can be feasibly scheduled by the fixed-priority scheduling.

We will show later that Corollary 1 in fact dominates all the analyses discussed in Section 3.

**Example 1.** *We use an example to demonstrate how Corollary 1 can be applied. Suppose that we have three tasks*

- $C_1 = 4, S_1 = 5, T_1 = D_1 = 10$ ,
- $C_2 = 6, S_2 = 1, T_2 = D_2 = 19$ , and
- $C_3 = 4, S_3 = 0, T_3 = D_3 = 35$ .

Tasks  $\tau_1$  and  $\tau_2$  can be verified to be schedulable under the fixed-priority scheduling by using Eq. (2).

We focus on task  $\tau_3$ . For task  $\tau_3$ , the blocking term  $B_3$  is  $4 + 1 = 5$  by Eq. (3). The minimum  $t$  to satisfy  $C_k + B_k + \sum_{i=1}^{k-1} \left\lceil \frac{t}{T_i} \right\rceil C_i \leq t$  happens when  $t = 37$ , i.e.,  $4 + 5 + \left\lceil \frac{37}{10} \right\rceil \cdot 4 + \left\lceil \frac{37}{19} \right\rceil \cdot 6 = 37$ . Therefore, task  $\tau_3$  cannot pass the schedulability test in Eq. (4). There are four possible vector assignments  $\vec{x}$  when we consider the schedulability of task  $\tau_3$ :

- *Case 1  $\vec{x} = (0, 0)$ : In this case, Theorem 1 states that  $R'_k$  is upper bounded by the minimum  $t$  under  $0 < t \leq T_3$  that satisfies*

$$4 + \left\lceil \frac{t+6}{10} \right\rceil \cdot 4 + \left\lceil \frac{t+13}{19} \right\rceil \cdot 6 \leq t. \quad (7)$$

*Such a value  $t$  does not exist for this case.*

- *Case 2  $\vec{x} = (0, 1)$ : In this case, Theorem 1 states that  $R'_k$  is upper bounded by the minimum  $t$  under  $0 < t \leq T_3$  that satisfies*

$$4 + \left\lceil \frac{t+7}{10} \right\rceil \cdot 4 + \left\lceil \frac{t+1}{19} \right\rceil \cdot 6 \leq t. \quad (8)$$

*Therefore,  $R'_k \leq 32$  due to  $4 + \left\lceil \frac{32+7}{10} \right\rceil \cdot 4 + \left\lceil \frac{32+1}{19} \right\rceil \cdot 6 = 32$ .*

- *Case 3  $\vec{x} = (1, 0)$ : In this case, Theorem 1 states that  $R'_k$  is upper bounded by the minimum  $t$  under  $0 < t \leq T_3$  that satisfies*

$$4 + \left\lceil \frac{t+5}{10} \right\rceil \cdot 4 + \left\lceil \frac{t+13}{19} \right\rceil \cdot 6 \leq t. \quad (9)$$

*Such a value  $t$  does not exist for this case.*

- *Case 4  $\vec{x} = (1, 1)$ : In this case, Theorem 1 states that  $R'_k$  is upper bounded by the minimum  $t$  under  $0 < t \leq T_3$  that satisfies*

$$4 + \left\lceil \frac{t+6}{10} \right\rceil \cdot 4 + \left\lceil \frac{t+1}{19} \right\rceil \cdot 6 \leq t. \quad (10)$$

*Therefore,  $R'_k \leq 32$  due to  $4 + \left\lceil \frac{32+6}{10} \right\rceil \cdot 4 + \left\lceil \frac{32+1}{19} \right\rceil \cdot 6 = 32$ .*

Among the above four cases, the tests in Cases 2 and 4 are tighter. By Corollary 1, task  $\tau_3$  is schedulable by the fixed-priority scheduling policy.

### 5.3 Dominance

In this section, we prove that the schedulability test presented in Corollary 1 dominates all the existing tests in the state-of-the-art, in the sense that if a task set is deemed schedulable by either of the tests presented in Section 3, then it is also deemed schedulable by Corollary 1.

**Lemma 1.** *The schedulability test of task  $\tau_k$  provided by Eq. (4) dominates that of Eq. (1).*

*Proof:* It is straightforward to see that

$$\begin{aligned} & C_k + S_k + \sum_{i=1}^{k-1} \left\lceil \frac{t}{T_i} \right\rceil (C_i + S_i) \\ & \geq C_k + S_k + \sum_{i=1}^{k-1} S_i + \sum_{i=1}^{k-1} \left\lceil \frac{t}{T_i} \right\rceil C_i \\ & \geq C_k + S_k + \sum_{i=1}^{k-1} \min(C_i, S_i) + \sum_{i=1}^{k-1} \left\lceil \frac{t}{T_i} \right\rceil C_i \end{aligned}$$

and by using the definition of  $B_k$  (i.e., Equation (3)), we get

$$C_k + S_k + \sum_{i=1}^{k-1} \left\lceil \frac{t}{T_i} \right\rceil (C_i + S_i) \geq C_k + B_k + \sum_{i=1}^{k-1} \left\lceil \frac{t}{T_i} \right\rceil C_i$$

Therefore, Eq. (4) will always have a solution which is smaller than or equal to the solution of Eq. (1). This proves the lemma. ■

**Lemma 2.** *The schedulability test presented in Corollary 1 dominates the schedulability test provided by Eq. (2).*

*Proof:* Consider the case where  $x_1 = x_2 = \dots = x_{k-1} = 0$ . Eq. (6) becomes identical to Eq. (2) for this particular vector assignment. Therefore, if Eq. (2) deems a task set as being schedulable, so does Corollary 1. This proves the lemma. ■

**Lemma 3.** *The schedulability test presented in Corollary 1 dominates the schedulability test provided by Eq. (4).*

*Proof:* In this proof, we first transform the worst-case response time analysis presented in Corollary 1 in a more pessimistic analysis. We then prove that this more pessimistic version of Corollary 1 provides the same solution than Eq. (4), which will then prove the lemma.

Since  $Q_i^{\vec{x}} \stackrel{\text{def}}{=} \sum_{j=i}^{k-1} S_j \times x_j$ , it holds that  $Q_i^{\vec{x}} \leq Q_1^{\vec{x}}$  for

$i = 1, 2, \dots, k-1$ . It follows that

$$\begin{aligned} & C_k + S_k + \sum_{i=1}^{k-1} \left\lceil \frac{t + Q_i^{\vec{x}} + (1 - x_i)(R_i - C_i)}{T_i} \right\rceil \\ & \stackrel{(Q_i^{\vec{x}} \leq Q_1^{\vec{x}})}{\leq} C_k + S_k + \sum_{i=1}^{k-1} \left\lceil \frac{t + Q_1^{\vec{x}} + (1 - x_i)(R_i - C_i)}{T_i} \right\rceil \\ & \stackrel{(R_i \leq D_i \leq T_i)}{\leq} C_k + S_k + \sum_{i=1}^{k-1} \left\lceil \frac{t + Q_1^{\vec{x}} + (1 - x_i)T_i}{T_i} \right\rceil \\ & \stackrel{(x_i \in \{0,1\})}{\leq} C_k + S_k + \sum_{i=1}^{k-1} (1 - x_i)C_i + \sum_{i=1}^{k-1} \left\lceil \frac{t + Q_1^{\vec{x}}}{T_i} \right\rceil \end{aligned}$$

Therefore, the smallest positive value  $t$  such that

$$C_k + S_k + \sum_{i=1}^{k-1} (1 - x_i)C_i + \sum_{i=1}^{k-1} \left\lceil \frac{t + Q_1^{\vec{x}}}{T_i} \right\rceil \leq t \quad (11)$$

is always larger than or equal to Equation (6)'s solution.

Replacing  $(t + Q_1^{\vec{x}})$  by  $\theta$  in Eq. (11), we get that  $R_k$  is upper bounded by the smallest  $\theta - Q_1^{\vec{x}} > 0$  and therefore by the smallest positive  $\theta$  such that

$$\begin{aligned} & C_k + S_k + \sum_{i=1}^{k-1} (1 - x_i)C_i + \sum_{i=1}^{k-1} \left\lceil \frac{\theta}{T_i} \right\rceil \leq \theta - Q_1^{\vec{x}} \\ \Leftrightarrow & C_k + S_k + Q_1^{\vec{x}} + \sum_{i=1}^{k-1} (1 - x_i)C_i + \sum_{i=1}^{k-1} \left\lceil \frac{\theta}{T_i} \right\rceil \leq \theta \\ \Leftrightarrow & C_k + S_k + \sum_{i=1}^{k-1} (x_i S_i + (1 - x_i)C_i) + \sum_{i=1}^{k-1} \left\lceil \frac{\theta}{T_i} \right\rceil C_i \leq \theta. \end{aligned} \quad (12)$$

Now, consider the particular vector assignment  $\vec{x}$  in which

$$x_i = \begin{cases} 1 & \text{if } S_i \leq C_i \\ 0 & \text{otherwise,} \end{cases}$$

for  $i = 1, 2, \dots, k-1$ . By the definition of  $B_k$  (i.e., Eq. (3)), we get that

$$B_k = S_k + \sum_{i=1}^{k-1} \min(C_i, S_i) = S_k + \sum_{i=1}^{k-1} (x_i S_i + (1 - x_i)C_i)$$

and Eq. (12) thus becomes identical to Eq. (4). Therefore, if Eq. (4) deems a task set as being schedulable, so does Corollary 1. ■

**Theorem 2.** *The schedulability test presented in Corollary 1 dominates the schedulability tests provided by Equations (1), (2), and (4).*

*Proof:* It directly follows from Lemmas 1, 2 and 3. ■

### 5.4 Proof of Correctness

We now provide the proof to support the correctness of the test in Theorem 1. Our proof strategy is to show that the worst-case response time of task  $\tau_k$  can be safely upper-bounded by

any assignment of  $\vec{x}$  of the  $k - 1$  higher-priority tasks when adopting Eq. (5) as the response time analysis.

Throughout the proof, we consider any arbitrary assignment  $\vec{x}$ , in which  $x_i$  is either 0 or 1. For the sake of notational brevity, we classify the  $k - 1$  higher-priority tasks into two sets:  $\mathbf{T}_0$  and  $\mathbf{T}_1$ . A task  $\tau_i$  is in  $\mathbf{T}_0$  if  $x_i$  is 0; otherwise, it is in  $\mathbf{T}_1$ .

Our analysis is also based on very simple properties and lemmas enunciated as follows:

**Property 1.** *In a preemptive fixed-priority schedule, the lower-priority jobs do not impact the schedule of the higher-priority jobs.*

**Lemma 4.** *In a preemptive fixed-priority schedule, if the worst-case response time of task  $\tau_i$  is no more than its period  $T_i$ , preventing the release of a job of task  $\tau_i$  does not affect the schedule of any other job of task  $\tau_i$ .*

*Proof:* Since the worst-case response time of task  $\tau_i$  is no more than its period, any job  $\tau_{i,j}$  of task  $\tau_i$  completes its execution before the release of the next job  $\tau_{i,j+1}$ . Hence, the execution of  $\tau_{i,j}$  does not directly interfere with the execution of any other job of  $\tau_i$ , which then depends only on the schedule of the higher priority jobs. Furthermore, as stated in Property 1, the removal of  $\tau_{i,j}$  has no impact on the schedule of the higher-priority jobs, thereby implying that the other jobs of task  $\tau_i$  are not affected by the removal of  $\tau_{i,j}$ . ■

With the above properties, we can present the detailed proof of Theorem 1. However, the proof involves several transformation steps. To illustrate some important steps in the proof, we also provide one concrete example. Consider a task system with the following 4 tasks:

- $T_1 = 6, C_1 = 1, S_1 = 1, x_1 = 1,$
- $T_2 = 10, C_2 = 1, S_2 = 6, x_2 = 0,$
- $T_3 = 18, C_3 = 4, S_3 = 1, x_3 = 1,$
- $T_4 = 20, C_4 = 5, S_4 = 0.$

Figure 1 demonstrates a schedule for the jobs of the above 4 tasks. We assume that the first job of task  $\tau_1$  arrives at time  $4 + \epsilon$  with a very small  $\epsilon > 0$ . The first job of task  $\tau_2$  suspends itself from time 0 to time  $5 + \epsilon$ , and is blocked by task  $\tau_1$  from time  $5 + \epsilon$  to time  $6 + \epsilon$ . After some very short computation with  $\epsilon$  amount of time, the first job of task  $\tau_2$  suspends itself again from time  $6 + 2\epsilon$  to 7.

**Proof of Theorem 1.** Let us consider the task set  $\tau'$  composed of  $\{\tau_1, \tau_2, \dots, \tau_{k-1}, \tau'_k, \tau_{k+1}, \dots\}$  and let  $\Psi$  be a schedule of  $\tau'$ , in which  $R'_k \leq T_k$  by our assumption. Suppose that a job  $J_k$  of task  $\tau'_k$  arrives at time  $r_k$  and finishes at time  $f_k$ . We will prove that the response time analysis in Eq. (5) gives us a safe upper bound on  $f_k - r_k$  for any job  $J_k$  in  $\Psi$ .

The proof is built upon the three following steps:

- 1) We discard all the jobs that do not contribute to the response time of  $J_k$  in the schedule  $\Psi$ . We follow an inductive strategy by iteratively inspecting the schedule of the higher priority tasks in  $\Psi$ , starting with  $\tau_{k-1}$  until the highest priority task  $\tau_1$ . At each iteration, a time instant  $t_j$  is identified such that  $t_j \leq t_{j+1}$  ( $1 \leq j < k$ ). Then, all the jobs of task  $\tau_j$  released before  $t_j$  are removed

from the schedule and, if needed, replaced by an artificial job mimicking the interference caused by the residual workload of task  $\tau_j$  at time  $t_j$  on the response time of job  $J_k$ .

- 2) The final reduced schedule is analyzed so as to characterize the worst-case response time of  $\tau'_k$  in  $\Psi$  by using workload functions.
- 3) We then prove that the response time analysis in Eq. (5) is indeed an upper bound on the worst-case response time  $R'_k$  of  $\tau'_k$ .

### Step 1: Reducing the schedule $\Psi$

During this step, we iteratively build an artificial schedule  $\Psi^j$  from  $\Psi^{j+1}$  (with  $1 \leq j < k$ ) so that the response time of  $\tau'_k$  remains identical. At each iteration, we define  $t_j$  for task  $\tau_j$  in the schedule  $\Psi^{j+1}$  (with  $j = k - 1, k - 2, \dots, 1$ ) and build  $\Psi^j$  by removing all the jobs released by  $\tau_j$  before  $t_j$ .

#### Basic step (definition of $\Psi^k$ and $t_k$ ):

Recall that the job  $J_k$  of task  $\tau'_k$  arrives at time  $r_k$  and finishes at time  $f_k$  in schedule  $\Psi$ . We know by Property 1 that the lower priority tasks  $\tau_{k+1}, \tau_{k+2}, \dots, \tau_n$  do not impact the response time of  $J_k$ . Moreover, since we assume that the worst-case response time of task  $\tau'_k$  is no more than  $T_k$ , Lemma 4 proves that removing all the jobs of task  $\tau'_k$  but  $J_k$  has no impact on the schedule of  $J_k$ . Therefore, let  $\Psi^k$  be a schedule identical to  $\Psi$  but removing all the jobs released by the lower priority tasks  $\tau_{k+1}, \dots, \tau_n$  as well as all the jobs released by  $\tau'_k$  at the exception of  $J_k$ . The response time of  $J_k$  in  $\Psi^k$  is thus identical to the response time of  $J_k$  in  $\Psi$ .

We define  $t_k$  as the release time of  $J_k$  (i.e.,  $t_k = r_k$ ).

#### Induction step (definition of $\Psi^j$ and $t_j$ with $1 \leq j < k$ ):

Let  $r_j$  be the arrival time of the last job released by  $\tau_j$  before  $t_{j+1}$  in  $\Psi^{j+1}$  and let  $J_j$  denote that job. Removing all the jobs of task  $\tau_j$  arrived before  $r_j$  has no impact on the schedule of any other job released by  $\tau_j$  (Lemma 4) or any higher priority job released by  $\tau_1, \dots, \tau_{j-1}$  (Property 1). Moreover, because by the construction of  $\Psi^{j+1}$ , no task with a priority lower than  $\tau_j$  executes jobs before  $t_{j+1}$  in  $\Psi^{j+1}$ , removing the jobs released by  $\tau_j$  before  $t_{j+1}$  does not impact the schedule of the jobs of  $\tau_{j+1}, \dots, \tau_k$ . Therefore, we can safely remove all the jobs of task  $\tau_j$  arrived before  $r_j$  without impacting the response time of  $J_k$ . Two cases must then be considered:

- (a)  $\tau_j \in \mathbf{T}_1$ . In this case, we analyze two different subcases:
  - $J_j$  completed its execution before or at  $t_{j+1}$ . By Lemma 4 and Property 1, removing all the jobs of task  $\tau_j$  arrived before  $t_{j+1}$  has no impact on the schedule of the higher-priority jobs (jobs released by  $\tau_1, \dots, \tau_{j-1}$ ) and the jobs of  $\tau_j$  released after or at  $t_{j+1}$ . Moreover, because no task with lower priority than  $\tau_j$  executes jobs before  $t_{j+1}$  in  $\Psi^{j+1}$ , removing the jobs released by  $\tau_j$  before  $t_{j+1}$  does not impact the schedule of the jobs of  $\tau_{j+1}, \dots, \tau_k$ . Therefore,  $t_j$  is set to  $t_{j+1}$  and  $\Psi^j$  is generated by removing all the jobs of task  $\tau_j$  arrived

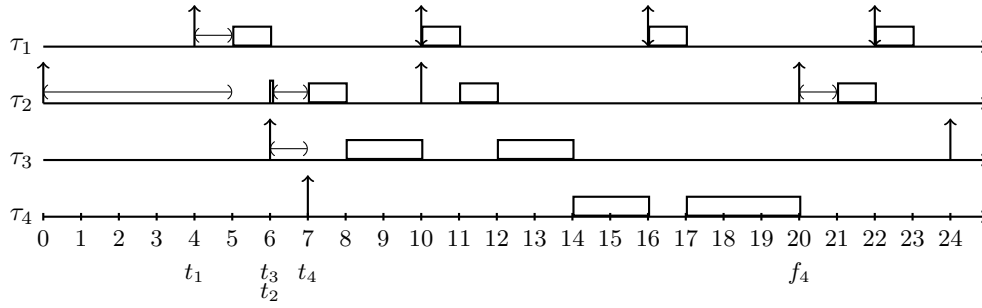


Fig. 1: An illustrative example of Step 1 in the proof of Theorem 1.

before  $t_{j+1}$ . The response time of  $J_k$  in  $\Psi^j$  thus remains unchanged in comparison to its response time in  $\Psi^{j+1}$ .

- $J_j$  did not complete its execution by  $t_{j+1}$ . For such a case,  $t_j$  is set to  $r_j$  and hence  $\Psi^j$  is built from  $\Psi^{j+1}$  by removing all the jobs released by  $\tau_j$  before  $r_j$ .

Note that because by the construction of  $\Psi^{j+1}$  and hence  $\Psi^j$  there is no job with priority lower than  $\tau_j$  available to be executed before  $t_{j+1}$ , the maximum amount of time during which the processor remains idle within  $[t_j, t_{j+1})$  is at most  $S_j$  time units.

- (b)  $\tau_j \in \mathbf{T}_0$ . For such a case, we set  $t_j$  to  $t_{j+1}$ . Let  $c_j^*$  be the remaining execution time for the job of task  $\tau_j$  at time  $t_j$ . We know that  $c_j^*$  is at most  $C_j$ . Since by the construction of  $\Psi^j$ , all the jobs of  $\tau_j$  released before  $t_j$  are removed, the job of task  $\tau_j$  arrived at time  $r_j$  ( $< t_j$ ) is replaced by a new job released at time  $t_j$  with execution time  $c_j^*$  and the same priority than  $\tau_j$ . Clearly, this has no impact on the execution of any job executed after  $t_j$  and thus on the response time of  $J_k$ . The remaining execution time  $c_j^*$  of  $\tau_j$  at time  $t_j$  is called the *residual workload* of task  $\tau_j$  for the rest of the proof.

The above construction of  $\Psi^{k-1}, \Psi^{k-2}, \dots, \Psi^1$  is repeated until producing  $\Psi^1$ . The procedures are well-defined. Therefore, it is guaranteed that  $\Psi^1$  can be constructed. Note that after each iteration, the number of jobs considered in the schedule have been reduced, yet without affecting the response time of  $J_k$ .

An example of the procedures in Step 1: In this schedule illustrated in Figure 1,  $f_k$  is set to  $20 - \epsilon$ . We define  $t_4$  as 7. Then, we set  $t_3$  to 6. When considering task  $\tau_2$ , since it belongs to  $\mathbf{T}_0$ , we greedily set  $t_2$  to  $t_3 = 6$  and the residual workload  $c_2^*$  is 1. Then,  $t_1$  is set to  $4 + \epsilon$ . In the above schedule, the idle time from  $4 + \epsilon$  to  $20 - \epsilon$  is at most  $2 = S_1 + S_3$ . We have to further consider one job of task  $\tau_2$  arrived before time  $t_1$  with execution time  $C_2$ .

## Step 2: Analyzing the final reduced schedule $\Psi^1$

We now analyze the properties of the final schedule  $\Psi^1$  in which all the unnecessary jobs have been removed. The proof is based on the fact that for any interval  $[t_1, t)$ , there is

$$\text{idle}(t_1, t) + \text{exec}(t_1, t) = (t - t_1) \quad (13)$$

where  $\text{exec}(t_1, t)$  is the amount of time during which the processor executed tasks within  $[t_1, t)$ , and  $\text{idle}(t_1, t)$  is the amount of time during which the processor remained idle

within the interval  $[t_1, t)$ .

If  $t_i < t_{i+1}$ , the processor may idle in the time interval  $[t_i, t_{i+1})$  in  $\Psi^1$ . Suppose that  $\sigma_i$  is the sum of the idle time in this interval  $[t_i, t_{i+1})$  in  $\Psi^1$ . If  $t_i$  is equal to  $t_{i+1}$ , then  $\sigma_i$  is set to 0. Therefore, we have

$$\text{idle}(t_1, t) \leq \sum_{i: t_i < t} \sigma_i. \quad (14)$$

From case (a) of Step 1, we know that  $\sigma_i \leq S_i$ .

Because there is no job released by lower priority tasks than  $\tau_k'$  in  $\Psi^1$ , we only focus on the execution patterns of the tasks  $(\tau_1, \tau_2, \dots, \tau_{k-1}, \tau_k')$ . According to Step 1, we should consider two cases:

- If task  $\tau_j$  is in  $\mathbf{T}_1$ , there is no job of task  $\tau_j$  arrived before  $t_j$  in  $\Psi^1$ . This corresponds to both subcases in case (a) in Step 1. In this case, for any  $\Delta \geq 0$ , the workload, defined as  $W_j^1(\Delta)$ , contributed from task  $\tau_j$  from  $t_j$  to  $t_j + \Delta$  that is executed on the processor is at most

$$W_j^1(\Delta) = \left\lfloor \frac{\Delta}{T_j} \right\rfloor C_j + \min \left\{ \Delta - \left\lfloor \frac{\Delta}{T_j} \right\rfloor T_j, C_j \right\}. \quad (15)$$

- If task  $\tau_j$  is in  $\mathbf{T}_0$ , there may be a job arrived before  $t_j$  with residual workload  $c_j^*$  at time  $t_j$ . This corresponds to case (b) in Step 1. In this case, for any  $\Delta \geq 0$ , the workload, defined as  $\widehat{W}_j^0(\Delta, c_j^*)$ , contributed from task  $\tau_j$  from  $t_j$  to  $t_j + \Delta$  has to consider two subcases:
  - If the residual workload  $c_j^*$  of task  $\tau_j$  is 0, the earliest arrival time of task  $\tau_j$  can be any time point at or after  $t_j$ . In this case, for any  $\Delta \geq 0$ , the workload contributed from task  $\tau_j$  from  $t_j$  to  $t_j + \Delta$  that is executed on the processor is at most

$$\widehat{W}_j^0(\Delta, 0) = W_j^1(\Delta). \quad (16)$$

- If the residual workload  $c_j^*$  of task  $\tau_j$  is positive, the absolute deadline of the job corresponding to the residual workload must be at least  $t_j + c_j^*$ ; otherwise, the job corresponding to the residual workload would miss its deadline. Therefore, the earliest arrival time of task  $\tau_j$  arriving *strictly after*  $t_j$  is at least  $t_j + (T_j - D_j + c_j^*)$  in  $\Psi^1$ . For notational brevity, let  $\rho_j$  be  $(T_j - D_j + c_j^*)$ . In this case, for any  $\Delta \geq 0$  and  $c_j^* > 0$ , the workload contributed from task  $\tau_j$  from  $t_j$  to  $t_j + \Delta$  that is

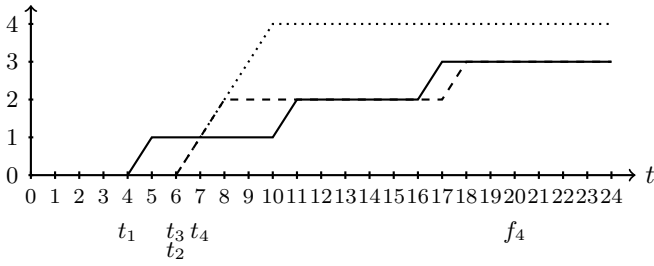


Fig. 2: The workload function for the three higher-priority tasks in Figure 2. Solid line:  $W_1^1(t - t_1)$ , Dashed line:  $W_2^0(t - t_2)$ , Dotted line:  $W_3^1(t - t_3)$ , where the functions are 0 if  $t - t_j < 0$  for  $j = 1, 2, 3$ .

executed on the processor is at most

$$\widehat{W}_j^0(\Delta, c_j^*) = \begin{cases} \Delta & \text{if } \Delta \leq c_j^* \\ c_j^* & \text{if } c_j^* < \Delta \leq \rho_j \\ c_j^* + W_j^1(\Delta - \rho_j) & \text{otherwise.} \end{cases} \quad (17)$$

It is proved in Lemma 5 that the worst case residual workload in  $\widehat{W}_j^0(\Delta, c_j^*)$  by considering both Eq. (16) and Eq. (17) is to have  $c_j^* = C_j$ , i.e., for all  $\Delta \geq 0$ , we have  $\widehat{W}_j^0(\Delta, C_j) \geq \widehat{W}_j^0(\Delta, c_j^*)$ . For the sake of notational brevity, let

$$W_j^0(\Delta) \stackrel{\text{def}}{=} \widehat{W}_j^0(\Delta, C_j) \quad (18)$$

Putting the execution time from the tasks in  $\mathbf{T}_0$  and  $\mathbf{T}_1$  together, we have for  $i = 2, 3, \dots, k-1$ ,  $\forall t \mid t_{i-1} \leq t < t_i$

$$\text{exec}(t_1, t) \leq \sum_{j=1}^{i-1} x_j \cdot W_j^1(t - t_j) + (1 - x_j) \cdot W_j^0(t - t_j). \quad (19)$$

Putting Eqs. (13), (14), (19) together, we have for  $i = 2, 3, \dots, k-1$ ,  $\forall t \mid t_{i-1} \leq t < t_i$

$$\sum_{j=1}^{i-1} x_j \cdot (W_j^1(t - t_j) + \sigma_j) + (1 - x_j) \cdot W_j^0(t - t_j) \geq t - t_1. \quad (20)$$

Moreover,  $\forall t \mid t_k \leq t < f_k$ , we have

$$C'_k + \sum_{j=1}^{k-1} x_j \cdot (W_j^1(t - t_j) + \sigma_j) + (1 - x_j) \cdot W_j^0(t - t_j) > t - t_1. \quad (21)$$

An example of the procedures in Step 2: In the example used in Figure 1, we have  $\sigma_1 = 1, \sigma_2 = 0$ , and  $\sigma_3 = 1$ . The corresponding functions  $W_1^1(t - t_1)$ ,  $W_2^0(t - t_2)$ ,  $W_3^1(t - t_3)$  are illustrated in Figure 2. Therefore, it is rather clear that all the conditions in Eq. (20) and Eq. (21) hold by simple arithmetics.

### Step 3: Creating Safe Response-Time Analysis

This step constructs a safe response-time analysis based on the conditions in Eqs. (20) and (21). We will construct another release pattern which moves  $t_i$  to  $t_i^*$  for  $i = 2, 3, \dots, k$  such that  $t_i^* \leq t_i$  and the corresponding conditions in Eqs. (20)

and (21) will become worse when we use  $t_i^*$ . We start the procedure as follows:

- Initial Step: Let  $t_1^*$  be  $t_1$ .
- Iterative steps ( $i = 2, 3, \dots, k$ ): Let  $t_i^*$  be  $t_{i-1}^* + x_{i-1} \cdot \sigma_{i-1}$ .

This results in  $t_i^* \leq t_i$  for  $i = 2, 3, \dots, k$ . Moreover, by definition,  $t_j^*$  is  $t_1^* + \sum_{i=1}^{j-1} x_i \cdot \sigma_i$  for  $j = 2, 3, \dots, k$ . For any task  $\tau_j$  in  $\mathbf{T}_1$ ,  $\forall \Delta \geq 0$ , since  $t_j \geq t_j^*$ , we have

$$W_j^1(\Delta) \leq W_j^1(\Delta + (t_j - t_j^*)). \quad (22)$$

For any task  $\tau_j$  in  $\mathbf{T}_0$ ,  $\forall \Delta \geq 0$ , since  $t_j \geq t_j^*$ , we have

$$W_j^0(\Delta) \leq W_j^0(\Delta + (t_j - t_j^*)). \quad (23)$$

Therefore, for any  $j = 1, 2, \dots, k-1$ , the contribution  $W_j^1(t - t_j) \leq W_j^1(t - t_j^*)$  and  $W_j^0(t - t_j) \leq W_j^0(t - t_j^*)$  for any  $t \geq t_j$ . Putting these into Eqs. (20)  $\forall t \mid t_k^* \leq t < t_k$  leads to

$$\begin{aligned} & \sum_{j=1}^{k-1} x_j \cdot (W_j^1(t - t_j^*) + \sigma_j) + (1 - x_j) \cdot W_j^0(t - t_j^*) \geq t - t_1, \\ \Rightarrow & \sum_{j=1}^{k-1} x_j \cdot W_j^1(t - t_j^*) + (1 - x_j) \cdot W_j^0(t - t_j^*) \geq t - t_k^*. \end{aligned} \quad (24)$$

Similarly, putting these into Eqs. (21)  $\forall t \mid t_k \leq t < f_k$  leads to

$$C'_k + \sum_{j=1}^{k-1} x_j \cdot W_j^1(t - t_j^*) + (1 - x_j) \cdot W_j^0(t - t_j^*) > t - t_k^*. \quad (25)$$

By the assumption that  $C'_k \geq C_k > 0$ , we can unify the above inequalities in Eq. (24) and Eq. (25) as follows:  $\forall t \mid t_k^* \leq t < f_k$

$$C'_k + \sum_{j=1}^{k-1} x_j \cdot W_j^1(t - t_j^*) + (1 - x_j) \cdot W_j^0(t - t_j^*) > t - t_k^*. \quad (26)$$

By definition,  $\forall t \mid t_k^* \leq t < f_k$ , we have  $t - t_j^* = t - t_k^* + \sum_{\ell=j}^{k-1} x_\ell \sigma_\ell$  for every  $j = 1, 2, \dots, k-1$ . Therefore, we know that  $W_j^1(t - t_j^*) \leq \left\lceil \frac{t - t_k^* + \sum_{\ell=j}^{k-1} x_\ell \sigma_\ell}{T_j} \right\rceil C_j$  for task  $\tau_j$  in  $\mathbf{T}_1$ . Moreover,  $\forall t \mid t_k^* \leq t < f_k$ , we have  $W_j^0(t - t_j^*) \leq \left\lceil \frac{t - t_k^* + \sum_{\ell=j}^{k-1} x_\ell \sigma_\ell + (1 - x_j)(D_j - C_j)}{T_j} \right\rceil C_j$  for task  $\tau_j$  in  $\mathbf{T}_0$ . Therefore, we can conclude that  $\forall t \mid t_k^* \leq t < f_k$

$$C'_k + \sum_{j=1}^{k-1} \left\lceil \frac{t - t_k^* + X_j + (1 - x_j)(D_j - C_j)}{T_j} \right\rceil C_j > t - t_k^*, \quad (27)$$

where  $X_j$  is  $\sum_{\ell=j}^{k-1} x_\ell \sigma_\ell$ . We replace  $t - t_k^*$  with  $\theta$ . The above inequality implies that the minimum  $\theta$  with  $\theta > 0$  such that  $C'_k + \sum_{j=1}^{k-1} \left\lceil \frac{\theta + X_j + (1 - x_j)(D_j - C_j)}{T_j} \right\rceil C_j = \theta$  is larger than or equal to  $f_k - t_k^* \geq f_k - t_k$ .

However, the above condition requires the knowledge of  $\sigma_i$ . It is straightforward to see that  $\sum_{j=1}^{k-1} \left\lceil \frac{\theta + X_j + (1 - x_j)(D_j - C_j)}{T_j} \right\rceil C_j$  reaches the worst case

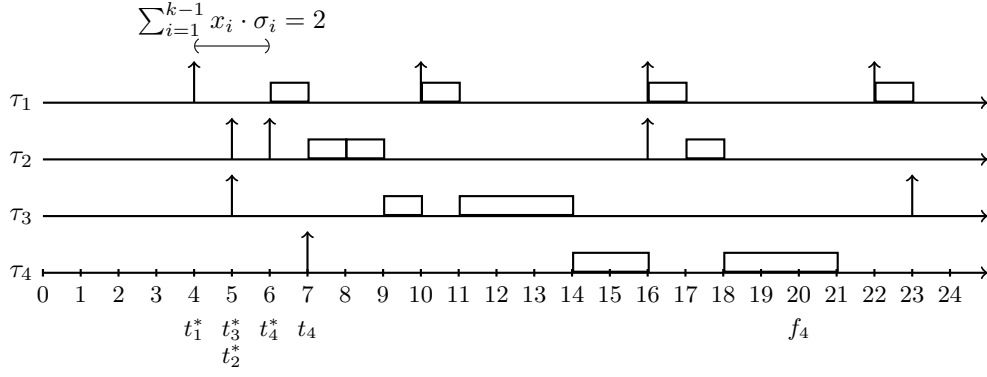


Fig. 3: An illustrative example of Step 3 in the proof of Theorem 1 based on an *imaginary* schedule.

if  $X_j$  is the largest. Since  $X_j$  is upper bounded by  $Q_j^{\vec{x}}$  defined in Theorem 1, we reach the conclusion.

An example of the procedures in Step 3: This can be demonstrated in Figure 3 based on the previous example in Figure 1. Figure 3 provides the imaginary workload and an imaginary execution plan based on the test behind the condition in Eq. (26). *Note that this is not an actual schedule since task  $\tau_2$  is artificially alerted to release two jobs within a short time interval. This is only for illustrative purposes.* For such a case,  $t_1^* = 4, t_2^* = 5, t_3^* = 5$ , and  $t_4^* = 6$ . The two idle time units are used between time 4 and time 6. The accumulated workload is then started to be executed at time 6 and the processor does not idle after time 6. Over here, we see that two jobs of task  $\tau_2$  are executed back to back from time 7 to time 9. As shown in the imaginary schedule in Figure 3, the processor is busy executing the workload from time 6 to time 21, which is more pessimistic than the actual in Figure 1. The conclusion we have in the final statement of the theorem is that  $20 - 7 = f_k - r_k \leq 21 - 6$ .  $\square$

**Lemma 5.**  $\forall \Delta \geq 0$  and  $\forall C_j \geq c_j^* \geq 0$ ,

$$\widehat{W}_j^0(\Delta, C_j) \geq \widehat{W}_j^0(\Delta, c_j^*),$$

where  $\widehat{W}_j^0(\Delta, 0)$  is defined in Eq. (16) and  $\widehat{W}_j^0(\Delta, c_j^*)$  is defined in Eq. (17) if  $c_j^* > 0$ .

*Proof:* The proof is based on simple observations of the workload function. We first prove that  $\widehat{W}_j^0(\Delta, C_j) \geq W_j^1(\Delta)$  defined in Eq. (16). By the definition of  $\rho_j = T_j - D_j + C_j$  when  $c_j^*$  is  $C_j$  and the assumption  $C_j \leq D_j \leq T_j$ , we have  $0 \leq \rho_j \leq T_j$ . Therefore, for  $\Delta \geq T_j$ , we have  $W_j^1(\Delta) = C_j + W_j^1(\Delta - T_j) \leq C_j + W_j^1(\Delta - \rho_j) \leq \widehat{W}_j^0(\Delta, C_j)$ . For  $0 \leq \Delta < T_j$ , it is also obvious that  $\widehat{W}_j^0(\Delta, C_j) \geq \min\{\Delta, C_j\} = W_j^1(\Delta)$ .

We then prove that  $\widehat{W}_j^0(\Delta, C_j) \geq \widehat{W}_j^0(\Delta, c_j^*)$  for any  $0 < c_j^* \leq C_j$  based on the definition in Eq. (17). Figure 4 provides an illustrative example for  $\widehat{W}_j^0(\Delta, c_j^*)$ . We consider three subcases:

- For  $0 \leq \Delta \leq C_j$ , it is obvious that  $\widehat{W}_j^0(\Delta, C_j) \geq \widehat{W}_j^0(\Delta, c_j^*)$ .
- For  $C_j < \Delta \leq T_j - D_j + C_j$ , we have  $\widehat{W}_j^0(\Delta, C_j) = C_j$ ,

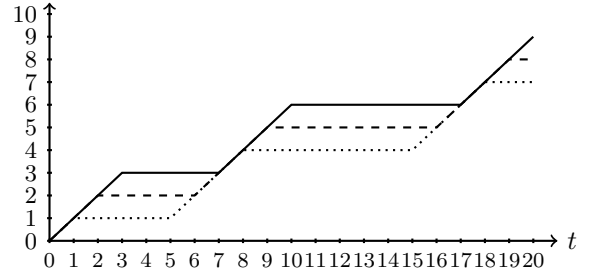


Fig. 4: The workload function  $\widehat{W}_j^0(\Delta, c_j^*)$  when  $T_j = 10$ ,  $C_j = 3$ , and  $D_j = 6$ . Solid line:  $c_j^*$  is 3, Dashed line:  $c_j^*$  is 2, Dotted line:  $c_j^*$  is 1.

and it is obvious that  $\widehat{W}_j^0(\Delta, c_j^*) = c_j^* + \max\{0, \Delta - (T_j - D_j + c_j^*)\} \leq c_j^* + C_j - c_j^* = C_j$ .

- For  $T_j - D_j + C_j < \Delta$ , we have  $\widehat{W}_j^0(\Delta, C_j) = C_j + W_j^1(\Delta - (T_j - D_j + C_j))$ . Moreover, by definition, we also know  $\widehat{W}_j^0(\Delta, c_j^*) \leq \delta + \widehat{W}_j^0(\Delta - \delta, c_j^*)$  for any  $\delta$  with  $0 < \delta \leq \Delta$ . Therefore, for such a case, we can conclude  $\widehat{W}_j^0(\Delta, c_j^*) = c_j^* + W_j^1(\Delta - (T_j - D_j + c_j^*)) \leq C_j + W_j^1(\Delta - (T_j - D_j + C_j))$  by setting  $\delta$  to  $C_j - c_j^*$  with the previous inequality.

■

## 6 Linear Approximation

To test the schedulability of task  $\tau_k$ , Corollary 1 implies to test all the possible vector assignments  $\vec{x} = (x_1, x_2, \dots, x_{k-1})$ , in which there are  $2^{k-1}$  different combinations. Therefore, the time complexity becomes exponential if we consider all the vector assignments. Therefore, in this section, we provide a solution to reduce the time complexity associated to Corollary 1.

Hence, we explain how to use the linear approximation of the test in Eq. (6) to help derive a good vector assignment. By



the definition of  $\lceil x \rceil$ , we have the following inequality:

$$\begin{aligned}
& C'_k + \sum_{i=1}^{k-1} \left\lceil \frac{t + \sum_{\ell=i}^{k-1} x_\ell S_\ell + (1-x_i)(D_i - C_i)}{T_i} \right\rceil C_i \\
& \leq C'_k + \sum_{i=1}^{k-1} \left( \frac{t + \sum_{\ell=i}^{k-1} x_\ell S_\ell + (1-x_i)(D_i - C_i)}{T_i} + 1 \right) C_i \\
& = C'_k + \sum_{i=1}^{k-1} \left( U_i \cdot t + C_i + U_i(1-x_i)(D_i - C_i) + U_i \sum_{\ell=i}^{k-1} x_\ell S_\ell \right) \\
& = C'_k + \sum_{i=1}^{k-1} \left( U_i \cdot t + C_i + U_i(1-x_i)(D_i - C_i) + x_i S_i \left( \sum_{\ell=1}^i U_\ell \right) \right) \quad (28)
\end{aligned}$$

By observing Eq. (28), the contribution of  $x_i$  can be individually determined as  $U_i(D_i - C_i)$  when  $x_i$  is 0 or  $S_i(\sum_{\ell=1}^i U_\ell)$  when  $x_i$  is 1. Therefore, whether  $x_i$  should be set to 0 or 1 can be easily decided by individually comparing the two constants  $U_i(D_i - C_i)$  and  $S_i(\sum_{\ell=1}^i U_\ell)$ . We denote the vector assignment obtained above by  $\vec{x}^{linear}$ . That is, for each higher-priority task  $\tau_i$ ,

- if  $U_i(D_i - C_i) > S_i(\sum_{\ell=1}^i U_\ell)$ , we greedily set  $x_i^{linear}$  to 1;
- otherwise, we greedily set  $x_i^{linear}$  to 0.

For notational brevity, we denote the right-hand side of Eq. (28) as  $rbf_k(t, \vec{x})$  for any  $t > 0$  and given  $\vec{x}$ .

**Theorem 3.** *For any  $t > 0$ , the vector assignment  $\vec{x}^{linear}$  minimizes  $rbf_k(t, \vec{x})$  among all  $2^{k-1}$  possible vector assignments for the  $k-1$  higher-priority tasks. Task  $\tau_k$  is schedulable under the fixed-priority scheduling if*

$$rbf_k(D_k, \vec{x}^{linear}) \leq D_k. \quad (29)$$

Deriving  $\vec{x}^{linear}$  requires  $O(k)$  time complexity and testing Eq. (28) also requires only  $O(k)$  time complexity.

*Proof:* The correctness to test Eq. (29) is due to the derivation in Eq. (28) and Corollary 1. The other statements in this theorem are based on the above discussion in this section and simple observations. ■

## 7 Utilization Bounds and Speedup Factors

Suppose that  $S_i \leq \gamma C_i$  for every task  $\tau_i \in hp(\tau_k)$ . We will present the utilization bounds in this subsection.

We start from the analysis by Liu, which considers the self-suspension time as blocking time for such cases. By using the k2U framework, task  $\tau_k$  in an implicit deadline system is schedulable by using RM scheduling if

$$\left( \frac{C_k + S_k}{T_k} + 1 + \gamma \right) \prod_{i=1}^{k-1} (1 + U_i) \leq 2 + \gamma.$$

That is,  $0 < \alpha_i \leq 1 + \gamma$  and  $0 < \beta_i \leq 1$  for  $i = 1, 2, \dots, k-1$ . This gives the immediate utilization bound to find the infimum

$\sum_{i=1}^k U_k$  such that

$$\begin{aligned}
& (1 + \gamma) * (1 + U_k) \prod_{i=1}^{k-1} (1 + U_i) \\
& \geq \left( \frac{C_k + S_k}{T_k} + 1 + \gamma \right) \prod_{i=1}^{k-1} (1 + U_i) > 2 + \gamma. \\
& \Rightarrow \prod_{i=1}^k (1 + U_i) > \frac{2 + \gamma}{1 + \gamma}.
\end{aligned}$$

Therefore, the utilization bound for a given  $0 \leq \gamma \leq 1$  is  $\ln(\frac{2+\gamma}{1+\gamma})$ .

## 8 Conclusion

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