

Solution to Practice Problem 1: Trace of A Procedure Example in Section 3.7.4.

address	Stack Bottom
1004	
1000	old %ebp
FFC	534 1057
FF8	1057 534
FF4	
FF0	
FEC	FF8
FE8	FFC
FE4	.R1
FE0	1000
FDC	old %ebx
	Stack Top

Keep track of register values: %eax, %ebx, %ecx, %edx, %ebp, %esp

%eax	FF8	FFC	1057	1591	523*1591					
%ebx	534	old %ebx								
%ecx	FF8									
%edx	FFC	1057	523							
%ebp	1000	FE0	1000	Old %ebp						
%esp	1004	1000	FE8	FE4	FE0	FDC	FE0	FE4	1000	1004