

External connection
to a DDR memory

AXI

Multi-Port Memory Controller

AXI

Data
cache

AXI

Instruction
cache

Boot
ROM

PicoRV32
CPU

AXI

Ethernet
interface

UART

SPI

External connection
to a PHY chip for
Ethernet
communication with
the host computer

External connection to a
UART (or other) for
serial communication

External connection to
an SPI Flash memory
chip

