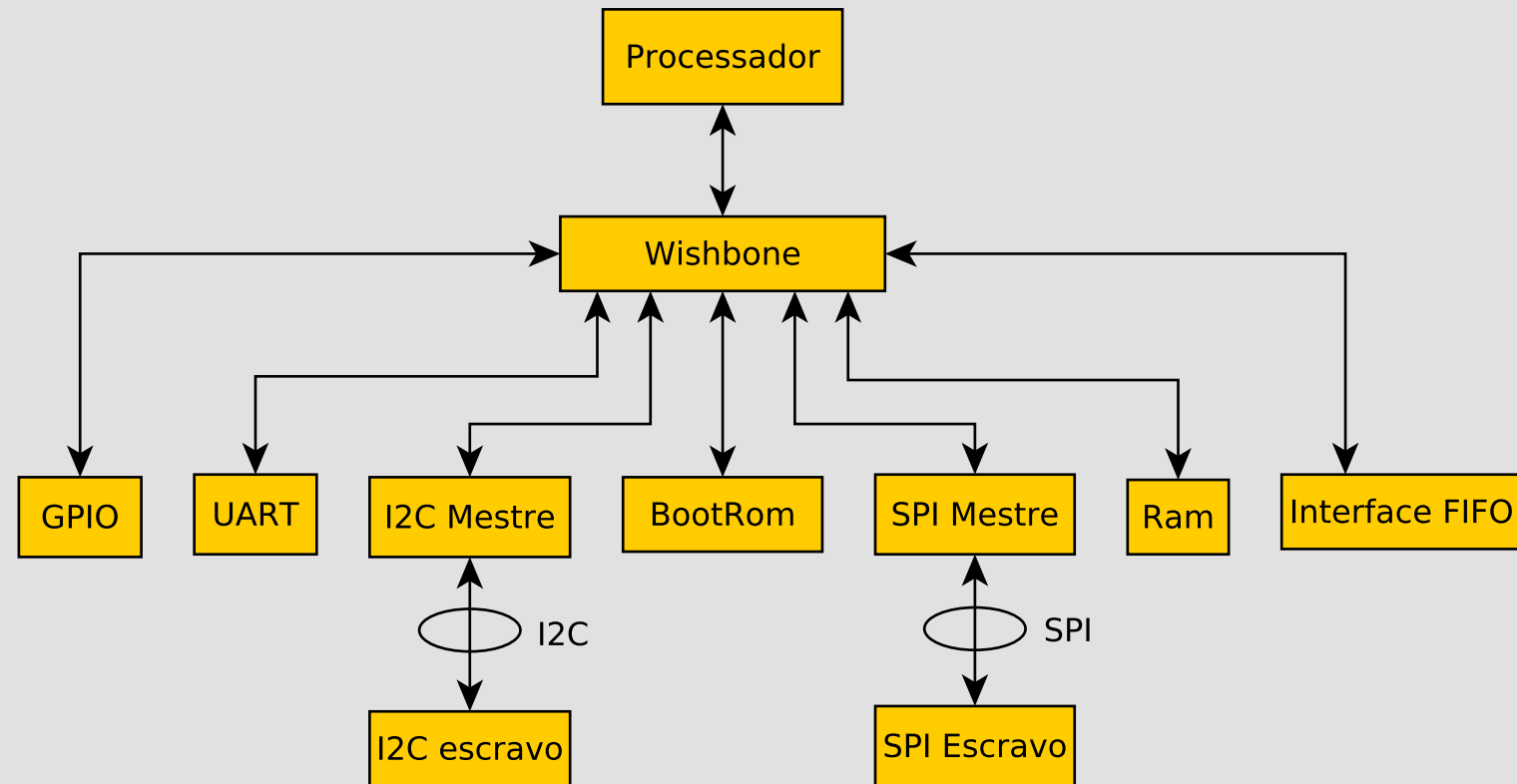


Placa FPGA

Sintetizado



sintetizador

sistema(verilog)

