

## DEBUG GUI DEMO

Subtitle goes here

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## Debug GUI Objective

- We aim to provide a self-contained debug tool for spica5 and future products
- Firmware developers are able to
  - fine tune critical DSP parameters
  - and transparently diagnose internal HW status

#### We will show

- 1. **Emulator** launch on the *veloce* sever
- Remote connection to Emulator via Debug GUI
- 3. PMR/POR set up using script
- 4. **Debug GUI's** features and data plot



# 1. Emulator launch on the *veloce* sever



#### Copy emulation image (on DC5)

```
mlogin -noterm
proj_swgrp spica5n00d
cd /proj/spica5n00d/wa/your_user_name
cp -rf /proj/spica5n00d/wa_emu/gcasuccio/wa_emul/emulation_4PMR_4POR_v3 ./
```

#### Run emulation image (on SJ7)

```
mlogin -eda
proj_swgrp spica5n00d
setenv WORKAREA /proj/spica5n00d/wa/your_user_name/your_emulation_image/pathtest/design_1xport
cd $WORKAREA
source env.csh
velrun -c -comod_host sjc7-m5-cm04 -sgq icomodel:8c:5m

Then
run

Wait for resource allocation success to obtain veloce resource IP & Port, which is used by GUI to connect
via socket
```



# 2. Remote connection to Emulator via Debug GUI

## GUI Release Package

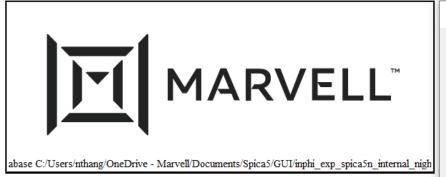
Download Link

http://sw.inphi-corp.local/~swott-jenkins/projects/inphi\_explorer/hsc/builds/debug/spica5n/latest/

Or the latest

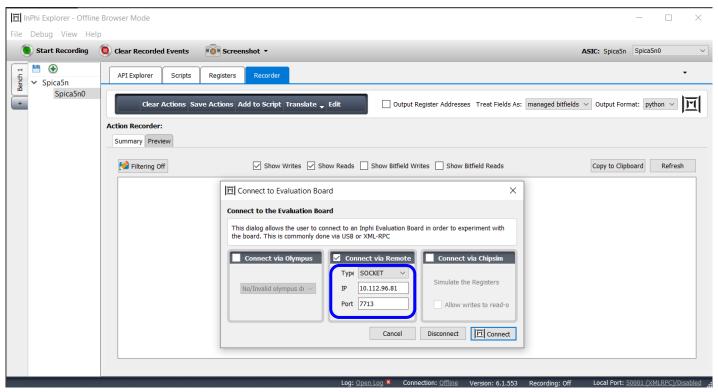
http://sw.inphi-corp.local/~swott-jenkins/projects/inphi\_explorer/hsc/builds/debug/spica5n/6.1.576/

## Start Debug GUI





## Configure Socket Connection



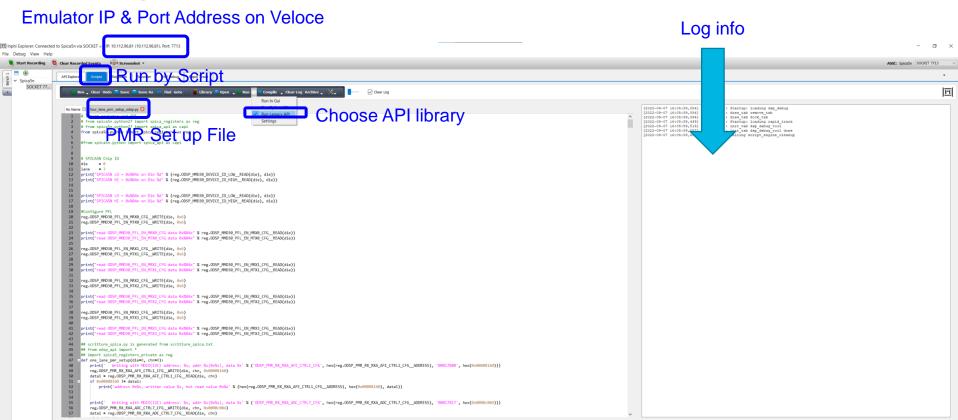


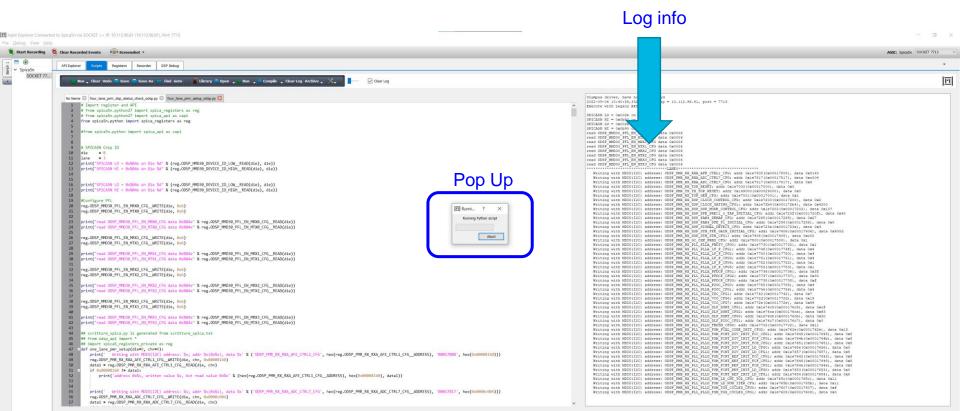
## 3. PMR set up using script

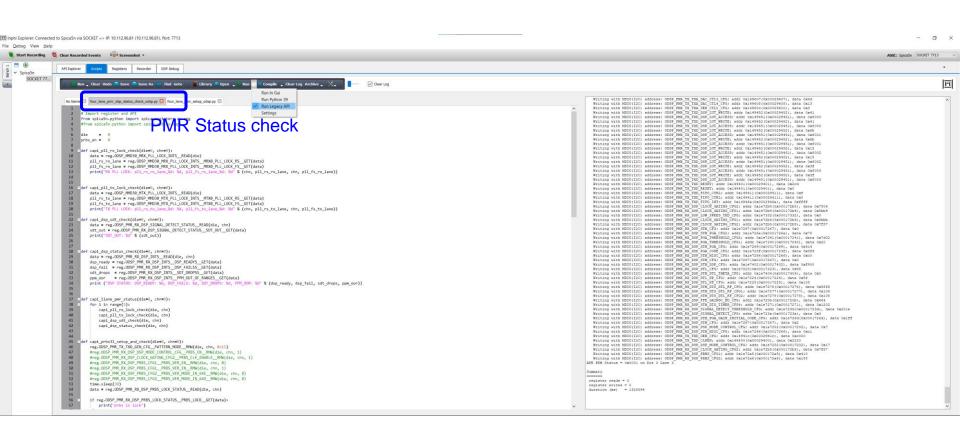
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## Set Up 4 PRM Instances

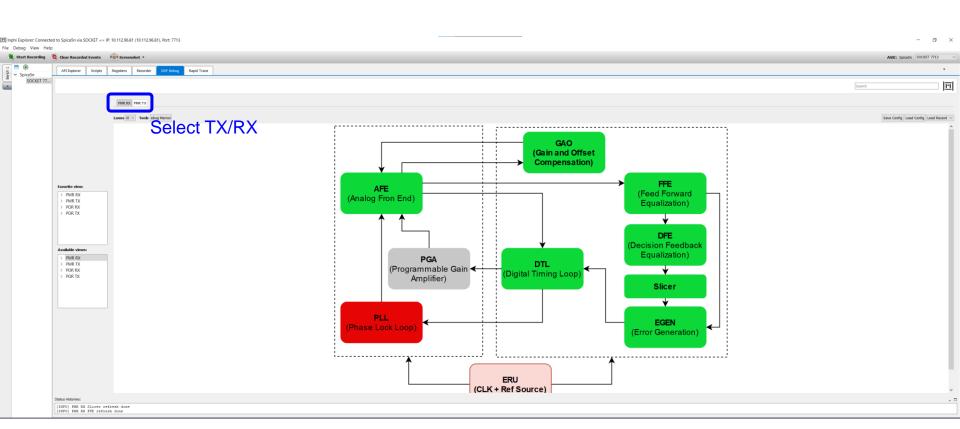


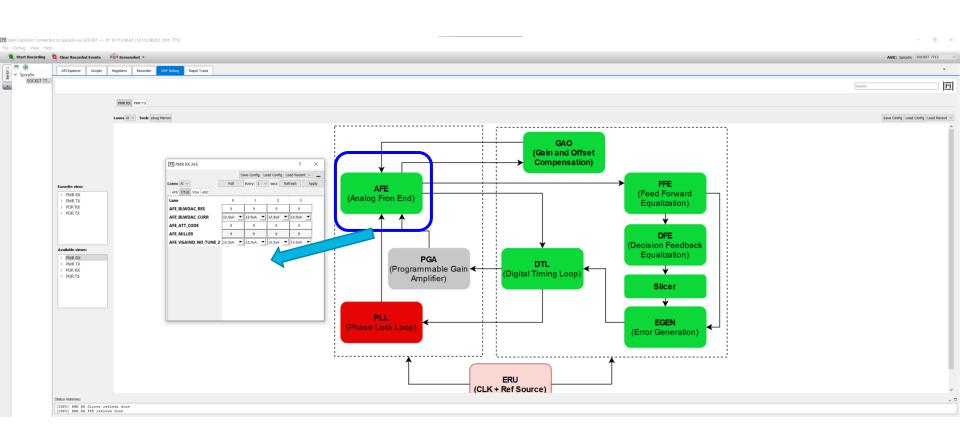


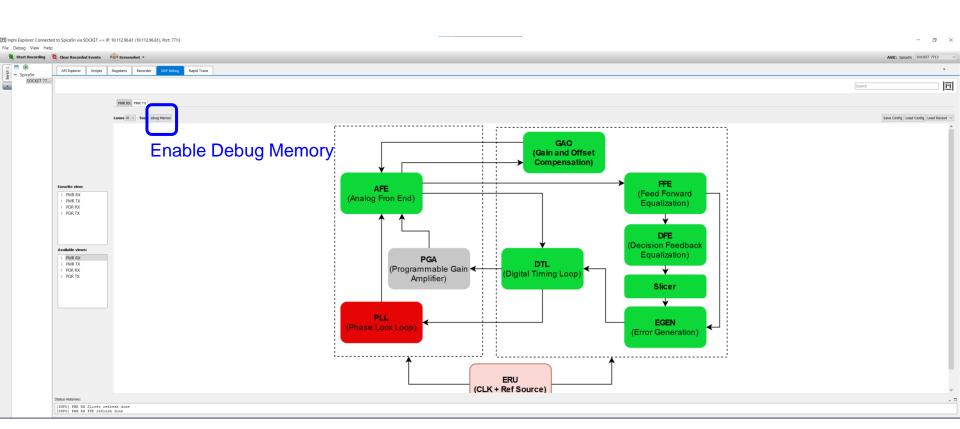


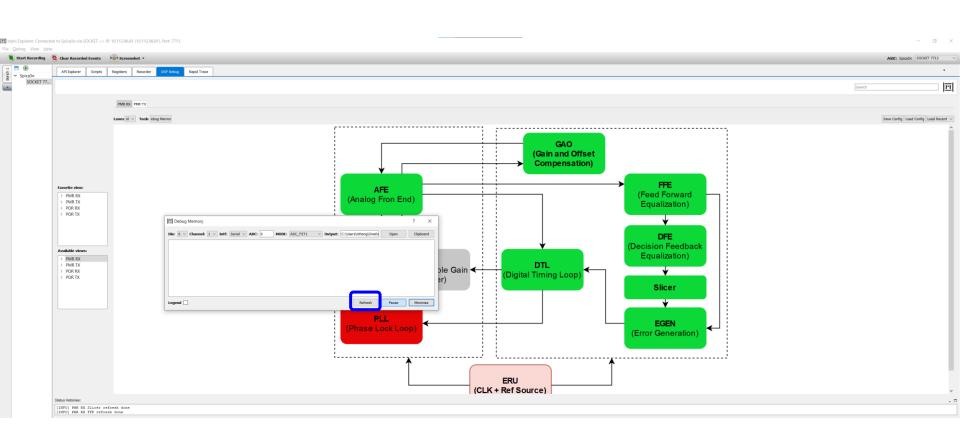


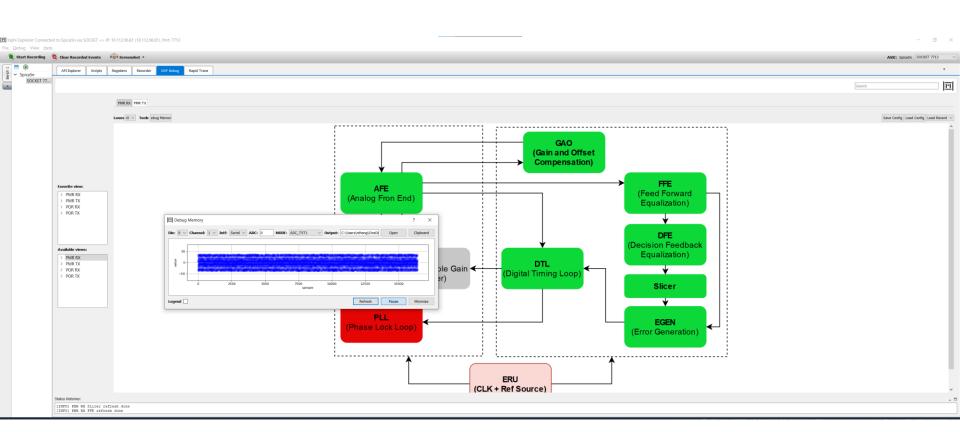
# 4. Debug GUI's features and data plot











#### Conclusion

- Debug GUI demo shows
  - PMR Rx general block
  - Register settings and access via GUI
  - Debug Memory
- Next versions of Debug GUI will include the following features
  - TX PMR
  - PLL
  - ERU
  - ATB
  - PRBS











## Thank You



Essential technology, done right™