

SPECIFICATION

FOR LCD MODULE

MODEL NO:	TM12864J1CCWGWA
CUSTOMER:	
CUSTOMER P/N.	
VERSION	V0.0
CUSTOMER APPROVED	

- ☒ Preliminary specification
- ☐ Final specification

PREPARED BY	CHECKED BY	VERIFIED BY QA DEPT.	APPROVED BY

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RoHS

REVISION RECORD

Version	Page	Revision Items	Name	Date
0.0	1	First release	WP	2009.5.31

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1 Description

The TM12864J1CCWGWA LCM unit consists of 128×64 dot-matrix LCD, and a single-chip S7567 which incorporates LCD controller and common/segment driver. This module can be connected directly to microprocessor with 4-line serial interface or 8-bit parallel interface. Built in a RAM with 65×132-bit capacity for storing the display data, so the unit can efficiently display the desired content under microprocessor control.

- ◆ Wide Operating temperature.
- ◆ Requirements on environmental protection: RoHS.

2 Features

Item	Contents
LCD type	FSTN
	Positive
LCD Duty	1/65
LCD Bias	1/9
Polarizer	transflective
LCD background color	white
Segment color	black
Backlighting	LED
Backlighting type	edge
Backlighting color	white
Backlighting drive	4.0 V(TYPE),I=60mA
View direction	6:00
Operating temperature	-20℃~70℃
Storage temperature	-30℃~80℃
Controller	ST7567
Technology	COG
Power supply	VDD=3.0V
Data Transfer	8 Bit Parallel or SPI

Notes:

- Color tone can slightly change with temperature and driving voltage.
- Color tone will be changed by backlight.

3 Absolute maximum ratings

(Without LED backlighting ,Ta=25℃)

Parameter	Symbol	Min	Max	Unit	Remark
Logic circuit supply voltage	V _{DD}	-0.3	3.6	V	
LCD driving voltage	V _{LCD}	-0.3	16	V	
Operating temperature range	Top	-20	+70	℃	No Condensation
Storage temperature range	Tst	-30	+80	℃	

Note :

- LCD operating voltage V_{LCD} measured between XV0and V0 pins.
- If the module is above these absolute maximum ratings. It may become permanently damaged.

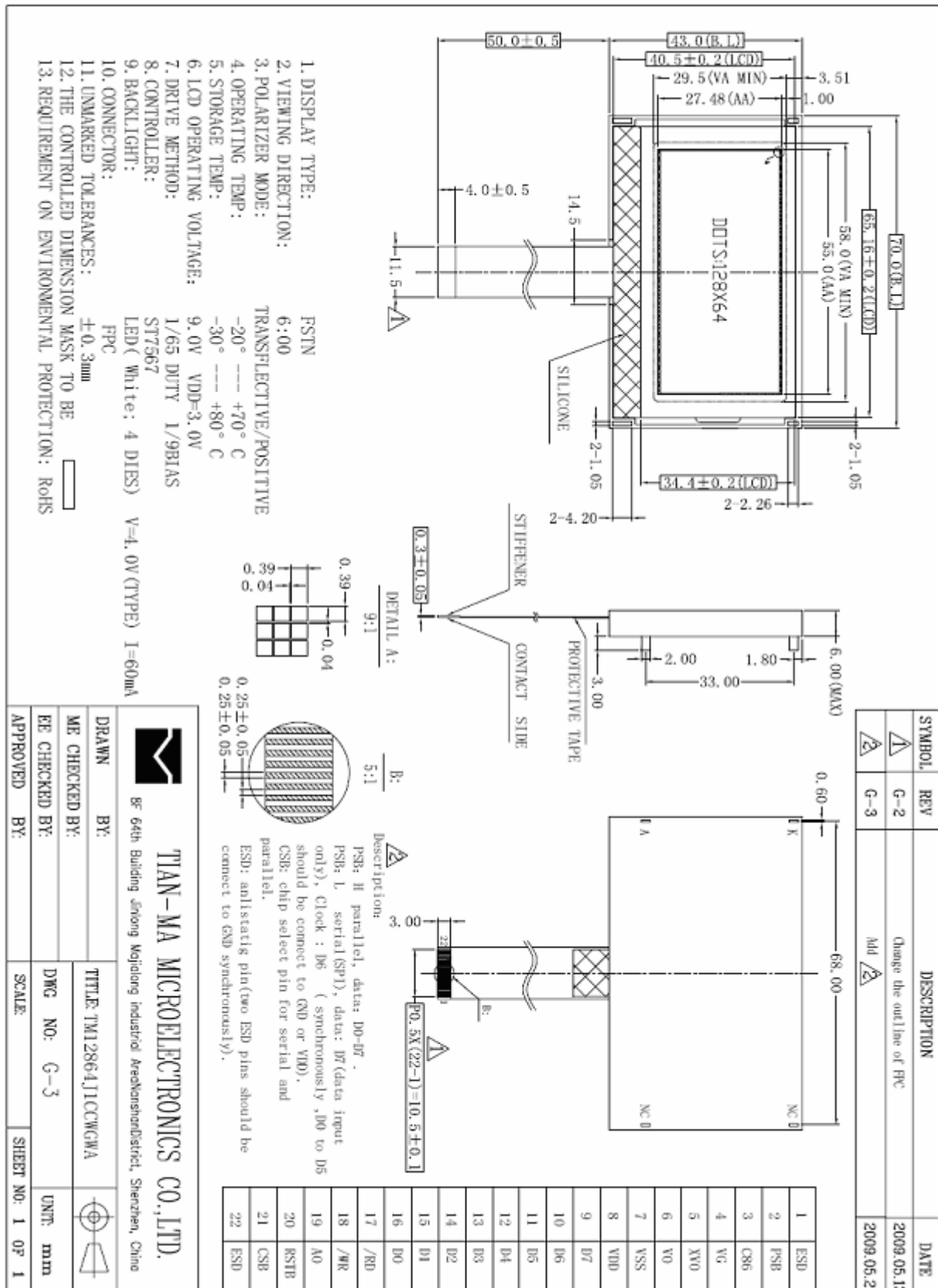
4 Mechanical Characteristics

4.1 Mechanical features

Parameter	Standard Value	Unit
Display type	Graphics module	--
Character size(W×H)	--	mm
Number of dots/characters (W×H)	128X64	--
View area (W×H)	58.0X29.5	mm
Active Area (W×H)	55.0X27.48	mm
Dot Size (W×H)	0.39X0.39	mm
Dot Pitch (W×H)	0.43X0.43	mm
Module size(W×H×D)	70.0X43.0X6.0 (MAX) *	mm
Module total weight (approx)	--	g
Module outline dimensions	Refer to page 5-“Mechanical drawing”	--

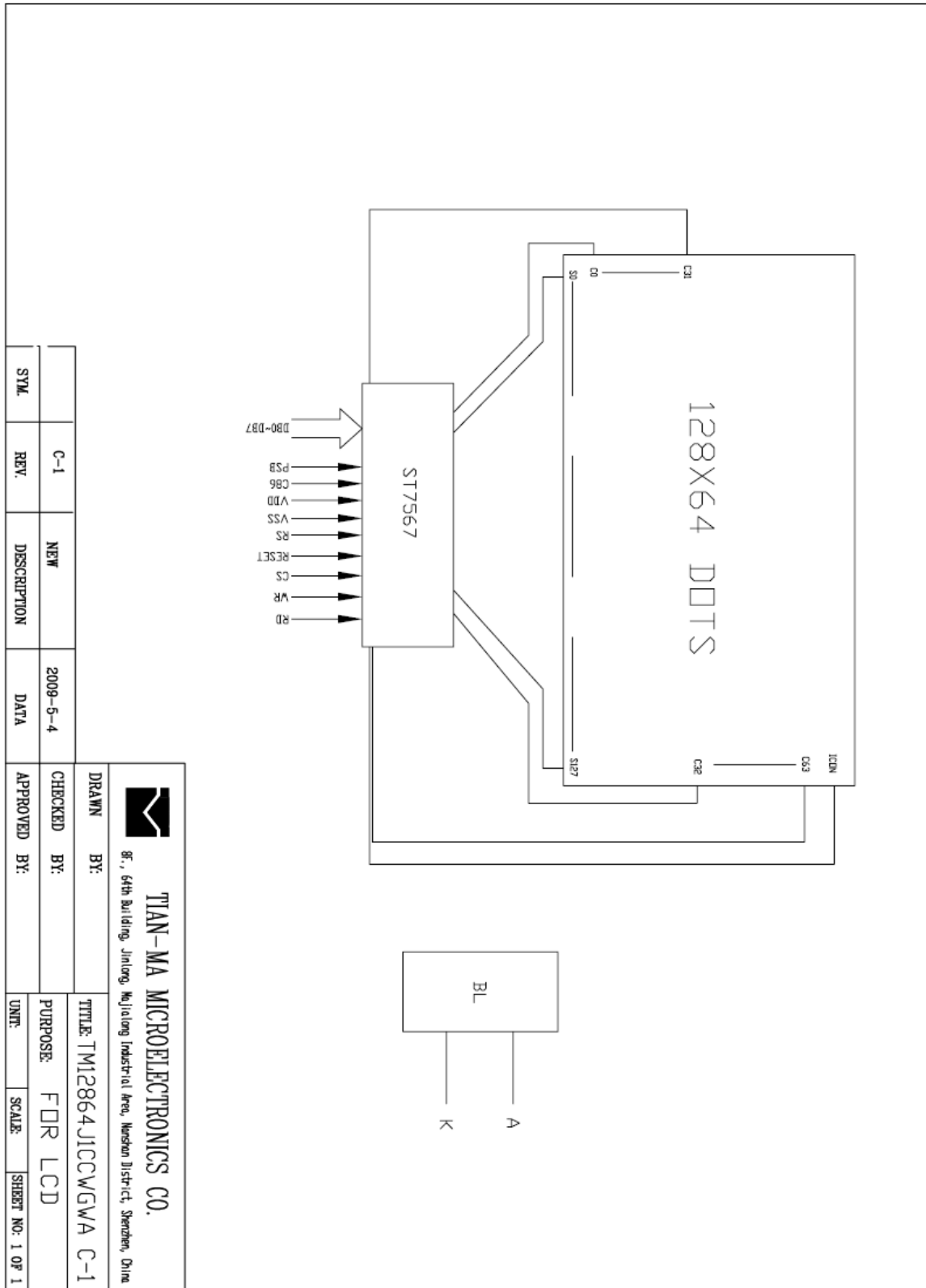
NOTE: Exclude the length of the FPC

4.2 Mechanical drawing.



5 Circuit

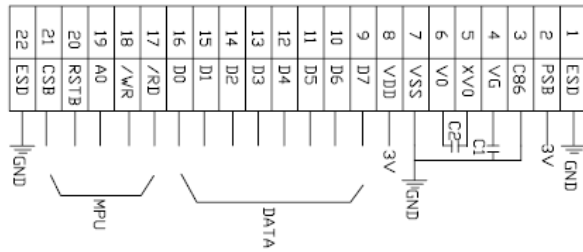
5.1 Block Diagram



5.2 Recommend interface circuit

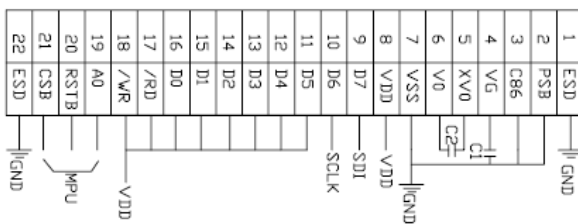
Recommend interface circuit

For 8080 timing




NOTE:
C1=0.1~1.0uF(6V)
C2=0.1~1.0uF(16V)

For SPI



NOTE:
1、C1=0.1~1.0uF(6V)
2、C2=0.1~1.0uF(16V)
3、D0 to D5, /WR and /RD are not used in SPI interface and should fix to VDD

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DRAWN BY:	BY:	TITLE:	TM12864J1CCWGWA
CHECKED BY:	BY:	PURPOSE:	Only reference for customer
APPROVED BY:	BY:	UNIT:	SCALE: SHEET NO: 1 OF 1

6 Interface description .

Pin No.	Symbol	I/O	Description
1	ESD	I	Anlistatig pin ,connect to GND
2	PSB	I	Interface type select pin(L:SPI H: Parallel)
3	C86	I	C86 selects the microprocessor type in parallel interface mode.
4	VG	P	VG is the LCD driving voltage for segment circuits.
5	XV0	P	XV0 is the LCD driving voltage for common circuits at positive frame.
6	V0	P	V0 is the LCD driving voltage for common circuits at negative frame.
7	VSS	0V	GND
8	VDD	3.0V	Power supply voltage for logic
9	D7	I/O	Data bit7
10	D6	I/O	Data bit6
11	D5	I/O	Data bit5
12	D4	I/O	Data bit4
13	D3	I/O	Data bit3
14	D2	I/O	Data bit2
15	D1	I/O	Data bit1
16	D0	I/O	Data bit0
17	/RD	I	Read/Write execution control pin.
18	/WR	I	Read/Write execution control pin.
19	A0	I	data or command select pin
20	RSTB	I	Hardware reset input pin.
21	CSB	I	Chip select input pin.
22	ESD	I	Anlistatig pin, connect to GND

7 Instruction Code & Timing characteristics

7.1 COMMAND

The table below lists the types of commands, including the code of each command. more details refer to ST7567 data sheet please.

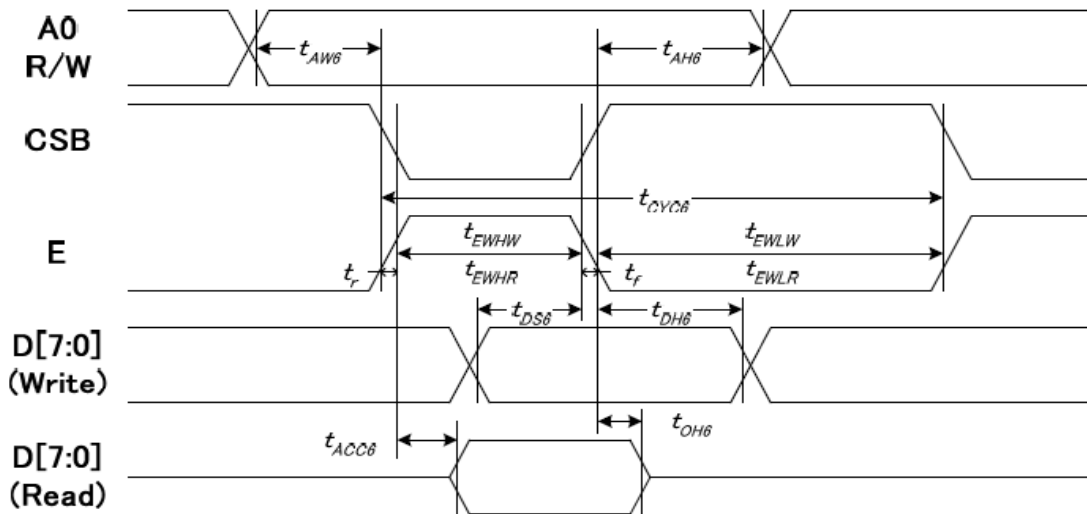
INSTRUCTION	A0	R/W (RWR)	COMMAND BYTE								DESCRIPTION
(1) Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF
(2) Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	S0	Set display start line
(3) Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address
(4) Set Column Address	0	0	0	0	0	1	X7	X6	X5	X4	Set column address (MSB)
	0	0	0	0	0	0	X3	X2	X1	X0	Set column address (LSB)
(5) Read Status	0	1	0	MX	D	RST	0	0	0	0	Read IC Status
(6) Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM
(7) Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM
(8) SEG Direction	0	0	1	0	1	0	0	0	0	0	MX Set scan direction of SEG MX=1, reverse direction MX=0, normal direction
(9) Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display
(10) All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display
(11) Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)
(12) Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0, Write:+1
(13) END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode
(14) RESET	0	0	1	1	1	0	0	0	1	0	Software reset
(15) COM Direction	0	0	1	1	0	0	MY	-	-	-	Set output direction of COM MY=1, reverse direction MY=0, normal direction
(16) Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF
(17) Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio
(18) Set EV	0	0	1	0	0	0	0	0	0	1	Double command!! Set electronic volume (EV) level
	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	
(19) Set Booster	0	0	1	1	1	1	1	0	0	0	Double command!! Set booster level: BL=0: 4X BL=1: 5X
	0	0	0	0	0	0	0	0	0	BL	
(20) Power Save	0	0	Compound Command								Display OFF + All Pixel ON
(21) NOP	0	0	1	1	1	0	0	0	1	1	No operation
(22) Test	0	0	1	1	1	1	1	1	1	-	Do NOT use. Reserved for testing.

Note: Symbol "-" means this bit can be "H" or "L".

7.2 Interface Timing characteristics

Note: Please refer to IC: ST7567 data sheet for more details.

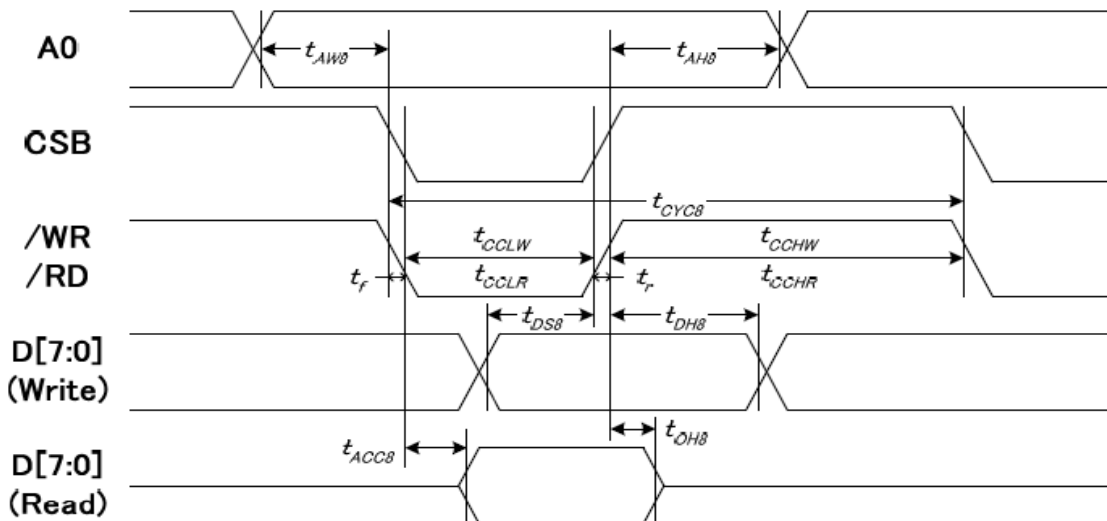
7.2.1 System Bus Timing for 6800 Series MPU



VDD=3.0V Ta=25°C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	t_{AW6}		0	—	ns
Address hold time		t_{AH6}		0	—	
System cycle time	E	t_{CYC6}		400	—	
Enable L pulse width (WRITE)		t_{EHLW}		220	—	
Enable H pulse width (WRITE)		t_{EHLR}		180	—	
Enable L pulse width (READ)		t_{EHLR}		220	—	
Enable H pulse width (READ)		t_{EHLR}		180	—	
Write data setup time	D[7:0]	t_{DS6}		40	—	
Write data hold time		t_{DH6}		20	—	
Read data access time		t_{ACC6}	CL = 16 pF	—	140	
Read data output disable time		t_{OH6}	CL = 16 pF	10	100	

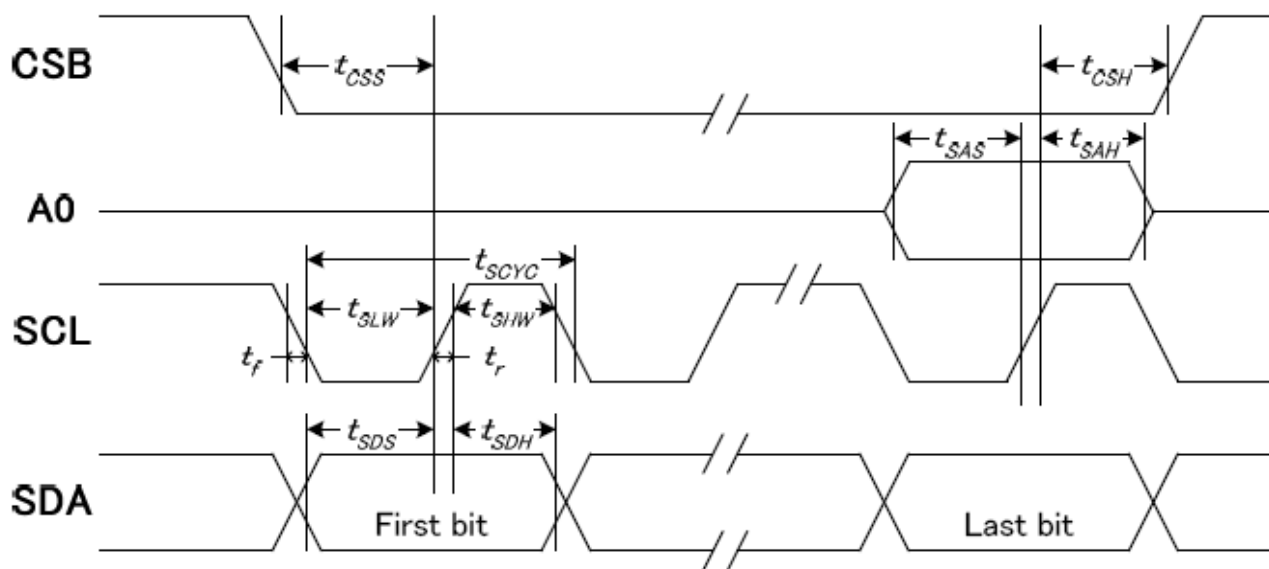
7.2.2 System Bus Timing for 8080 Series MPU



VDD=3.0V Ta=25°C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Address setup time	A0	tAW8		0	—	ns
Address hold time		tAH8		0	—	
System cycle time	/WR	tCYC8		400	—	
/WR L pulse width (WRITE)		tCCLW		220	—	
/WR H pulse width (WRITE)		tCCHW		180	—	
/RD L pulse width (READ)	RD	tCCLR		220	—	
/RD H pulse width (READ)		tCCHR		180	—	
WRITE Data setup time	D[7:0]	tDS8		40	—	
WRITE Data hold time		tDH8		20	—	
READ access time		tACC8	CL = 16 pF	—	140	
READ Output disable time		tOH8	CL = 16 pF	10	100	

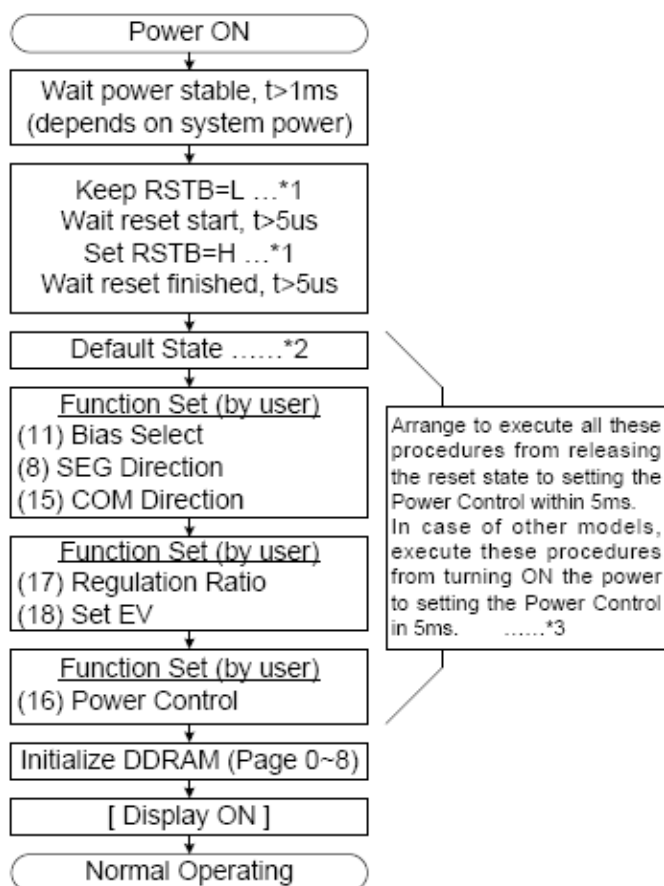
7.2.3 System Bus Timing for 4-Line Serial Interface



VDD=3.0V Ta=25°C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period	SCLK	tSCYC		100	—	ns
SCLK "H" pulse width		tSHW		50	—	
SCLK "L" pulse width		tSLW		50	—	
Address setup time	A0	tSAS		30	—	
Address hold time		tSAH		20	—	
Data setup time	SDA	tSDS		30	—	
Data hold time		tSDH		20	—	
CSB-SCLK time	CSB	tCSS		30	—	
CSB-SCLK time		tCSH		60	—	

7.3 Initialization flow map



Note: The detailed description can be found in the respective sections listed below.

1. Please refer to the timing specification of t_{RW} and t_R .
2. Refer to Section 7. RESET CIRCUIT.
3. The 5ms requirement depends on the characteristics of LCD panel and the external component of the power circuit. It is recommended to check with the real products with external component.
4. The detailed instruction functionality is described in Section 9. INSTRUCTION DESCRIPTION;
5. Power stable is defined as the time that the later power (VDDI or VDDA) reaches 90% of its rated voltage.

Timing Requirement:

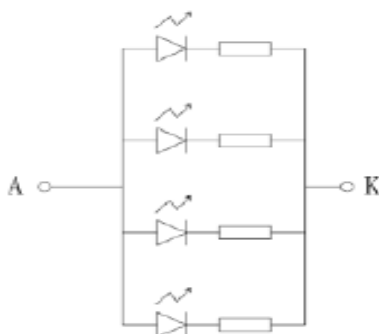
Item	Symbol	Requirement	Note
VDDA power delay	t_{ON-V2}	$0 \leq t_{ON-V2}$	<ul style="list-style-type: none"> ● Applying VDDI and VDDA in any order will not damage IC. ● The requirement listed here is to prevent abnormal display on LCD module.
RSTB input time	t_{ON-RST}	No Limitation	<ul style="list-style-type: none"> ● RSTB=L can be input at any time after power is stable. ● t_{RW} & t_R should match the timing specification of RSTB. ● To prevent abnormal display, the recommended timing is: $0 \leq t_{ON-RST} \leq 30$ ms. ● If RSTB is high or unstable during power ON, a successful hardware reset by RSTB is required after VDDI and VDDA are both stable (like Case-2).

8 Electrical characteristics

 $V_{SS}=0V$, $T_a=25^{\circ}C$

Parameter	Symbol	Condition	MIN	TYP	MAX	UNIT
Logic circuit supply voltage	V_{DD}	--	2.7	3.0	3.3	V
Power supply LCD	V_{LCD}	--	8.8	9.0	9.2	
Input voltage for logic circuit	"H"level	--	0.7X VDD	--	VDD	
	"L"level		0	--	0.3XVDD	
Output voltage for logic circuit	"H"level		0.7XVDD	--	VDD	
	"L"level		0	--	0.3XVDD	
Logic power supply current (Without backlighting)	I_{CC}	-	--	--	2.0	mA
Used driver IC	ST7567					

9 LED backlight characteristics



Circuit diagram (LED 1x4=4 dies)

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Forward voltage	V_f	$I_f=60mA$	3.8	4.0	4.2	V
Luminance	Bp		280	380	510	cd/m ²
Average			75	--	--	%
Color coordinate*	X		0.265	--	0.315	--
	Y		0.265	--	0.315	--

Note:

- Measured at the bare LED backlight unit.
- If the backlight is above these maximum ratings for long time, the service life of the LED backlight will reduce or it will cause poor reliability.

10 Optical Characteristics

10.1 Optical Characteristics

Ta=25℃

Parameter		Symbol	Ratings			Unit	Measuring Temp.	Reference
			Min	Type	Max.			
Operating voltage		V_o	8.7	9.0	9.3	V	25℃	
Frame frequency		f		75	-	Hz		(Note10-2)
Contrast ratio		$Cr(\theta=20^\circ, \Phi=90^\circ \text{ or } 270^\circ)$	3	-	-	-	25℃	(Note10-3)
Response time	Turn on	t_{on}	-	-	300	ms	25℃	(Note10-4)
			-	-	-		0℃	
	Turn off	t_{off}	-	-	300	ms	25℃	
			-	-	-		0℃	
Viewing angle (Cr≥2)	Up-down	θ_1 ($\Phi=90^\circ \text{ or } 270^\circ$)	-20		30	deg	25℃	(Note10-5)
	Left-right	θ_2 ($\Phi=0^\circ \text{ or } 180^\circ$)	-30		30	deg	25℃	

(Note10-1) The maximum and minimum ratings don't mean the LCD works well in the whole range of V_o . V_o must be adjusted to optimize the viewing angle and contrast. Refer to definition of drive voltage, refer to 10.2.

(Note10-2) The frequency shouldn't be too low to avoid flicker. Refer to definition of drive voltage, refer to 10.2.

(Note10-3) Refer to 10.2/10.3/10.4/10.5.

(Note10-4) The selected state is dark and non-selected state is white(or bright) with positive type, reversely the selected state is white (or bright) and non-selected state is dark with negative type. Refer to 10.6 definition of response time.

(Note10-5) Generally the viewing direction is 6:00 or 12:00, sometimes 3:00 or 9:00. The range of left to right and up to down based on Cr=2 show the viewing angle. Viewing angle range isn't the range of defects inspection. Refer to 10.4.

10.2 Definition of drive voltage

(1) Definition of drive voltage and waveform

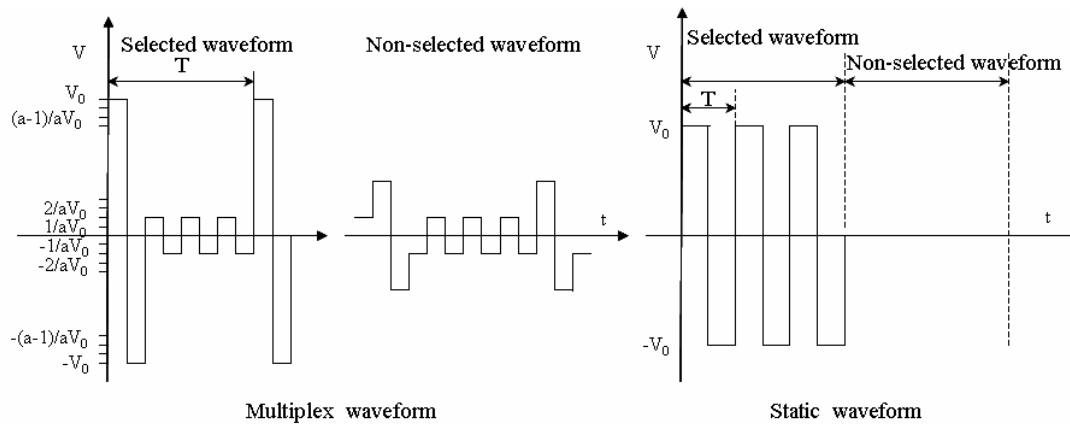


Fig.1 Definition of drive voltage and waveform

Operating voltage: V_0

Frame frequency: $f=1/T$

Duty: $1/N$

Bias: $1/a$

(2) Operating voltage: V_0

TIANMA can evaluate whether the LCD can be redesigned to obtain customer preferable performance if customer's LCD drive voltage isn't adjustable.

10.3 Optical characteristics measurement equipment and method

The setup and test method are showed in fig.2. Test methods are different according to different illumination mode.

Transmissive mode: light resource is placed at the back of LCD.

Reflective mode and transfective mode: light resource is placed at the front side of LCD.

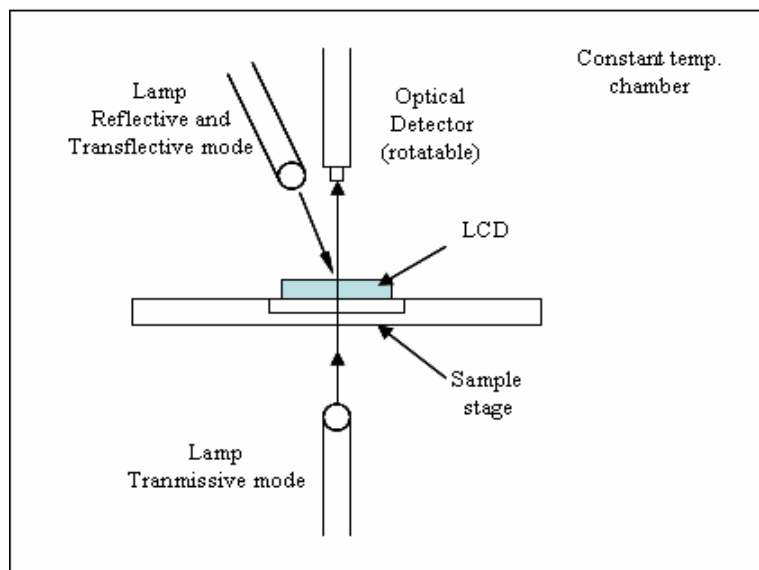


Fig.2 Optical characteristics measurement equipment

The chamber temperature, light resource and driving signal should be stable before testing. If test the characteristics under high or low temperature, the test system should be stable for more than 10 minutes before testing.

10.4 Definition of viewing direction

Refer to the graph below marked by θ and ϕ

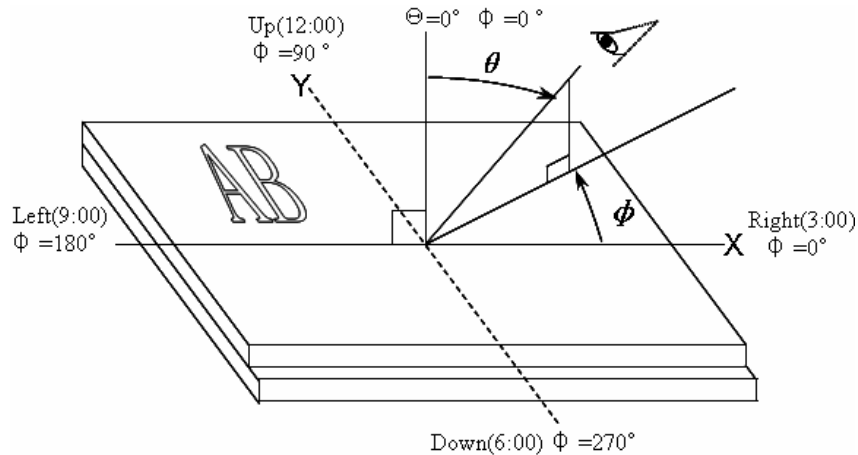


Fig.3 Definition of viewing direction

10.5 Definition of contrast ratio

Contrast ratio can be calculated by the formula (10-1) below for positive type. If the LCD is negative type, $Cr(\theta, \phi)$ is equal to luminance (θ, ϕ , non-selected state) divided by luminance (θ, ϕ , selected state). Note3-4 shows the relationship between selected state, non-selected state and bright state, dark state.

$$Cr(\theta, \phi) = \frac{L_2}{L_1} = \frac{\text{Luminance}(\theta, \phi) \text{ (Bright state)}}{\text{Luminance}(\theta, \phi) \text{ (Dark state)}} \quad (10-1)$$

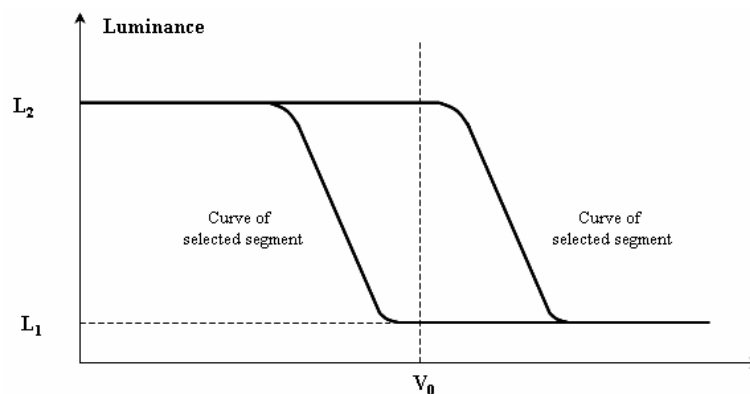


Fig.4 Electro-optical characteristic (EOC) graph (positive type)

10.6 Definition of response time

Turn on time (rise time): $t_{on} = t_d + t_r$ (from non-selected state to selected state)

Turn off time (fall time): $t_{off} = t_D + t_R$ (from selected state to non-selected state)

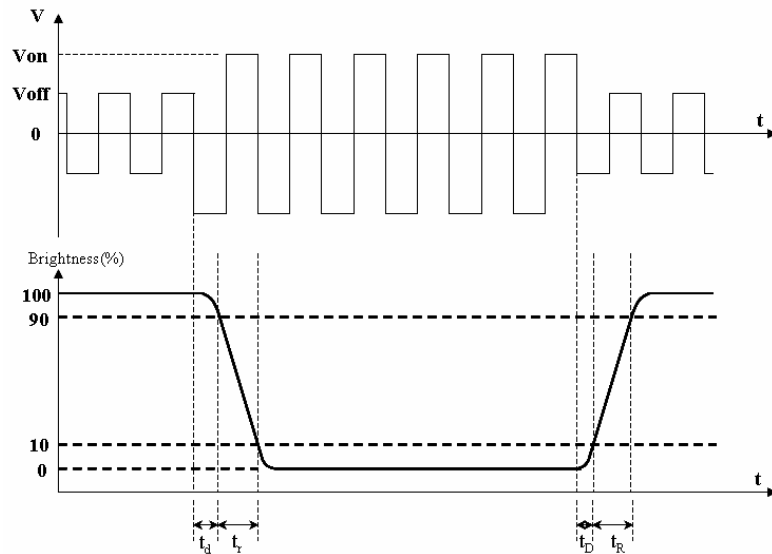


Fig.5 Definition of response time (positive type)

10.7 Definition of viewing angle

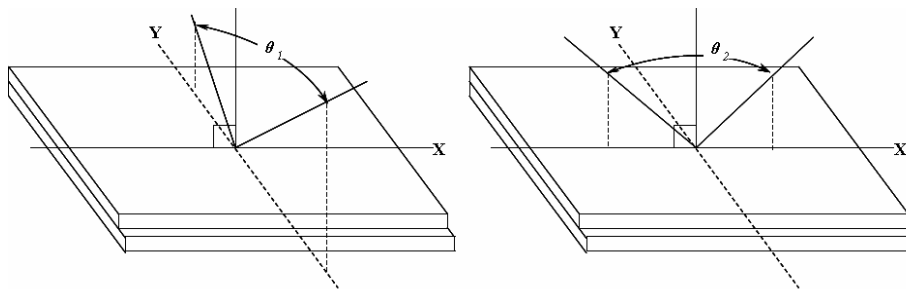


Fig 6 Definition of viewing angle

θ_1 ——range of viewing angle from up to down

θ_2 ——range of viewing angle from left to right.

11 Reliability

11.1 Content of Reliability Test

Ta=25℃

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80℃±2℃ 120H Restore 2H at 25℃ Power off	After testing, cosmetic and electrical defects should not happen.
2	Low Temperature Storage	-30℃±2℃ 120H Restore 2H at 25℃ Power off	
3	High Temperature Operation	70℃±2℃ 120H Restore 2H at 25℃ Power on	
4	Low Temperature Operation	-20℃±2℃ 120H Restore 4H at 25℃ Power on	
5	High Temperature & Humidity Operation	40℃±2℃ 90%RH 120H Power on	
6	Temperature Cycle	-30℃→25℃→80℃ 30min 5min 30min after 10cycle, Restore 2H at 25℃ Power off	
7	Vibration Test	10Hz~150Hz, 100m/s ² , 120min	
8	Shock Test	Half-sine wave,300m/s ² ,11ms	1.After testing, cosmetic and electrical defects should not happen. 2.the product should remain at initial place 3.Product uncovered or package broken is not permitted.
9	Drop Test(package state)	1000mm, concrete floor,1corner, 3edges, 6 sides each time	

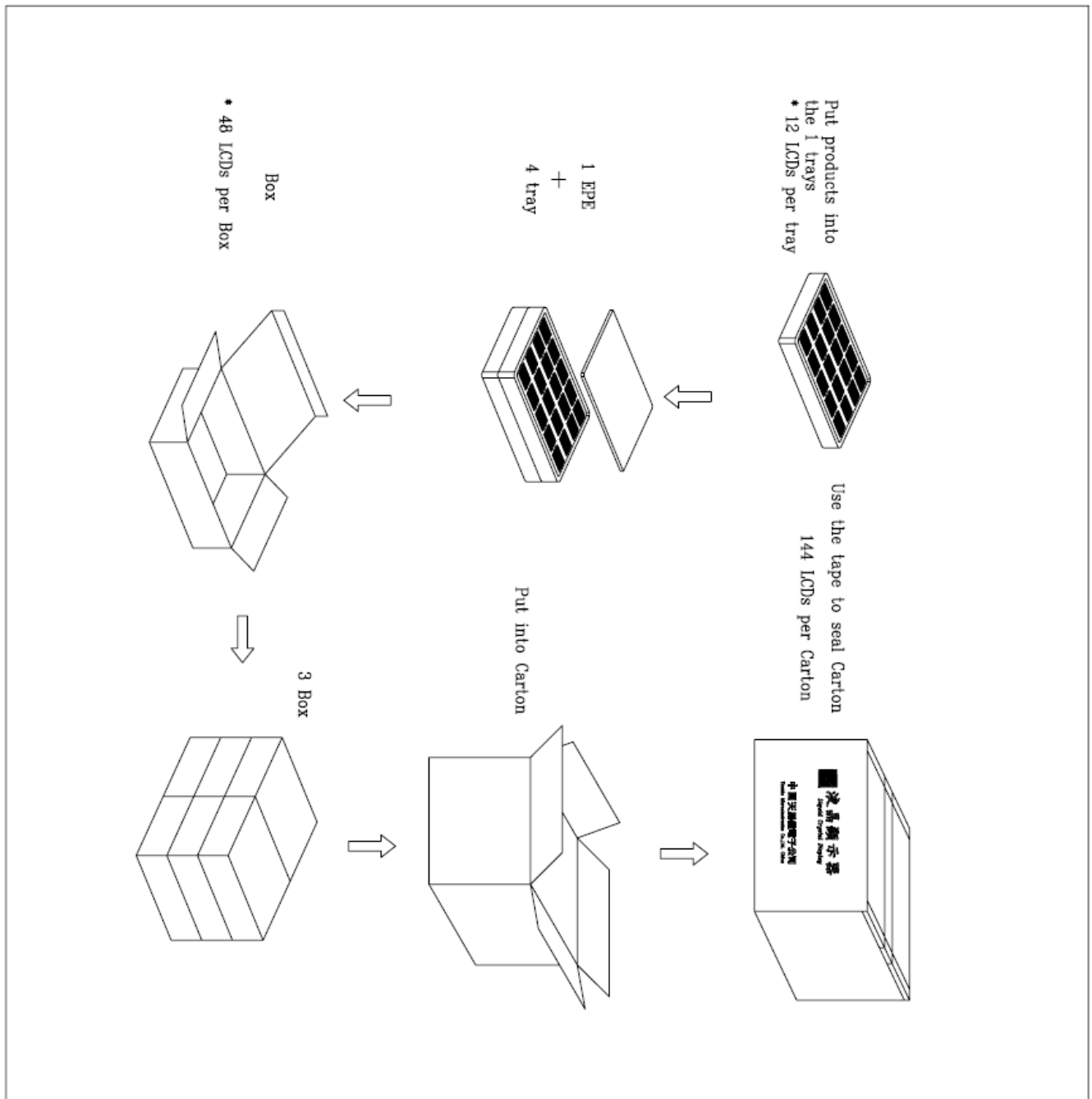
Notes:

- Each test item applies for a test sample only once, The test sample can not be used again in any other test item.
- The test sample is inspected after 2 hours or more storing at room temperature and room humidity after each test item is finished.
- The criteria refer to 11.2.

11.2 Inspection of criteria

Remark NO.	Content
1	Functional test is OK. Missing Segment, shorts, unclear segment, nondisplay, display abnormally, liquid crystal leak are unallowable.
2	After testing, cosmetic defects should not happen, no low temperature bubbles, seal loose and fall, frame rainbow, ACF bubble growing are unallowable in the appearance test.
3	Total current consumption should not be over 10% of initial value.
4	After tests being executed, Contrast must be larger than 70% of its initial value prior to the tests.
5	No glass crack, chipped glass, end seal loose frame crack and so on.
6	No structure loose and fall.

12 Package



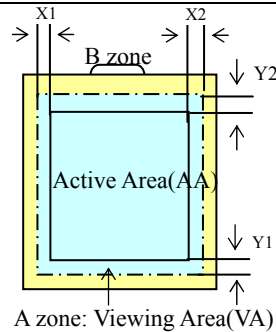
13 Quality level

13.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially degrade usability for product applications, including all functional defects (such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

13.2 Definition of inspection range

<p>For LCD defects, dividing two areas to make a judgment (according figure 1).</p> <p>A zone : Inside Viewing area</p> <p>B zone : Outside Viewing area</p> <p>X1(A.A~V.A): 2mm X2(A.A~V.A): 2mm</p> <p>Y1(A.A~V.A): 2mm Y2(A.A~V.A): 2mm</p>	 <p>Figure 1</p>
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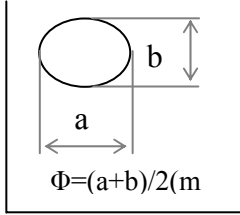
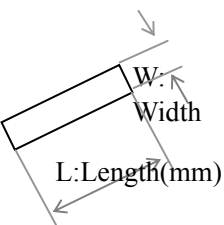
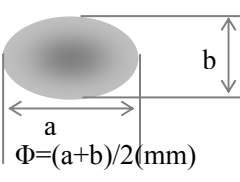
13.3 Inspection items and general notes

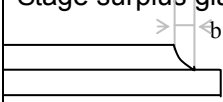
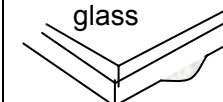
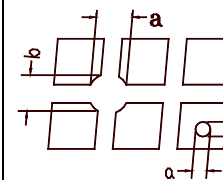
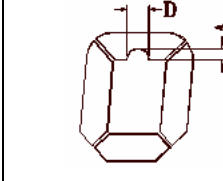
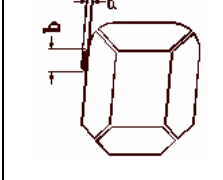
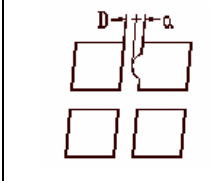
General notes	<p>①Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and TIANMA.</p> <p>②Viewing area should be the area which TIANMA guarantees.</p> <p>③Limit sample should be prior to this Inspection standard.</p> <p>④Viewing judgment should be under static pattern.</p> <p>⑤Inspection conditions</p> <p>Inspection distance: 250 mm (from the sample) Temperature : 25±5 °C</p> <p>Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)</p>	
Inspection items	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble	The color of a small area is different from the remainder. The phenomenon doesn't change with voltage
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage
	Polarizer defect	Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass
	Functional defect	no display, display abnormally, open or missing segment, short circuit, False viewing direction
	Glass defect	Glass crack, Shaved corner of glass, Surplus glass
	Segment defect	Pin holes or cracks in segment, Transformation of segment
	PCB defect	Components assembly defect

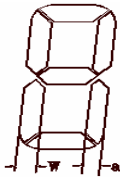
13.4 Outgoing Inspection level

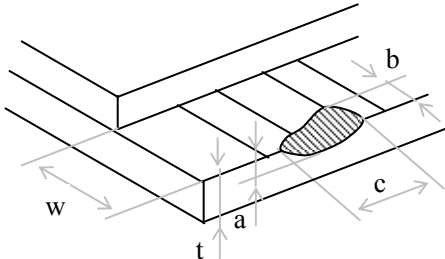
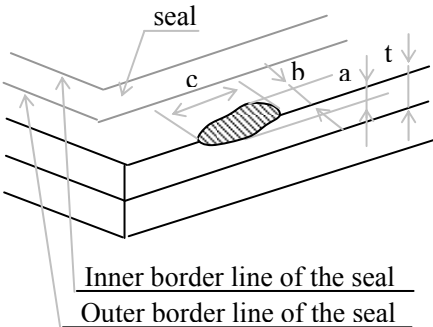
Outgoing Inspection standard	Inspection conditions	Inspection				
		Min.	Max.	Unit	IL	AQL
Major Defects	See 13.3 general notes	See 13.5			II	0.65
Minor Defects	See 13.3 general notes	See 13.5			II	1.5
Note: Sampling standard conforms to GB2828						

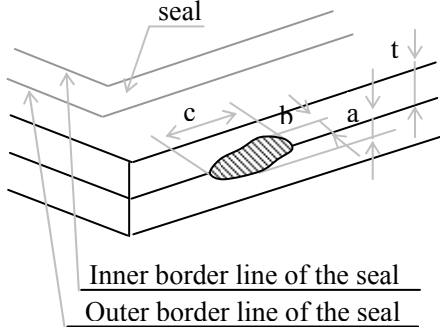
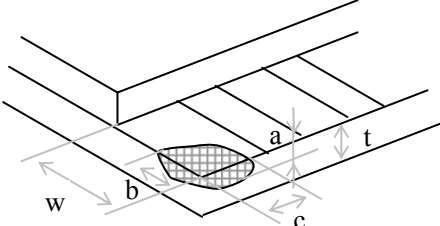
13.5 Inspection Items and Criteria

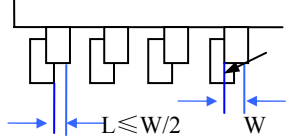
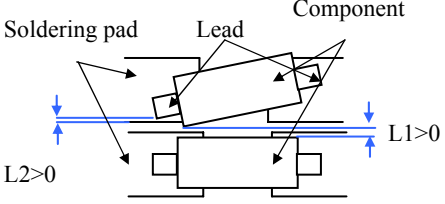
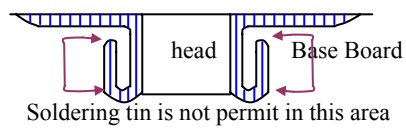
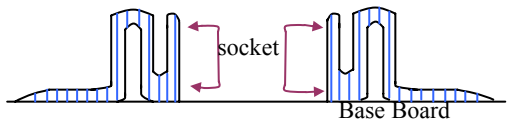
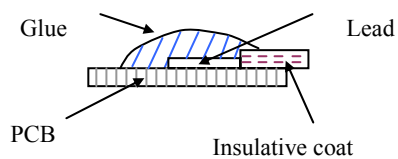
Inspection items			Judgment standard			
			Category		Acceptable number	
					A zone	B zone
1	Black spot, White spot, Bright Spot, Pinhole, Foreign Particle, Particle in or on glass, Scratch on glass		A	$\Phi \leq 0.10$	Neglected	Neglected
			B	$0.10 < \Phi \leq 0.20$	3	
			C	$0.20 < \Phi$	0	
2	Black line, White line, Particle Between Polarizer and glass, Scratch on glass		A	$W \leq 0.02$	Neglected	Neglected
			B	$0.02 < W \leq 0.05$ $L \leq 3.0$	3	
			C	$W > 0.05$ or $L > 3.0$	0	
3	Contrast variation		A	$\Phi \leq 0.2$	Neglected	Neglected
			B	$0.2 < \Phi \leq 0.3$	2	
			C	$0.3 < \Phi \leq 0.4$	1	
			D	$0.4 < \Phi$	0	
			Total defective point(B,C)		3	
4	Bubble inside cell		any size		none	none
5	Polarizer defect (if Polarizer is used)	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.			
		Bubble, dent and convex	A	$\Phi \leq 0.3$	Neglected	Neglected
			B	$0.3 < \Phi \leq 0.7$	2	

			C	$0.7<\Phi$	0	
6	Surplus glass	Stage surplus glass 	$b\leq 0.3\text{mm}$			
		Surrounding surplus glass 	Should not influence outline dimension and assembling.			
7	Open segment or open common		Not permitted			
8	Short circuit		Not permitted			
9	False viewing direction		Not permitted			
10	Contrast ratio uneven		According to the limit specimen			
11	Crosstalk		According to the limit specimen			
12	Black /White spot(display)		Refer to item 1			
13	Black /White line(display)		Refer to item 2			
14	Pin holes and cracks in segment		not counted	Max.3 dots allowed		Max.3 dots allowed
			$x<0.1\text{mm}$	$0.1\text{mm}\leq x\leq 0.2\text{mm}$		
			$x=(a+b)/2$			
			not counted	Max.2 dots allowed each segment		
			$A<0.1\text{mm}$	$0.1\text{mm}\leq A\leq 0.2\text{mm}$ $D<0.25\text{mm}$		
			$x=(a+b)/2$			
15	Transformation of segment		not counted	Max.1 defect allowed each segment		Max.3 defects allowed
			$x<0.1\text{mm}$	$0.1\text{mm}\leq x\leq 0.2\text{mm}$		
			$x=(a+b)/2$			
			not counted	Max.1 defect allowed each segment		
			$a<0.1\text{mm}$	$0.1\text{mm}\leq a\leq 0.2\text{mm}$ $D>0$		
			$x=(a+b)/2$			

			$0.8W \leq a \leq 1.2W$ a=measured value of width W=nominal value of width	Max.2 defects allowed
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Inspection items			Judgment standard		Acceptable number
			Category(application: B zone)		
17	Glass defect crack	①The front of lead terminals	A	$a \leq t, \quad b \leq 1/5W, \quad c \leq 3\text{mm}$	Max.3 defects allowed
			B	Crack at two sides of lead terminals should not cover patterns and alignment mark	
		②Surrounding crack—non-contact side	$b < \text{Inner borderline of the seal}$		
					

		③ Surrounding crack— contact side seal 	$b < \text{Outer borderline of the seal}$	
		④ Corner 	A $a \leq t, \quad b \leq 3.0, \quad c \leq 3.0$	B Glass crack should not cover patterns u and alignment mark and patterns.

Inspection items			Judgment standard
			Category(application: B zone)
18	PCB defect	<p>Component soldering:</p> <p>No cold soldering、short、open circuit、burr、tin ball</p> <p>The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1);</p> <p>the sheet component deviation:</p> <p>Pin deviates from the pad and contact with the near components is not permitted (Pic.2)</p>	<p>Component</p>  <p>Soldering pad Lead Component</p> 
		<p>lead defect:</p> <p>The lead lack must be less than 1/3 of its width;</p> <p>The lead burr must be less than 1/3 of the seam;</p> <p>Impurities connect with the near leads is not permitted</p>	
		<p>Connector soldering:</p> <p>Soldering tin is at contact position of the plug and socket is not permitted</p> <p>No foundation is scald</p> <p>Serious cave distortion on plug and socket contact pin is not permitted</p>	 <p>Soldering tin is not permit in this area</p>  <p>Soldering tin is not permit in this area</p>
		<p>Glue on root of the speaker receiver and motor lead:</p> <p>The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.</p>	 <p>Glue Lead PCB Insulative coat</p>

14 Precautions for Use of LCD Modules

14.1 Handling Precautions

- 14.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 14.1.2 Liquid in LCD is hazardous substance, if the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, thoroughly and promptly wash it off using soap and water.
- 14.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 14.1.4 Don't touch, push or rub the exposed polarizer covering the display surface of the LCD module with anything harder than an HB pencil lead, the polarizer is soft and easily scratched, handle it carefully.
- 14.1.5 Don't put or attach anything on the display area to avoid leaving any marks on.
- 14.1.6 If the display surface is contaminated or becomes dusty, breathe on the surface and gently wipe it with a soft dry cloth. do not scrub hard to avoid damage the surface. If still not completely clear, moisten cloth with one of the following solvents:
- Isopropyl alcohol
 - Ethyl alcohol
- Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
- Water
 - Ketone
 - Aromatic solvents
- 14.1.7 Do not attempt to disassemble the LCD Module.
- 14.1.8 If the logic circuit power is off, do not apply the input signals.
- 14.1.9 Avoid using the same display pattern long time (continuous ON segment).Software must be prepared so that the pattern will be changed
- 14.1.10 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- a. Be sure to ground the body and electric appliances when handling the LCD Modules. It is preferable to use conductive mat on table and wear cotton clothes or conductive processed fibre. Synthetic fibre is not recommended.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be careful and slow when peeling off this protective film since static electricity may be generated. It is recommended to use ionic fan or machine when operating. It is recommended to remove the protection foil slowly (> 3 sec.).
 - e. It is preferable to wear gloves etc, to avoid damaging the LCD. Please do not touch electrodes with bare hands or avoid any other contamination.

14.2 Storage precautions

14.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

14.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $5^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: $\leq 80\%$

14.2.3 The LCD modules should be stored in a clean environment or room, free from acid, alkali and harmful gas.

14.2.4 Store the module in anti-static electricity container and without any physical load.

14.3 Transportation precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

14.4 Soldering

14.4.1 Use the high quality solders, only solder the I/O terminals.

14.4.2 No higher than 280°C and time less than 3-4 second during soldering.

14.4.3 Rewiring: no more than 3 times.

14.4.4 when you remove connector or cable soldered to I/O terminals, please confirm that solder is fully melted. If you remove by force, electrodes at I/O terminals may be damaged (or stripped off). It is recommended to use solder suction machine.

15. LCD Module Part Numbering System

TM	12864	J1	C	C	W	G	W	A
①	②	③	④	⑤	⑥	⑦	⑧	⑨

NO.	Explanation	
①	TIANMA module indicating	
②	Module type: 128 columns X 64 rows	
③	TIANMA module series	
④	LCD type	
	C	positive, FSTN
⑤	Backlight type	
	C	Transflective ,LED
⑥	Temperature range	
	W	Wide temperature
⑦	Technology	
	G	COG
⑧	The color of backlight	
	W	White
⑨	Function choice	
	A	Base function