APV21B - Programmable Register Space

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INTRODUCTION

The APV21B Real-time Video 16X Bicubic Super-resolution core is a soft IP core. It provides fully real-time 16X Bicubic interpolation video super-resolution, and its high performance design allows it to support video output resolutions in excess of 4K 60FPS.

The APV21B is compatibled with the AXI4-Stream Video protocol as described in the **Video IP: AXI Feature Adoption** section of the *Vivado AXI Reference Guide* (Xilinx Inc. UG1037) and **AXI4-Stream Signaling Interface** section of the *AXI4-Stream Video IP and System Design Guide* (Xilinx Inc. UG934).

This document is part of the IP user manual and is intended to describe all the user registers in this IP that can be configured via the bus. Complete technical documentation can be found in the user manual of this IP.

Register Space

This IP is a fully-pipelined process IP, which no register configuration or user intervention is required. This section is not available for this IP.

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