













#3 AND  
#2 LDC  
#1 LD  
#0 NOPD  
#4 ANDC  
MCT#2008 Instruction Set

0 0-7 #21S RD  
0 8-F  
1 0-7  
1 8-F #000 WR  
W ADP I\O

#B DEN  
#A IEN  
#E SKS  
#D RTN  
#C JMP  
#B STO

C REC  
B RBV  
A RB0  
2-PlC  
MEM format  
Op Code I\O Ybl  
1 Code YblZ  
0 YblS YblZ  
YblS 2 2 2 0  
YblS 2 2 2 0  
Rst((Not ImpI))  
#250 Bln Cnt  
#21S Dat Set  
#000 Ybl Lbl  
#000 INV  
#011 INAND  
#001 NOR x#

FF  
nbbrr  
ROM

PIC 2-BV  
EPR0M 2V  
CPN 2-12V  
Power