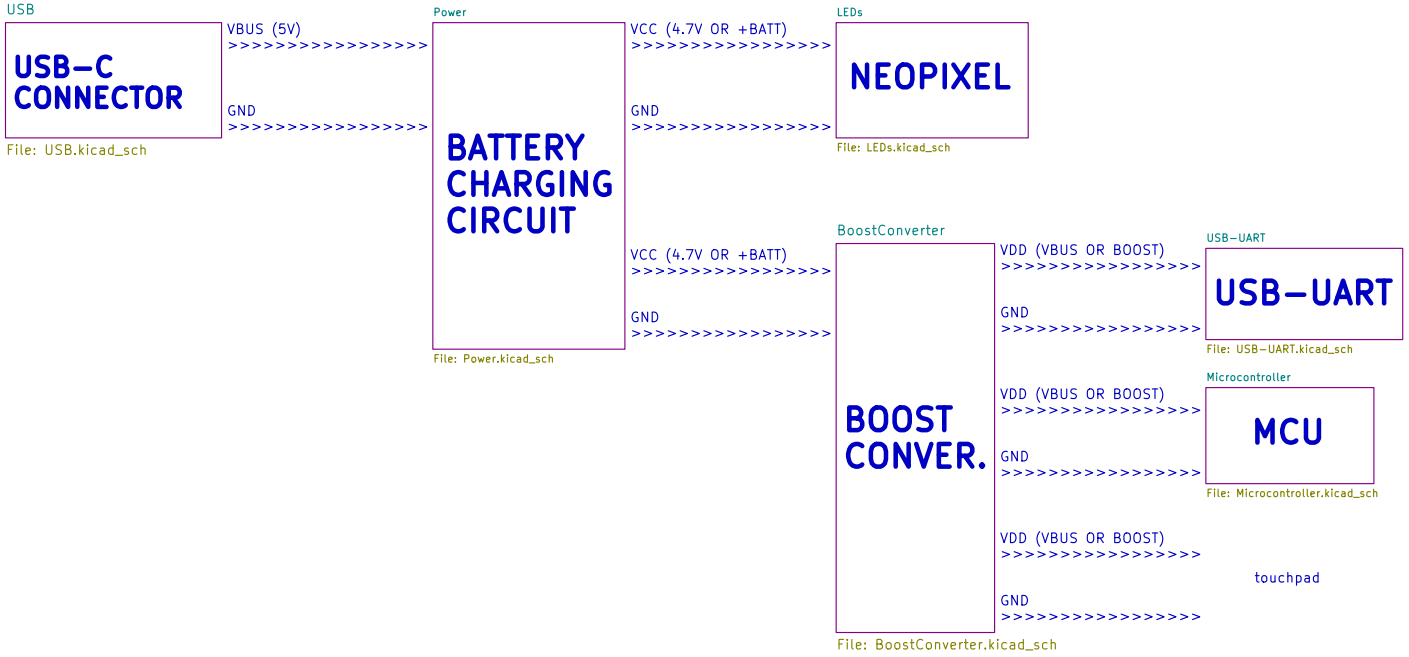
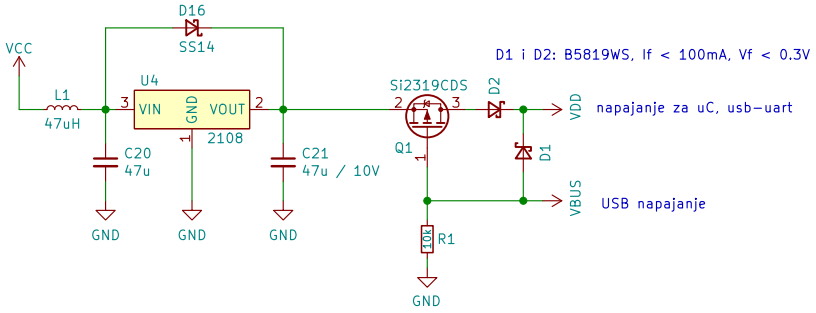
ČOVJEČE NE LJUTI SE (CNLJS)



Josip&Zvonimir		
Sheet: /		
File: CNLJS.kicad_sch		
Title: CNLJS		
Size: A4	Date: 2021–12–22	**Rev: v0.01**
KiCad E.D.A. eeschema (6.0.4)		Id: 1/7



Josip&Zvonimir		
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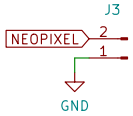
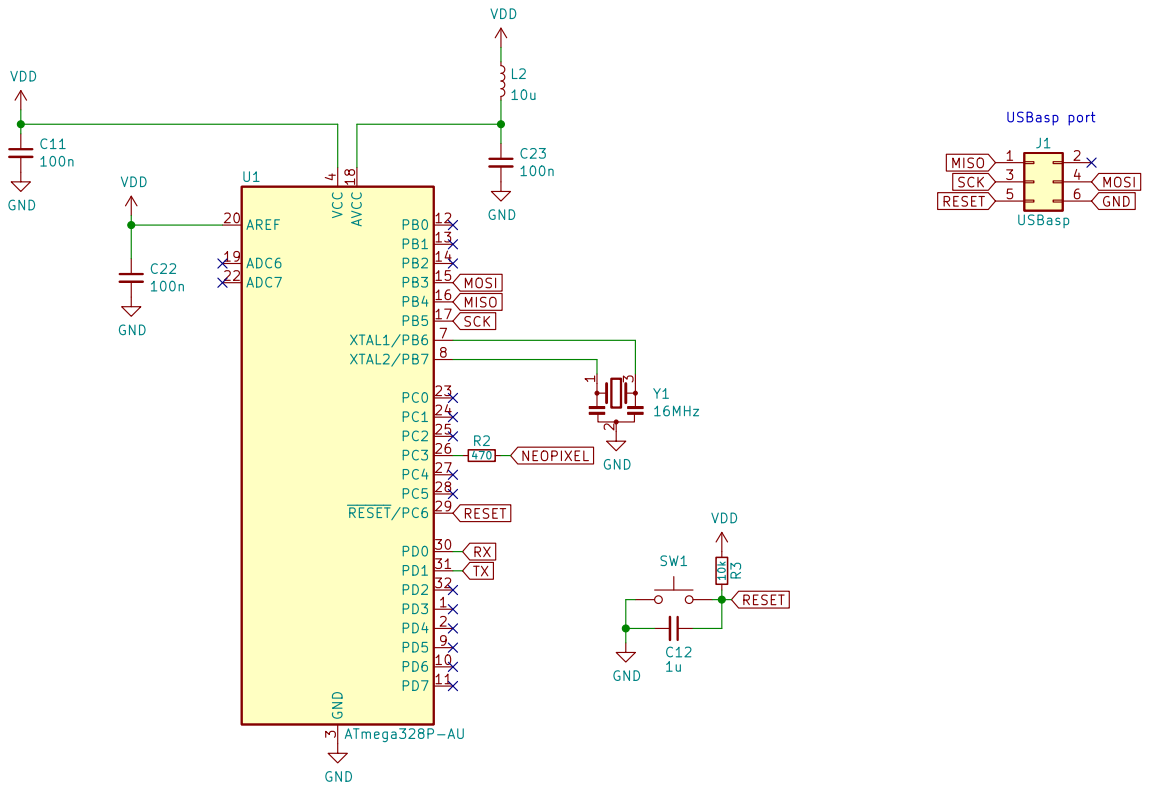
Title: CNLJS		
Size: A4	Date: 2021-12-22	**Rev: v0.01**
KiCad E.D.A. eeschema (6.0.4)		Id: 2/7



WS2812-4020 already include 100nF caps, no need to add them

Josip&Zvonimir		
Sheet: /LEDs/ File: LEDs.kicad_sch		
Title: CNLJS		
Size: A4	Date: 2021-12-22	**Rev: v0.01**
KiCad E.D.A. eeschema (6.0.4)		Id: 3/7



Josip&Zvonimir		
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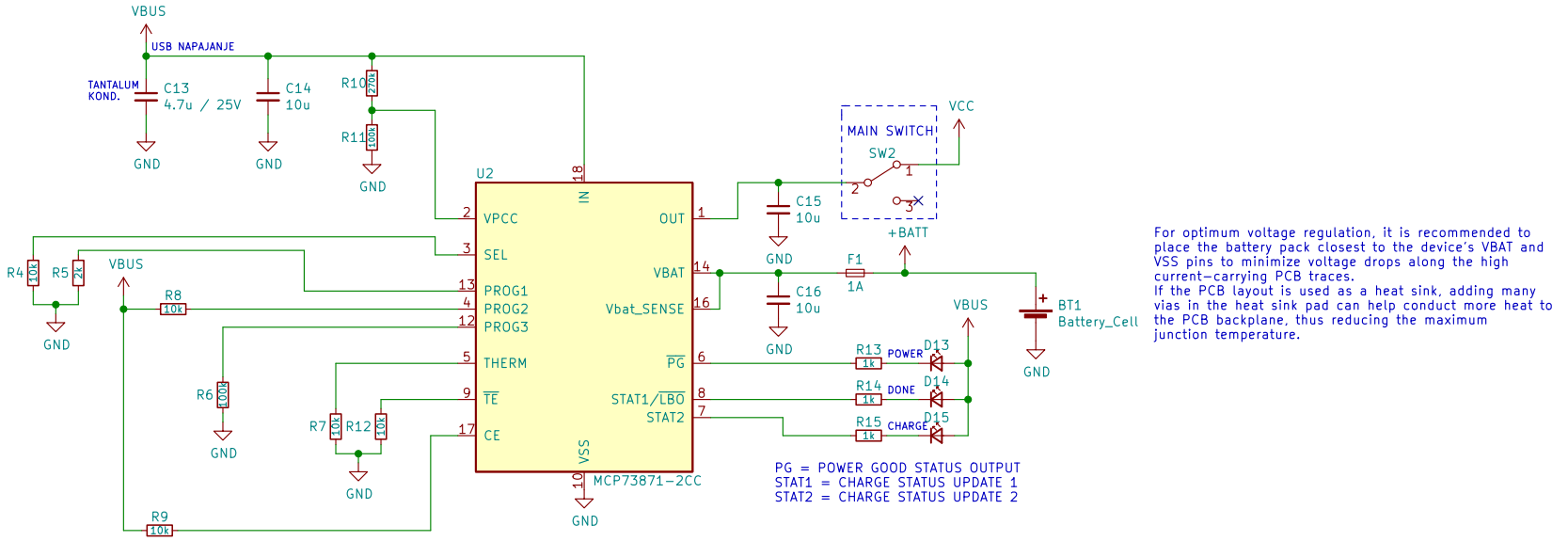
File: Microcontroller.kicad_sch		
Title: CNLJS		
Size: A4	Date: 2021-12-22	**Rev: v0.01**
KiCad E.D.A. eeschema (6.0.4)		Id: 4/7

MCP73871

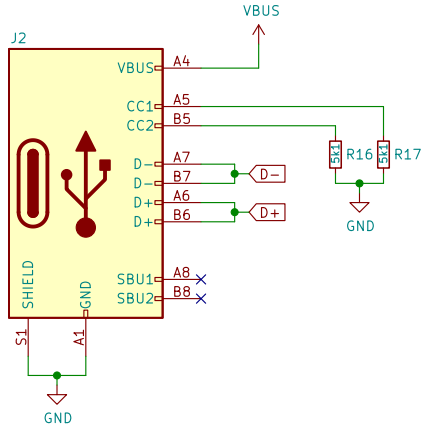


PG = POWER GOOD STATUS OUTPUT
STAT1 = CHARGE STATUS UPDATE 1
STAT2 = CHARGE STATUS UPDATE 2

If temperature monitoring is not required, place a standard 10k resistor from THERM to VSS.

For optimum voltage regulation, it is recommended to place the battery pack closest to the device's VBAT and VSS pins to minimize voltage drops along the high current-carrying PCB traces.
If the PCB layout is used as a heat sink, adding many vias in the heat sink pad can help conduct more heat to the PCB backplane, thus reducing the maximum junction temperature.

Josip&Zvonimir		
Sheet: /Power/		
File: Power.kicad_sch		
Title: CNLJS		
Size: A4	Date: 2021-12-22	**Rev: v0.01**
KiCad E.D.A. eeschema (6.0.4)		Id: 5/7

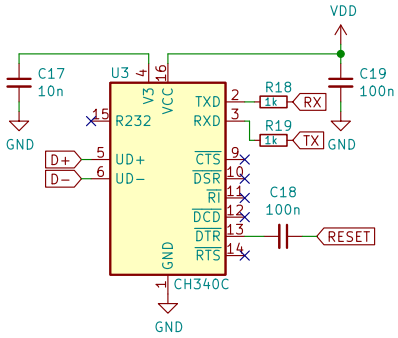


The device must have 5.1k pull-down on the its port CC pin (on both pins).

Once the C-C cable is connected between two link partners, the DFP (host) will sense the drag by 5.1k resistor (from device side). As result, it will turn the VBUS on. This is how a host recognizes that a connection has been made. The connect event is essentially controlled by sink side having 5.1k Rd.

SBU1/SBU2: these are low-speed lines used only for Alternate Mode and accessory mode. For example, with DisplayPort, AUX+/AUX- transmit over the SBU lines. For audio adapter accessory mode, these lines are used for the microphone input and analog GND.

Josip&Zvonimir		
Sheet: /USB/		
File: USB.kicad_sch		
Title: CNLJS		
Size: A4	Date: 2021-12-22	**Rev: v0.01**
KiCad E.D.A. eeschema (6.0.4)		Id: 6/7



Josip&Zvonimir		
Sheet: /USB−UART/		
File: USB−UART.kicad_sch		
Title: CNLJS		
Size: A4	Date: 2021−12−22	**Rev: v0.01**
KiCad E.D.A. eeschema (6.0.4)		Id: 7/7