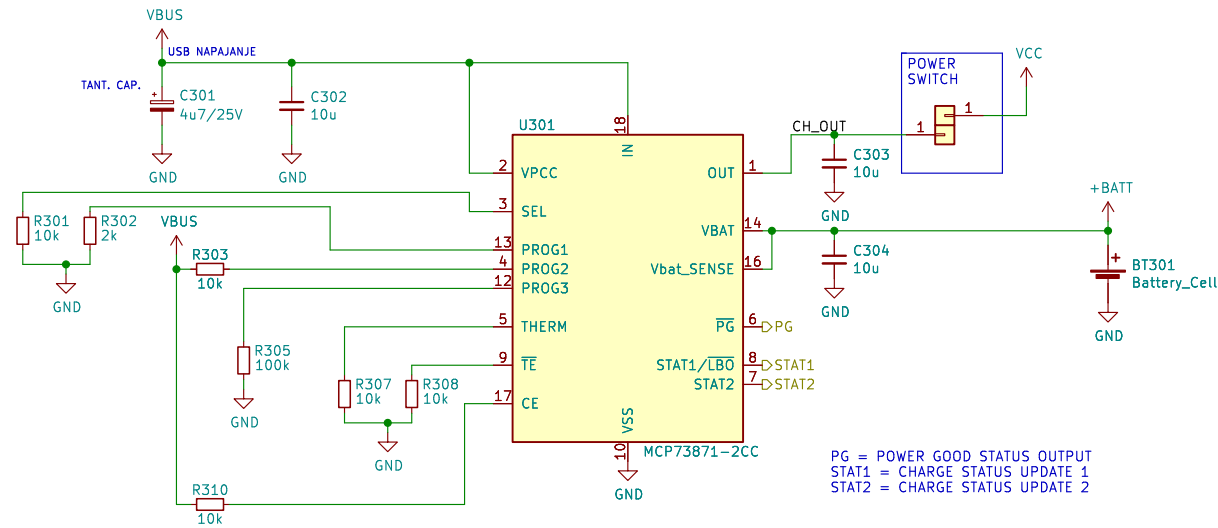


MCP73871



For optimum voltage regulation, it is recommended to place the battery pack closest to the device's VBAT and VSS pins to minimize voltage drops along the high current-carrying PCB traces. If the PCB layout is used as a heat sink, adding many vias in the heat sink pad can help conduct more heat to the PCB backplane, thus reducing the maximum junction temperature.

PG = POWER GOOD STATUS OUTPUT
STAT1 = CHARGE STATUS UPDATE 1
STAT2 = CHARGE STATUS UPDATE 2

If temperature monitoring is not required, place a standard 10k resistor from THERM to VSS.

Sheet: /Battery charger/
File: Battery charger.kicad_sch

Title: BoardGame

Size: A4 Date: 2022-09-21

KiCad E.D.A. 8.0.1

Rev: 1

Id: 3/13