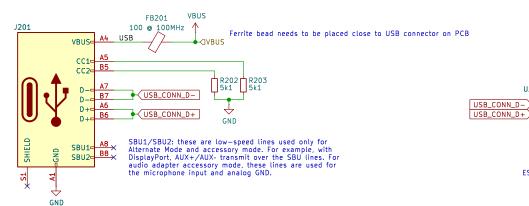


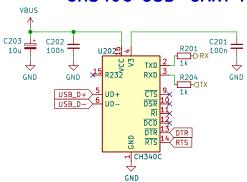
The device must have 5.1k pull-down on the its port CC pin (on both pins).

Once the C-C cable is connected between two link partners, the DFP (host) will sense the drag by 5.1k resistor (from device side). As result, it will turn the VBUS on. This is how a host recognizes that a connection has been made. The connect event is essentially controlled by sink side having 5.1k Rd.



Shield is grounded at the host side (PC) so NC here.

CH340C USB-UART IC



VBUS

GND

ESD protection IC

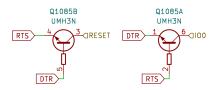
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3 4 4 USB_D-USB_D+

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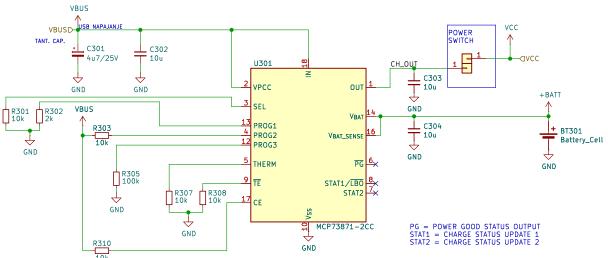
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ESP32 AUTORESET CIRCUIT



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MCP73871



For optimum voltage regulation, it is recommended to place the battery pack closest to the device's VBAT and VSS pins to minimize voltage drops along the high current-carrying PCB traces.

If the PCB layout is used as a heat sink, adding many vias in the heat sink pad can help conduct more heat to the PCB backplane, thus reducing the maximum junction temperature.

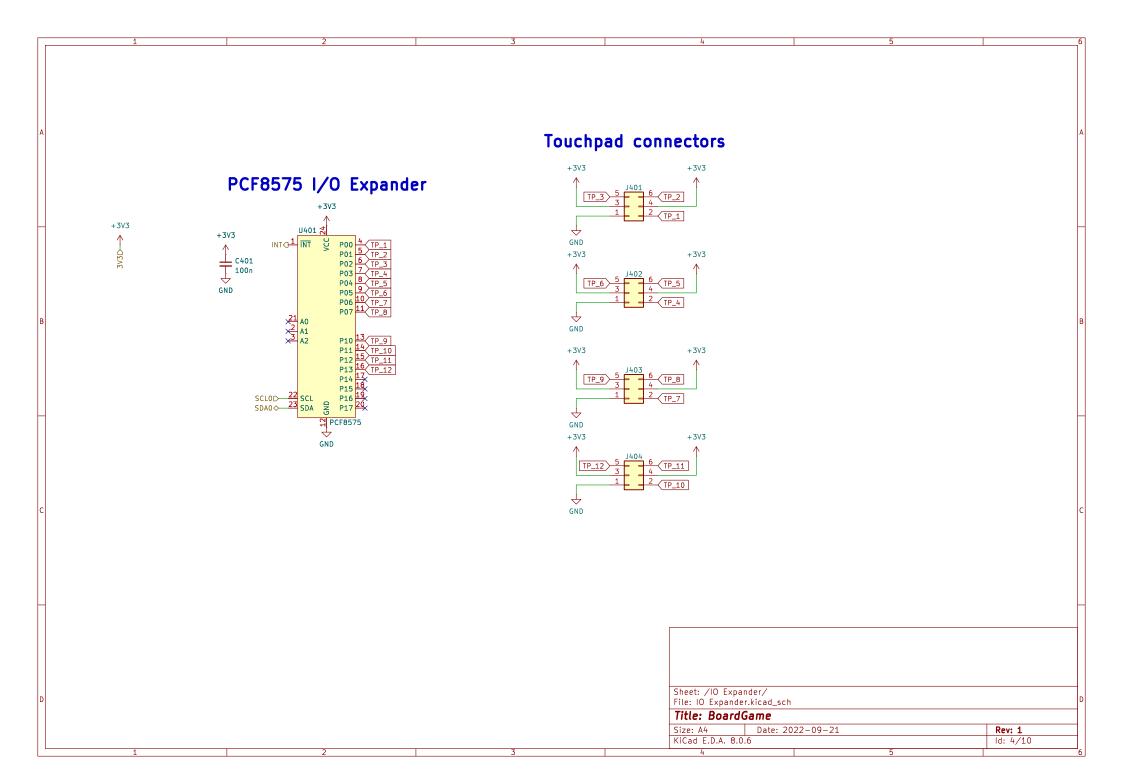
If temperature monitoring is not required, place a standard 10k resistor from THERM to VSS.

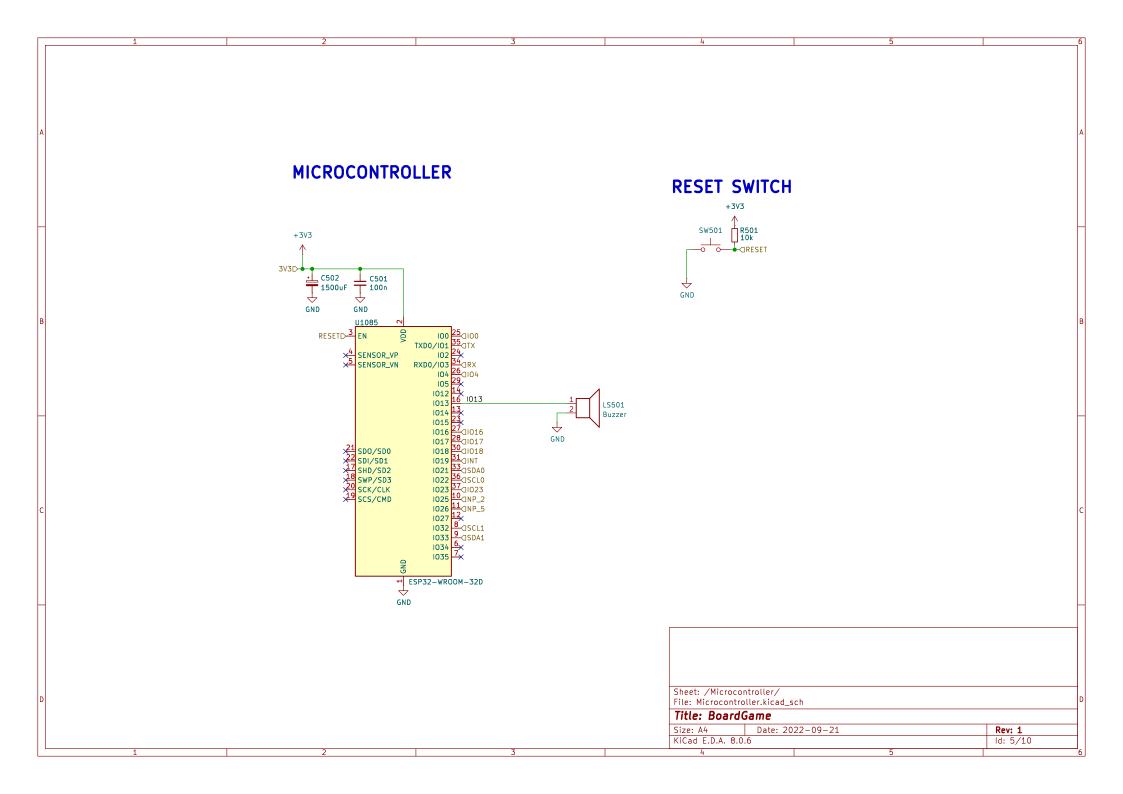
Sheet: /Battery charger/ File: Battery charger.kicad_sch

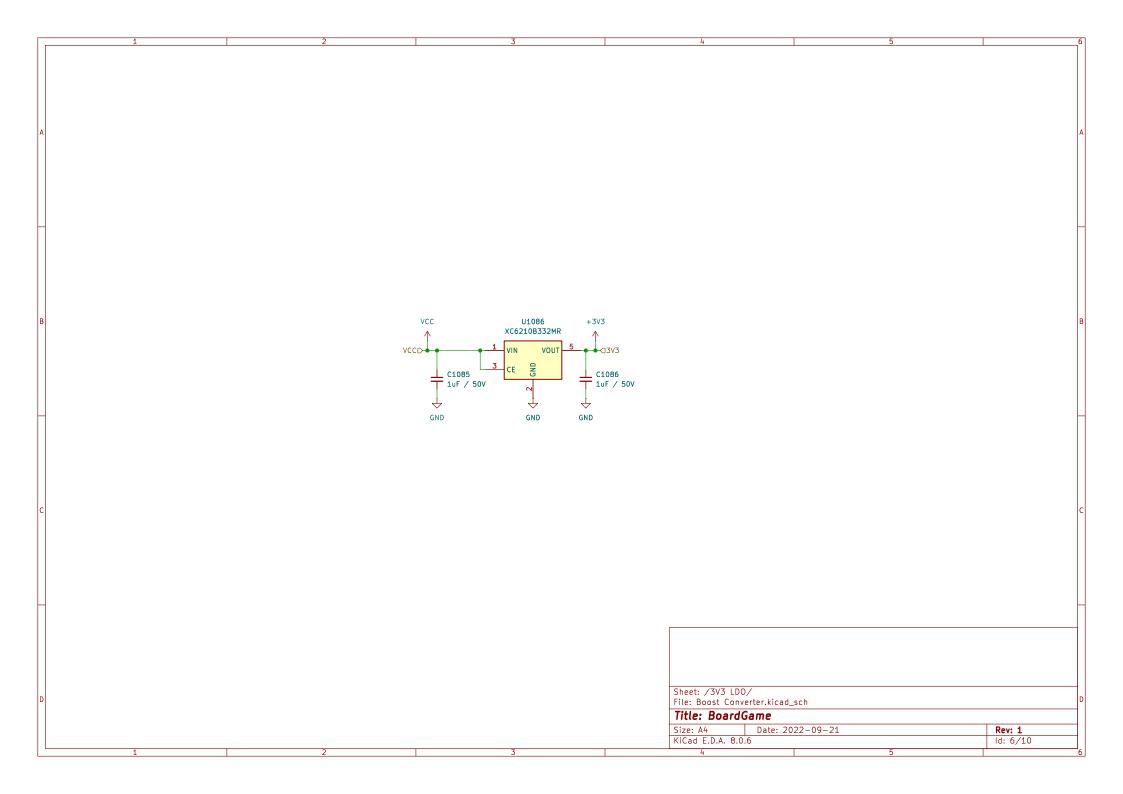
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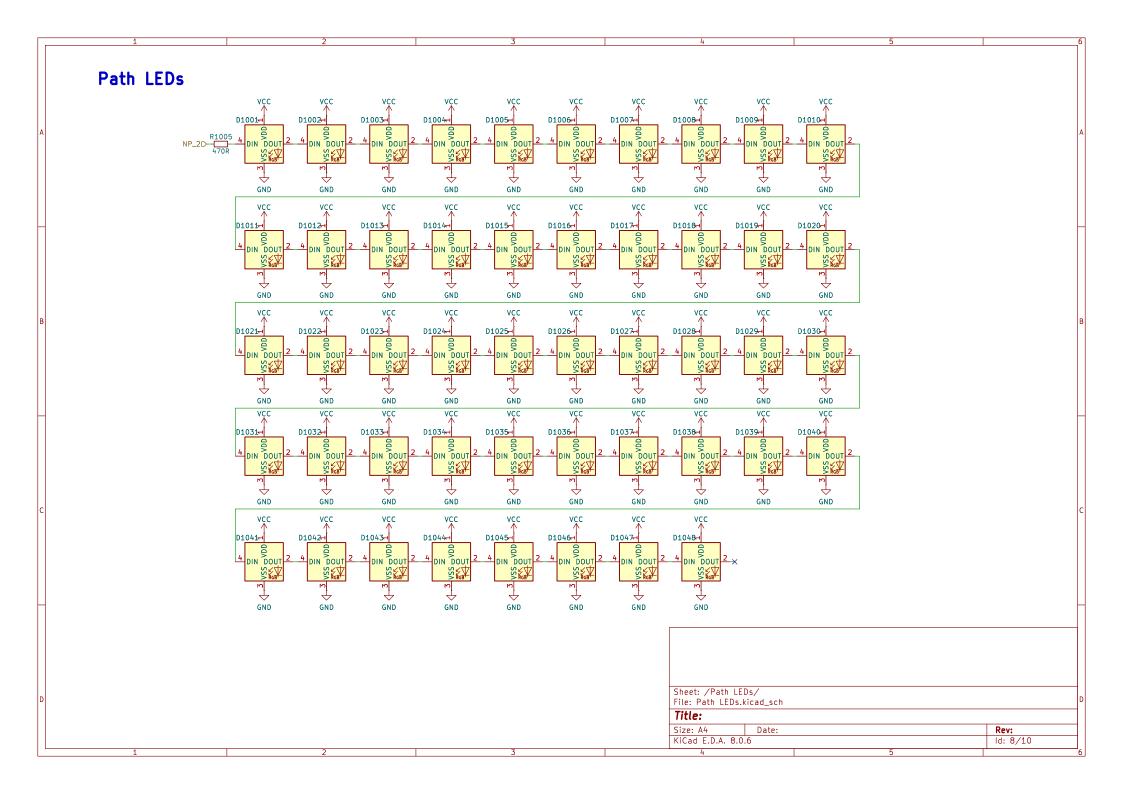
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 Rev: 1

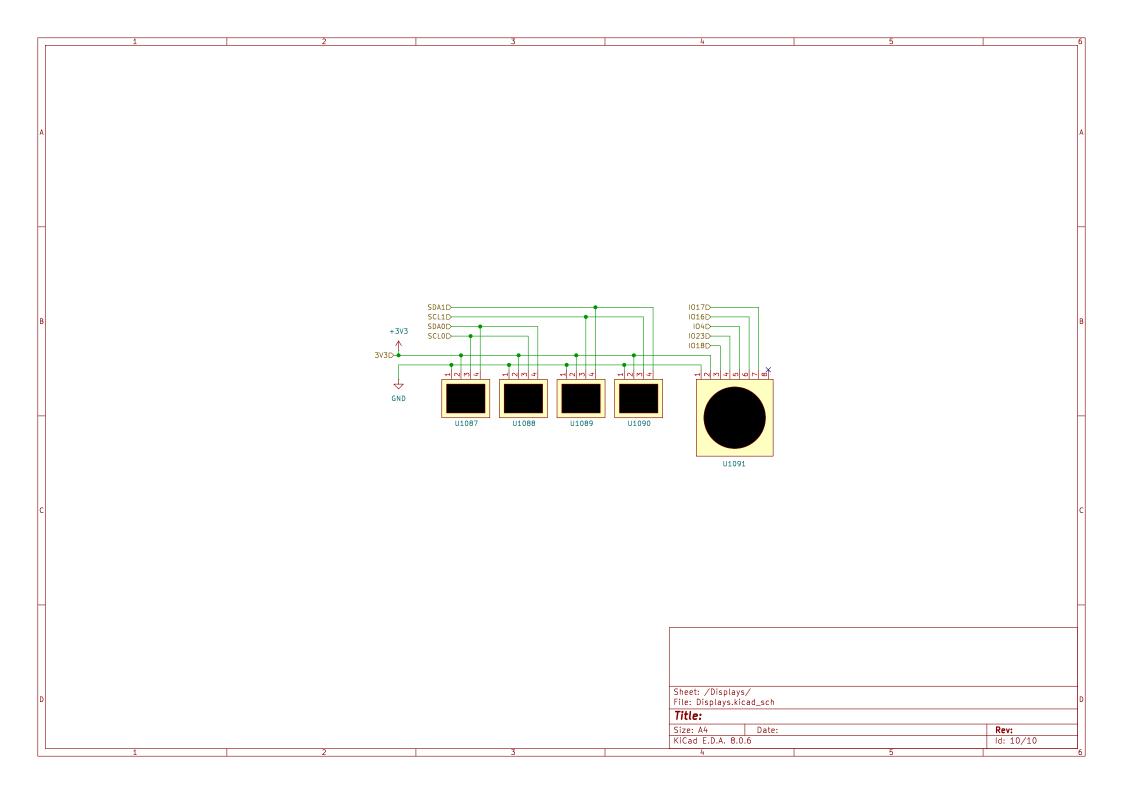
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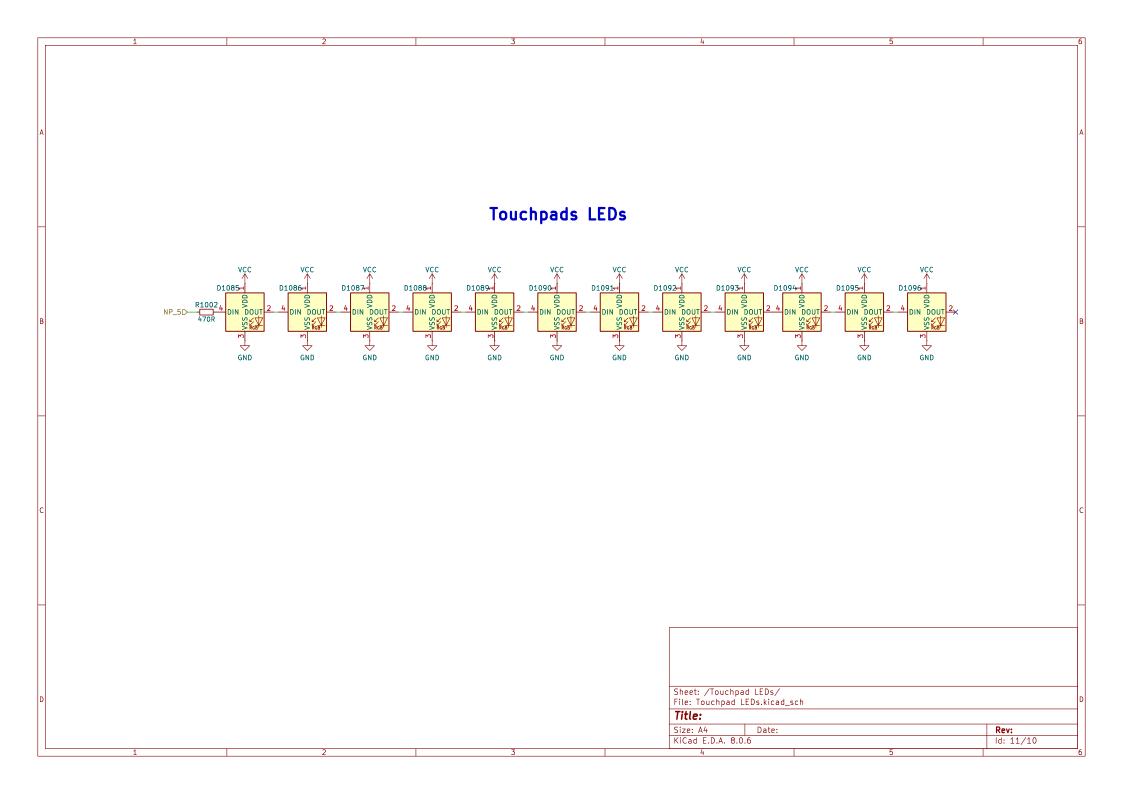












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