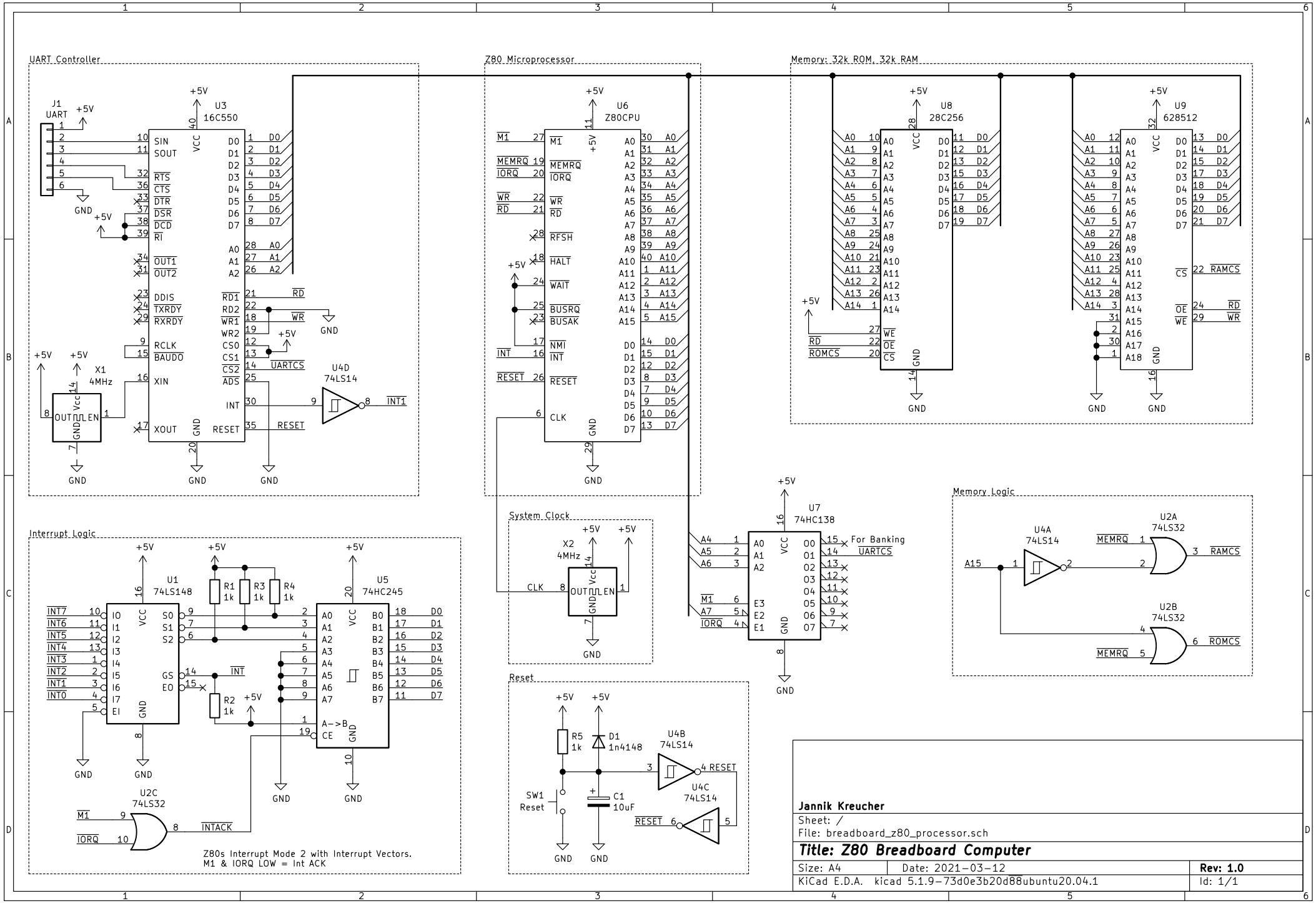
UART Controller
Z80 Microprocessor
Memory: 32k ROM, 32k RAM
J1 UART
U3 16C550
U6 Z80CPU
U8 28C256
U9 628512
X1 4MHz
U4D 74LS14
U7 74HC138
For Banking
UARTCS
System Clock
X2 4MHz
Memory Logic
U4A 74LS14
U2A 74LS32
U2B 74LS32
Interrupt Logic
U1 74LS148
U5 74HC245
R1 1k
R3 1k
R4 1k
R2 1k
U2C 74LS32
INTACK
Z80s Interrupt Mode 2 with Interrupt Vectors.
M1 & IORQ LOW = Int ACK
Reset
R5 1k
D1 1n4148
U4B 74LS14
U4C 74LS14
SW1 Reset
C1 10uF
Jannik Kreucher
Sheet: /
File: breadboard_z80_processor.sch
Title: Z80 Breadboard Computer
Size: A4
Date: 2021-03-12
Rev: 1.0
KiCad E.D.A. kicad 5.1.9-73d0e3b20d88ubuntu20.04.1
Id: 1/1