QA SUPPORT TOOL PROTOTYPE OVERVIEW

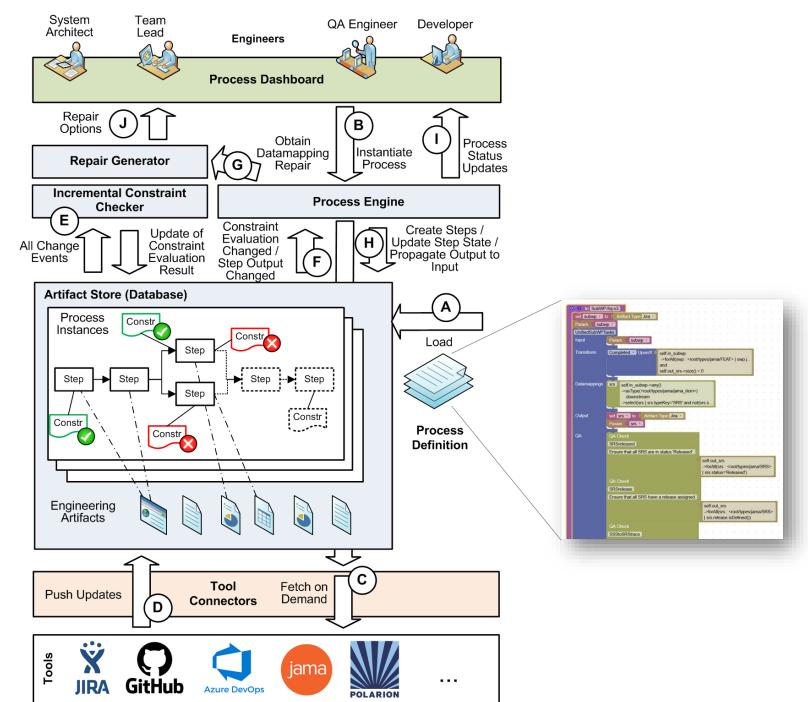


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PROTOTYPE ARCHITECTURE

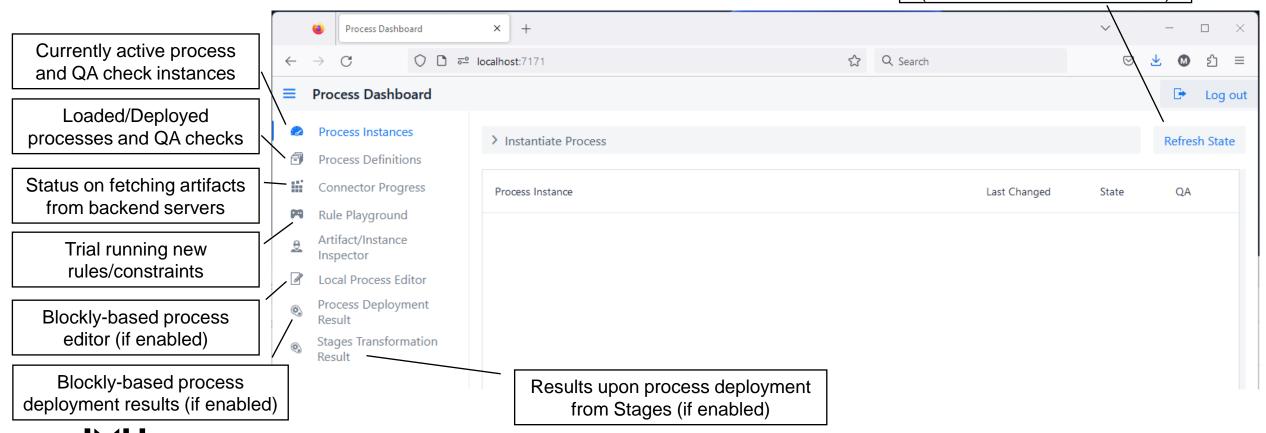




QA TOOL DASHBOARD

■ Navigating to [URL] port 7171, logging in with dev:dev

Forces refreshing of overview – usually UI updates automatically (does not force fetch artifacts!)



LOADED/DEPLOYED PROCESSES (INCL THEIR QA CHECKS)

Process Definitions

Process Definitions

Process Definitions

Process Instances

Connector Progress

Local Process Edito

Process Definitions

SIELA-jira-V3 (DSid: 1215)

✓ SIELA-jira-V3 (DSid: 1215)

✓ SIELA-jira-V3

PrepareProcess

TRefineToSuc

"WriteOrReviseMMF

CreateOrRefineCSC

A (DSid: 1471)

PrepareProcess

->forAll(issue | issue.issueType = 'Story')

Constraint

self.in story

->size() > 0)

Type

Type

jira_core_artifact

jira_core_artifact

jira_core_artifact

jira_core_artifact

-> for All(story : < root/types/jira_core

->select(ref : <root/types/jira_core_s

self.out mmfSubtask->size() > 0

self.out_sucSubtask->size() > 0

self.out_cscSubtask->size() > 0

self.in_story->size() = 1

self.in_story

PRECONDITION

POSTCONDITION

Input Parameters

Output Parameters

cscSubtask mmfSubtask

sucSubtask

Explanation

ProcessTrace Each Process (story) must trace to at

Hierarchical/Tree structure of process steps and any (optional) decision nodes (AND, OR, XOR)

Lists all available (correct)

process definitions*

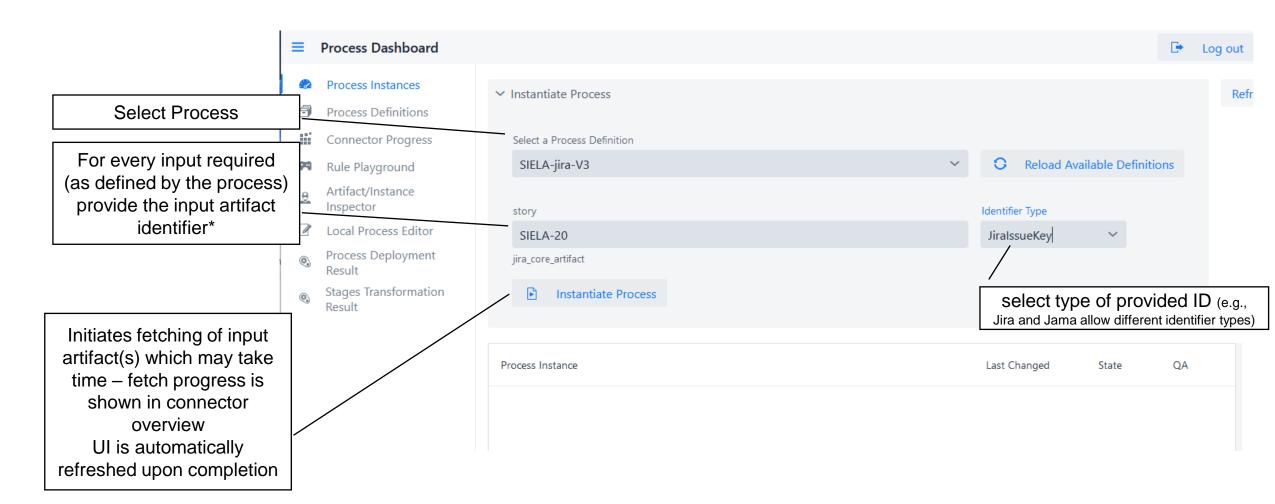
Displays selected process or process step details:

- Including description, pre/post condition (in OCL/ARL language)
- Any QA checks
- Input parameter name and type
- Output parameter name and type

Slider enables for size adjustment of details pane



CREATING A PROCESS INSTANCE





PROCESS INSTANCE OVERVIEW

> Instantiate Process

Root process allows replacement of input artifact by adding new and then removing old artifact

Process Instance ID: 1535

Hierarchical process structure, indicating completion and QA status

Step with collapsed QA status

Step with expanded QA status

Process Instance Last Changed State QA SIELA-jira-V3_[SIELA-20_Integrate Team Centric QA Tracking] \mathbf{A} PrepareProcess 🛱 Each Process (story) must trace to at least one 29/11/2023, 11:31 Stakeholder Request (SR) "| WriteOrReviseMMF 8 RefineToSuc 🛱 Each SUC must trace to at least one (parent) System 29/11/2023, Use Case (SUC) or a Business Use Case (BUC) 11:31 No engineering artifacts other than SUCs may be 29/11/2023, 11:31 traced to. CreateOrRefineCSC

Provides access to how the process and artifact properties are internally stored (e.g., for checking if an artifact update in the tool (e.g., Jama, Jira, etc) has already been received.

A history of the states each step and QA constraint has already reached earlier.

SIELA-jira-V3_[SIELA-20_Integrate Team Centric QA Tracking]

Refresh State

Outputs

Inputs

Process State: In Progress

- suc (jira_core_artifact) none
- buc (jira_core_artifact) none
- csc (jira_core_artifact) none
- mmf (jira_core_artifact) none

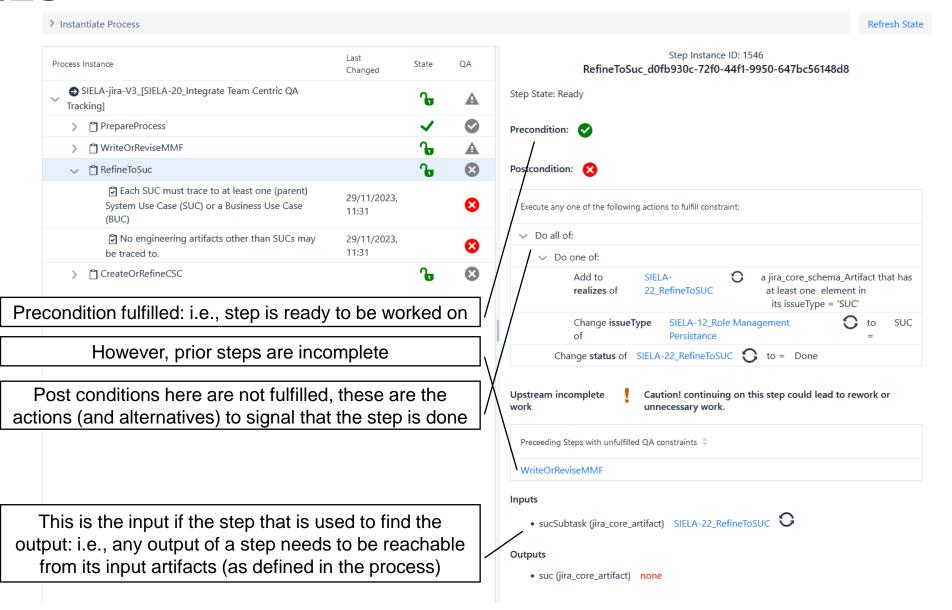


Internal Details (opens in new tab)

JSON Event Log (opens in new tab)



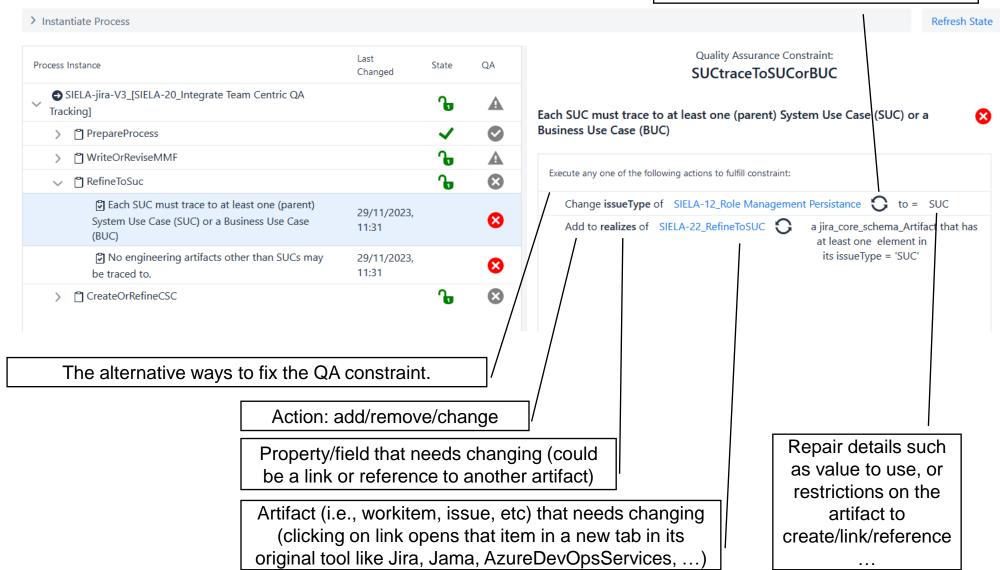
STEP DETAILS





QA CONSTRAINT FEEDBACK

When enabled in the configuration, the clicking this symbol forces an immediate refreshing of this artifact (and only this artifact) from the tools with subsequent constraint reevaluation



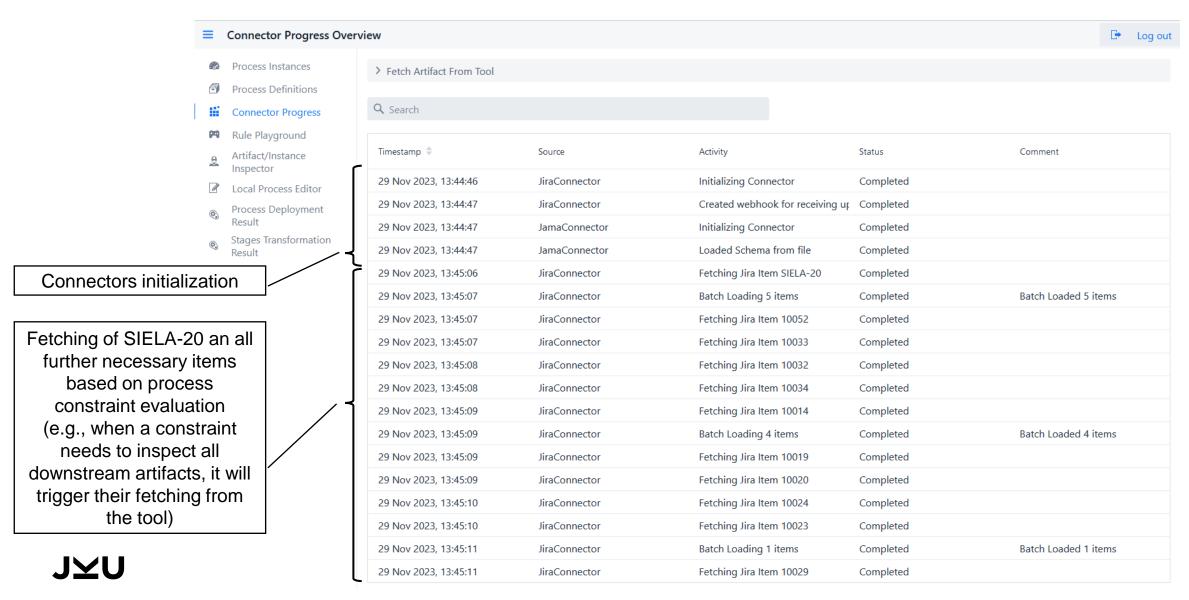


STATUS SYMBOLS

Symbol	Status	Explanation
8	Not Ready	Preconditions are not fulfilled
Ъ	Ready	Preconditions are fulfilled
~	Completed	Postconditions and QA Constraints are fulfilled
\bigcirc	No QA defined	This step does not have any QA constraints
	Step's QA state	This step/process has all/none/some QA constraints fulfilled
	Fulfilled	QA constraint are fulfilled
×	Violated	QA constraint not fulfilled



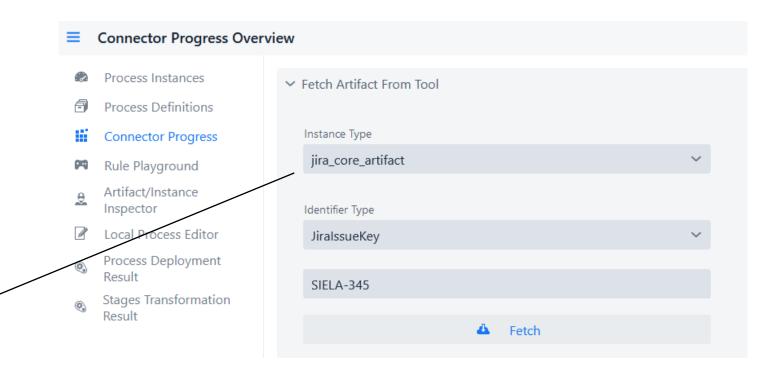
CONNECTOR PROGRESS



FETCHING INDIVIDUAL ARTIFACTS

Fetching of artifact from tool (if not already locally available) by providing the type, identifier type, and id (in the same manner as for providing process input)

Note that relations/links to other artifacts result in lazy loaded artifacts (that need separate fetching) unless these have already been fetched earlier.

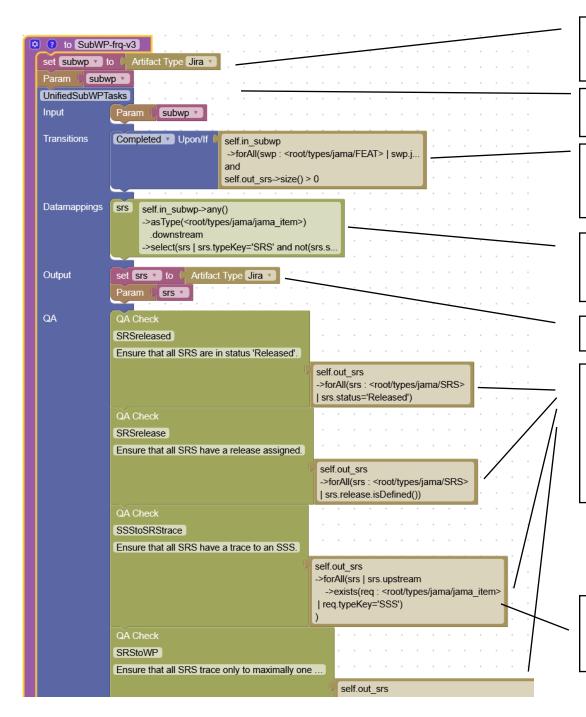




DESIGNING CONSTRAINTS/PROCESSES



PROCESS EDITOR



Process entry point with input definition

Process Step definition with input mapped from process

Exit/End condition
(also start/pre and cancel conditions available)

How one navigates from input to output, i.e., where does output come from

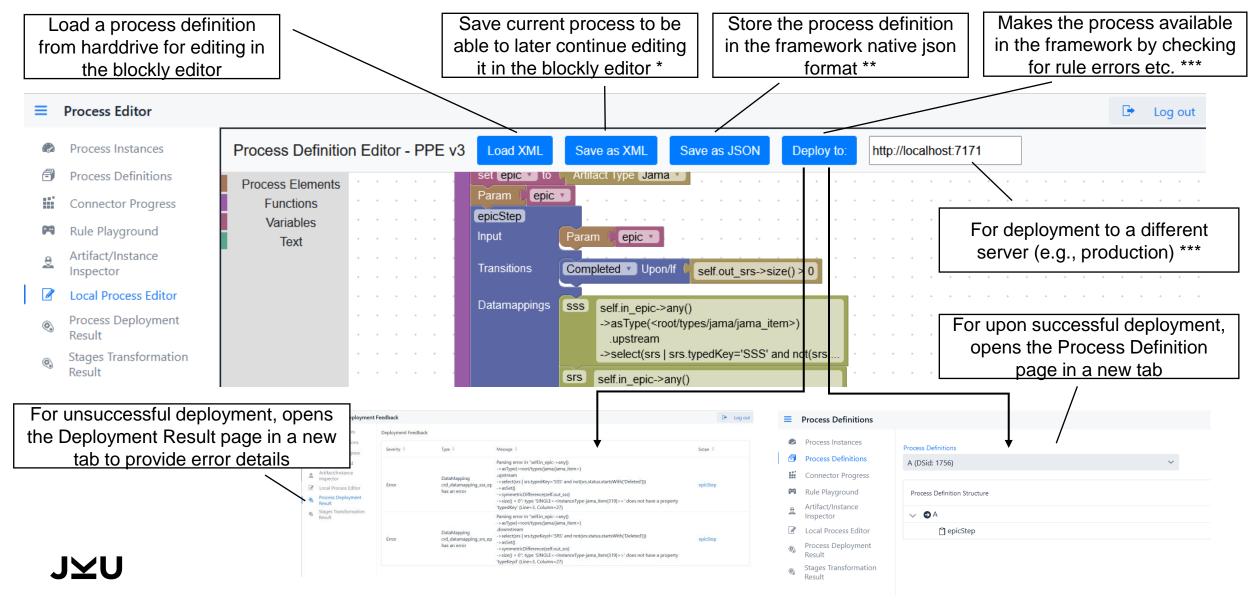
Step output, and type thereof

Any QA constraints, e.g., that input needs to be traced to something else,
Context/scope is always the step, navigating via input or output of the step

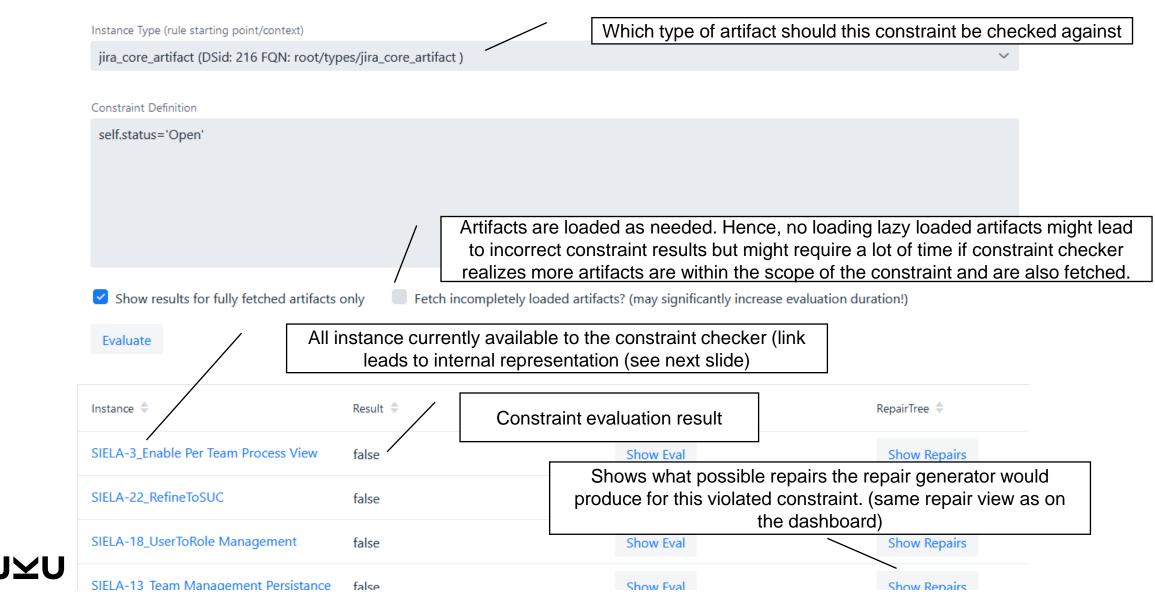
Constraints are best written and tested separately first (see next slide)



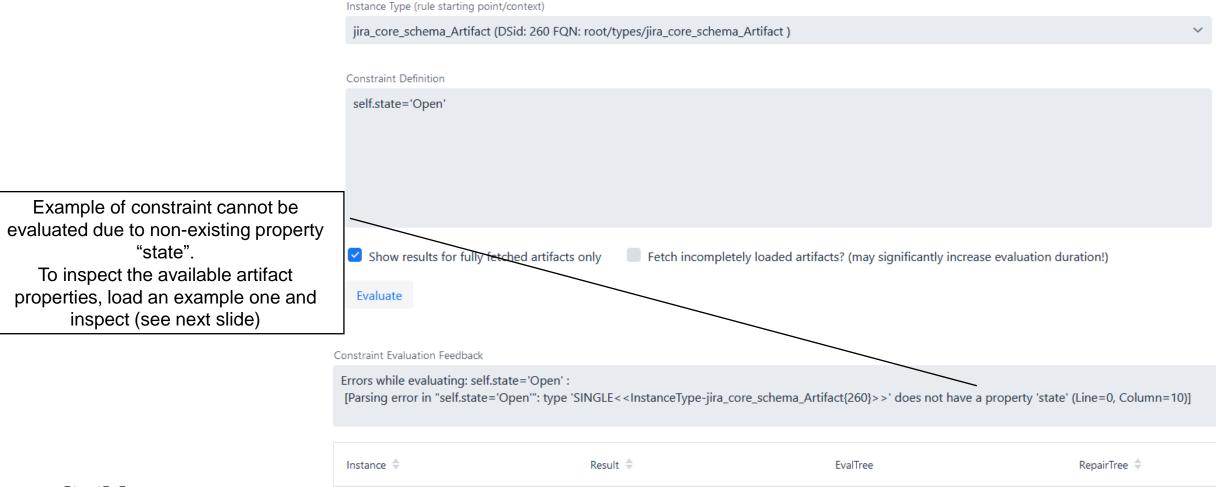
PROCESS DEPLOYMENT



OCL/ARL CONSTRAINT PLAYGROUND

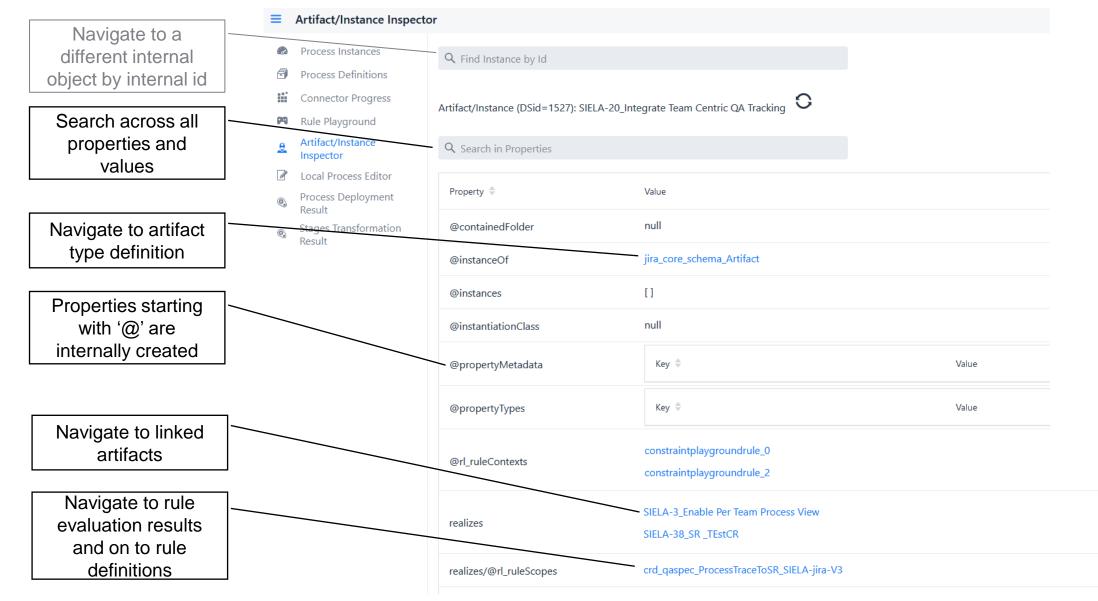


CONSTRAINT WRITING FEEDBACK IN CASE OF SYNTAX ERROR



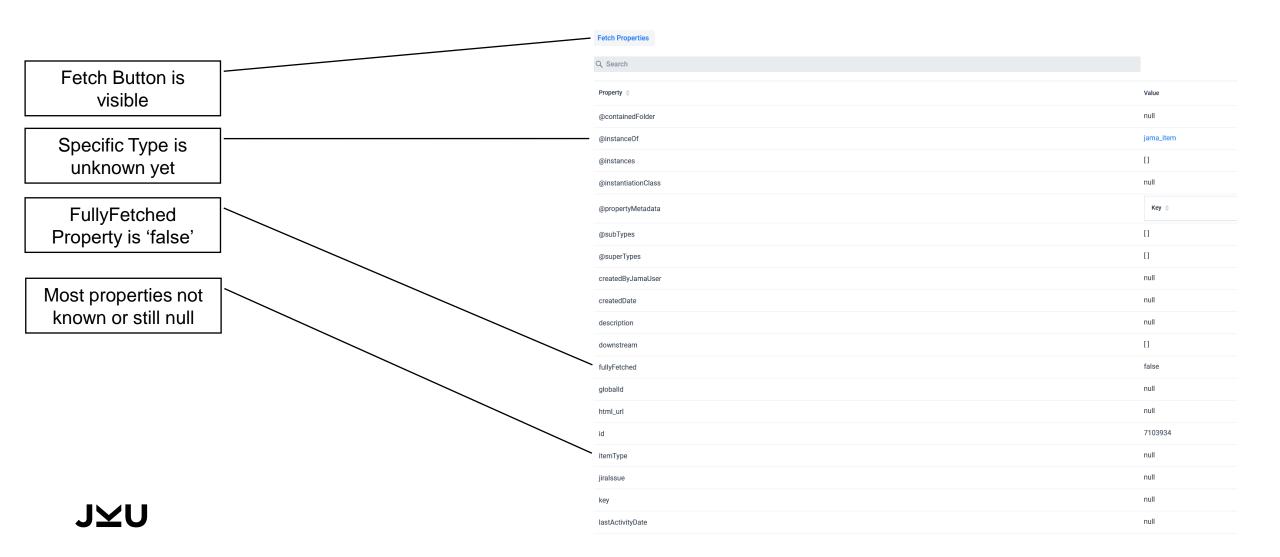


INTERNAL ARTIFACT REPRESENTATION





FETCHING LAZY LOADED ARTIFACTS

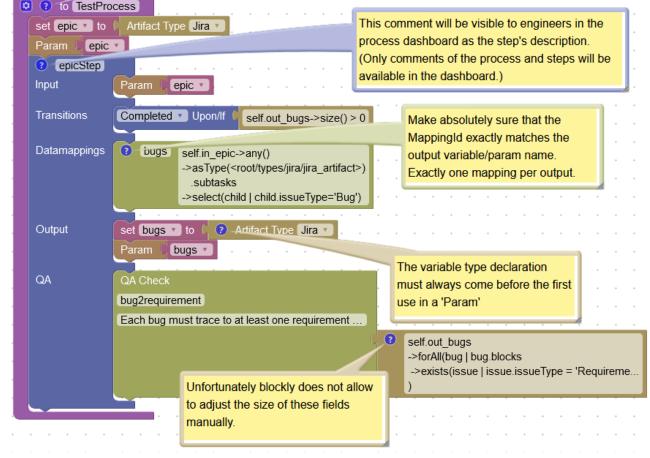


PROCESS DESIGN PROCEDURE

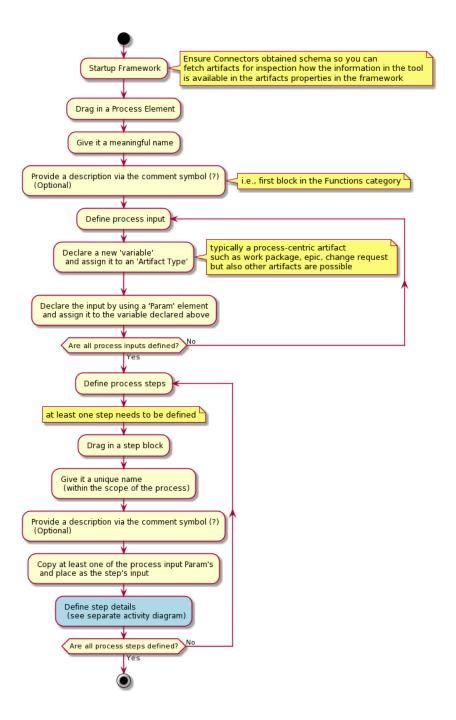


BASIC PROCESS EXAMPLE

This process demonstrates a minimal amount of information for step input, output, transitions, and QA checks.







DEFINING STEP DETAILS

■ Constraints used in example: ☐ Completion/PostConditions: self.out bugs->size() > 0 □ Datamapping: self.in_epic->any()->asType(<root/types/jira/jira_artifact>).subtasks ->select(child | child.issueType='Bug') \square QA: self.out bugs->forAll(bug | bug.blocks->exists(issue | issue.issueType = 'Requirement')) Define step transitions Define step QA constraints (optional) Define step output (optional) At least a 'Completed' (i.e., postconditon) condition must be provided, Drag in a 'QA Check' block and 'Canceled' (i.e., when step can be skipped) are Optional Declare a new 'variable' If the step only changes input artifacts and assign it to an 'Artifact Type' then there is no need to specify any step output Give it a process unique short identifie Drag in a 'Transition' block Declare the output by using a 'Param' element Provide a human readable description what the constraint checks Only one transition of each type may be defined! and assign it to the variable declared above Drag in an 'ARL code' block Drag in an 'ARL code' block The mapping navigates from the steps input, i.e., 'self.in VARNAME' (not a text block!) Declare the 'Datamapping' using OCL/ARL code (not a text block!) by selecting any of it (every input is a list) '->any()->asType(<root/types/jira/jira_core/artifact>)' how the output can be navigated from the input then navigating e.g., to all child artifacts '.children' Use the output variable name as the 'Mappingld' then selecting only a subset thereof, e.g., '->select(child | child type = 'Bug')' Provide the OCL/ARL constraint as text Provide the constraint as OCL/ARL code The 'Enabled' constraint must only use the steps input starting from the steps input or output (where applicable) starting from the steps input or output Are all step outputs defined? Are all required transitions defined? Are all desired QA constraints defined?

