

# Save Design As

Look in:

/research/ece/lnis/...180nm/labs/HDL/GATE



Comput



giacom

1

2

File name:

mips\_mapped.v

Open

Files of type:

Database Files ( \*.ddc \*.ddc.gz \*.db \*.db.gz \*.gdb \*.sdb \*.pd )

Cancel

Format:

VERILOG (v)

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☒ Save all designs in hierarchy

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SYNOPSYS