



Navigator

Schematic
nmos_carac

OBJECTS

- All
- Instances 1
- Nets
- Pins
- Nets and Pins

GROUPS

- Cells
- Types

Navigator

Property Editor

instance (1) All

| | |
|-------------------|---------------------------------|
| Model name | nch |
| description | 1.8V nominal VT NMOS transistor |
| I (M) | 180n M |
| w (M) | 2u M |
| total_width(M) | 2u M |
| Number of Fi... | 1 |
| Multiplier | 1 |
| total_m | 1 |
| Hard_constrain | true |
| S D swap | false |
| Calc Diff Para... | true |
| Source_area | 9.6e-13 |
| Drain_area | 9.6e-13 |
| Source_perip... | 4.96u M |
| Drain_periphe... | 4.96u M |
| NRS | 0.135 |
| NRD | 0.135 |

Property editor

Main menus and tools

Schematic display area

