

# Simulation Properties

Simulation Configuration Name

rtl

Place in Folder

Top Level

Add Folder...

Design

VHDL

Verilog

Libraries

SDF

Others

Name

Type

Path

+	verilog	Library	\$MODEL_TECH/./v
+	vh_ux01v_lib	Library	\$MODEL_TECH/./v
+	vhldopt_lib	Library	\$MODEL_TECH/./v
+	vital2000	Library	\$MODEL_TECH/./v
+	work	Library	xor2_rtl
-	xor2_rtl	Library	/research/ece/inis/US
	M _opt	Optimize...	
	M xor2	Module	/research/ece/inis/US
	M xor2 tb	Module	/research/ece/inis/US

Design Unit(s)

xor2\_rtl.xor2\_tb

Resolution

default

Optimization

☒ Enable optimization

Optimization Options

Save

Cancel