• Report Constraints	
Report for	
Current design: mips	
Carrette design. Thips	
Report options	
☑ Split <u>l</u> ine	∇erbose
☐ Show only <u>m</u> aximum capacitance	☐ Show only minimum <u>c</u> apacitance
☐ Show only maximum dela <u>y</u>	☐ Show only minimum delay
☐ Show only maximum leakage power	□ Show only minimum porosity
☐ Show only ma <u>x</u> imum dynamic power	□ Show only maximum transition
☐ Show only maximum fano <u>u</u> t	□ Show only maximum area
☐ Show only cell degradation	□ Show only connection class
☐ Show only multiport nets	☐ Show only cr <u>i</u> tical range
☐ Show only maximum total powe <u>r</u>	
Show only maximum toggle rate	☐ Show only minimum pulse wi <u>d</u> th
✓ Show all <u>v</u> iolators	Signi <u>f</u> icant digits: 2
Scenarios	
Output options	
▼ To report viewer	
▼ To file: RPT/mips_mapped_allviol.rpt Browse	
3 ☐ Append to file	
	4 OK Cancel Apply