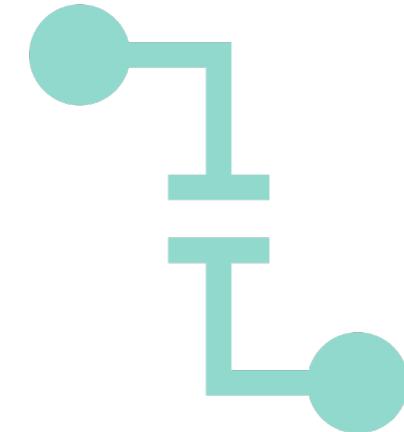
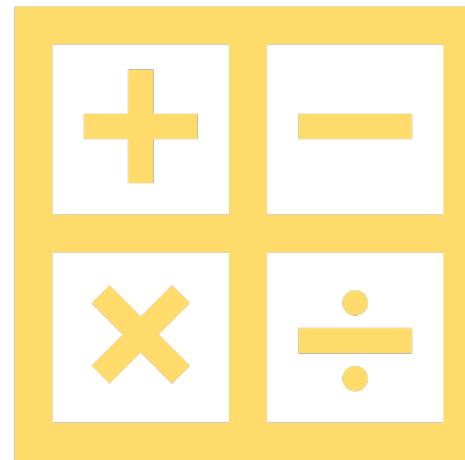
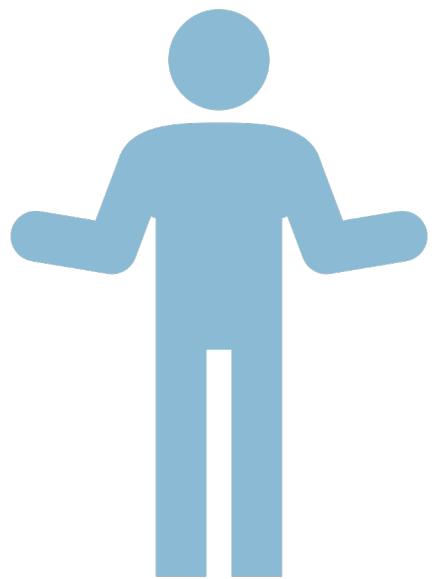
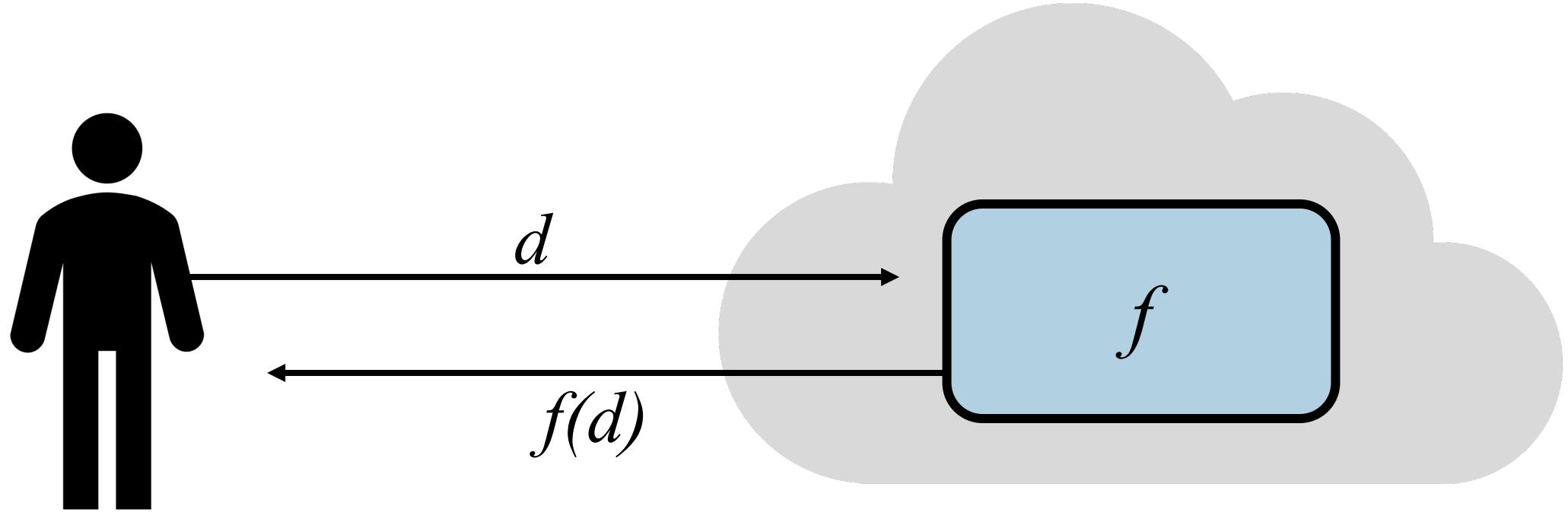


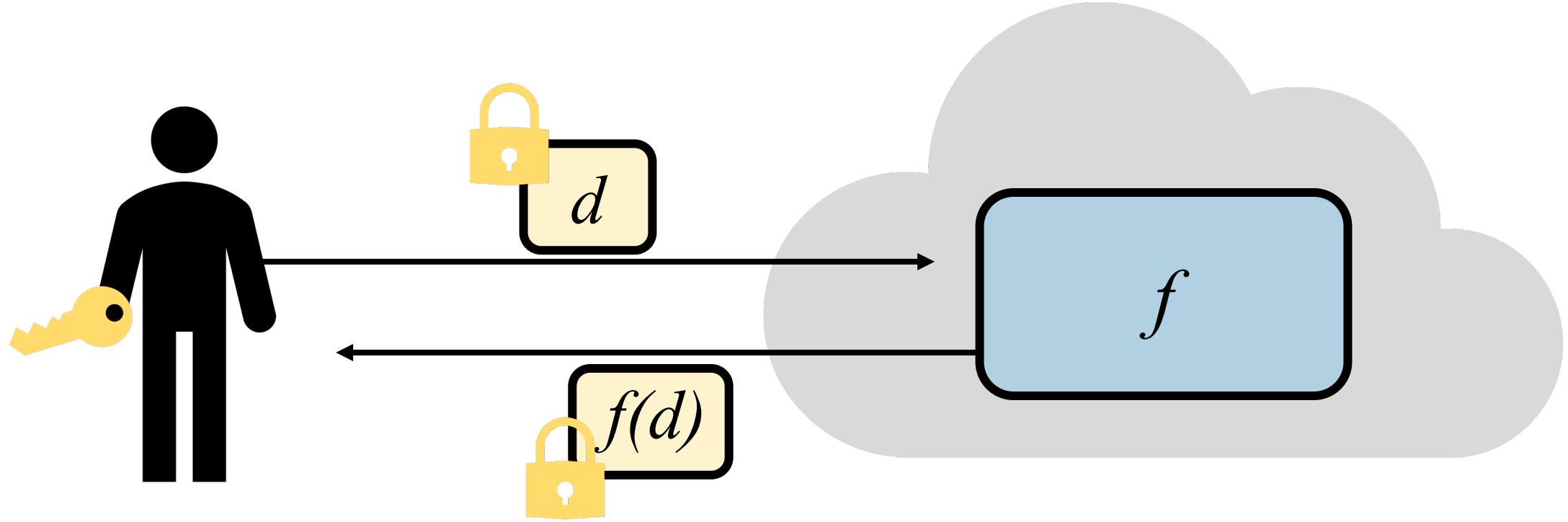
PARALLELIZATION OF FULLY HOMOMORPHIC DATA ENCODING

JESS WOODS

COMPUTER SCIENCE RESEARCH



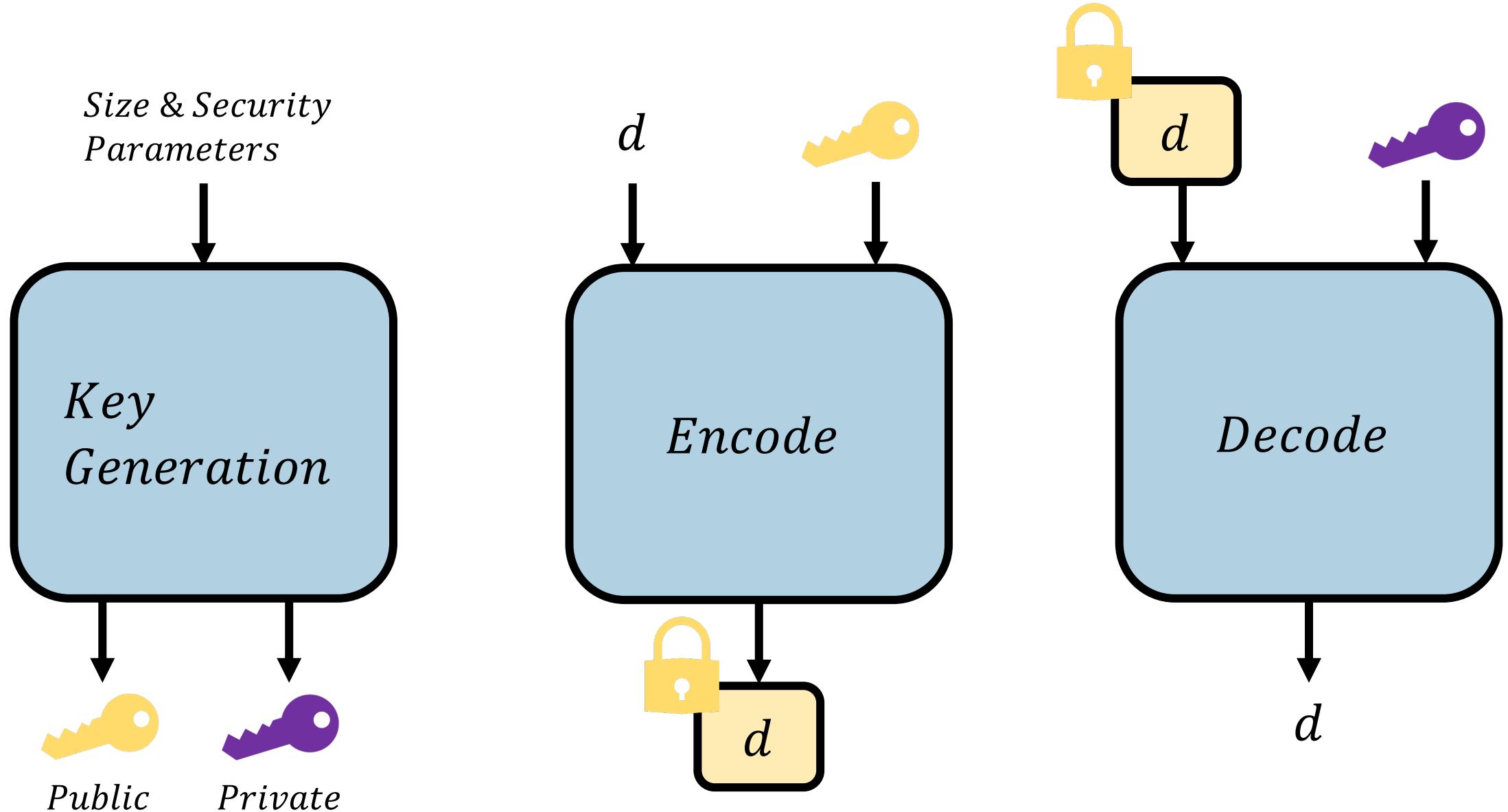


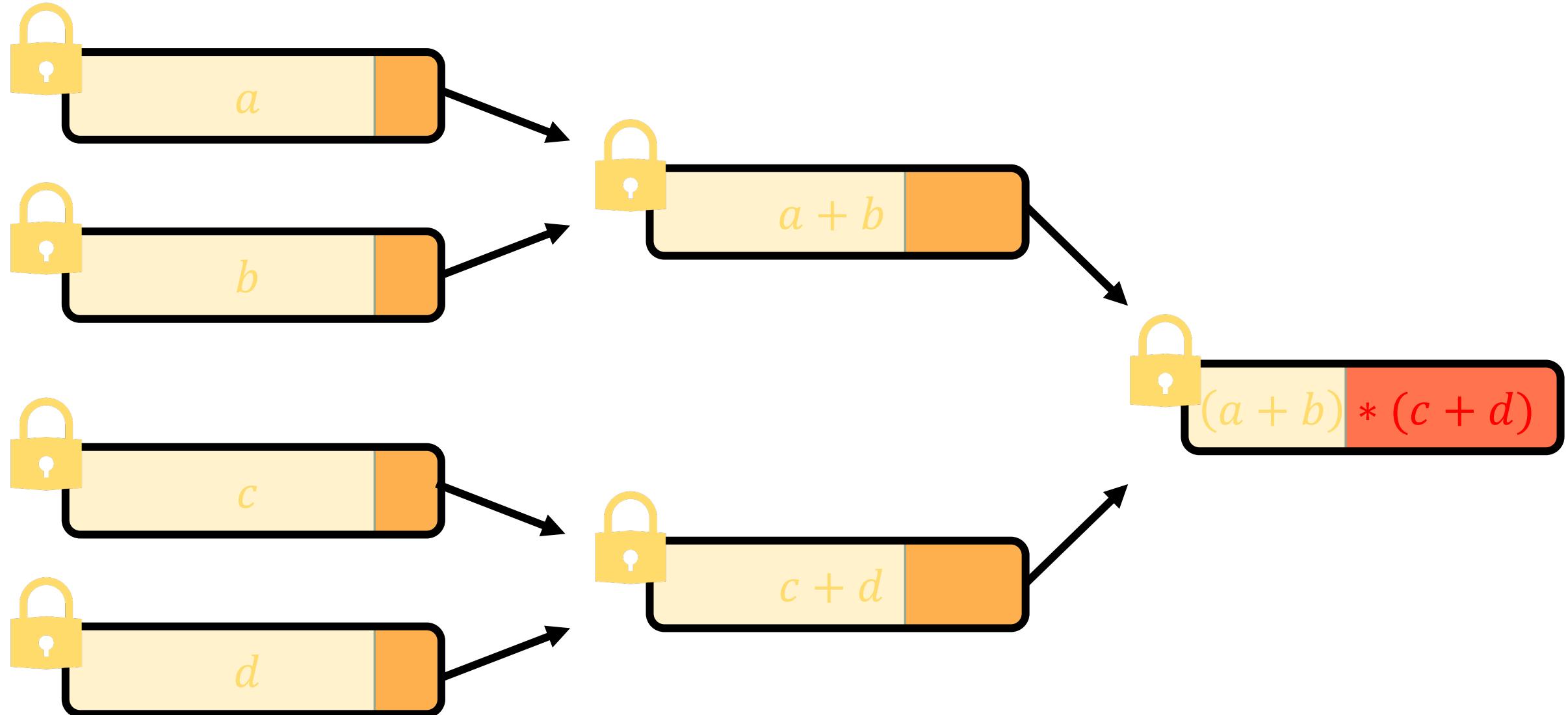


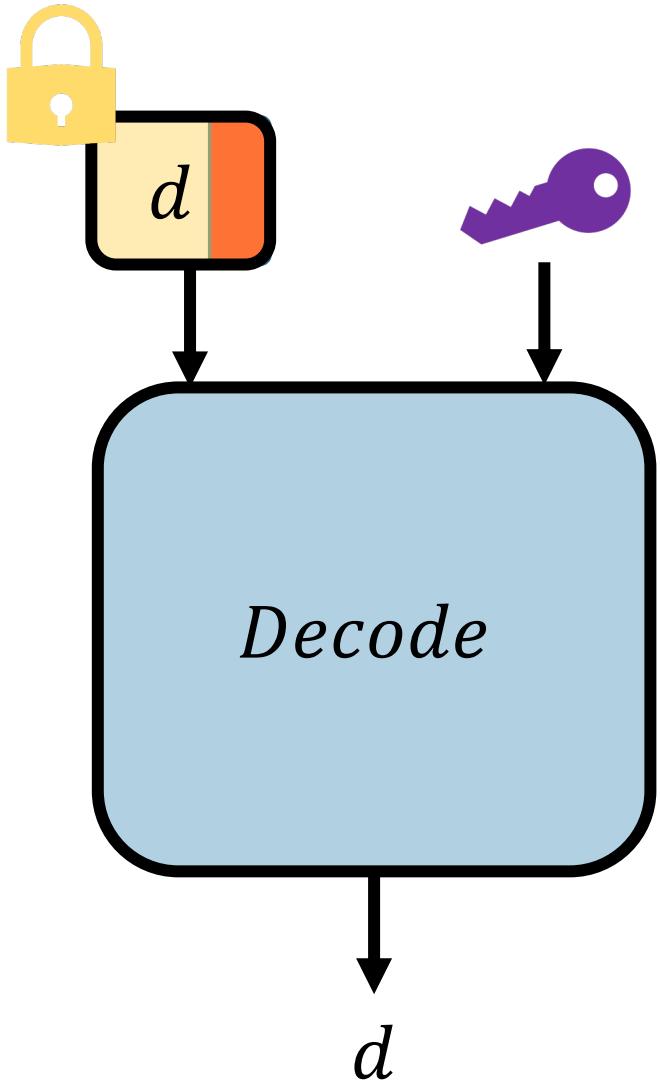
$$\begin{array}{c} \text{Yellow lock} \\ \text{---} \\ \boxed{d_1} \end{array} + \begin{array}{c} \text{Yellow lock} \\ \text{---} \\ \boxed{d_2} \end{array} = \begin{array}{c} \text{Yellow lock} \\ \text{---} \\ \boxed{d_1 + d_2} \end{array}$$

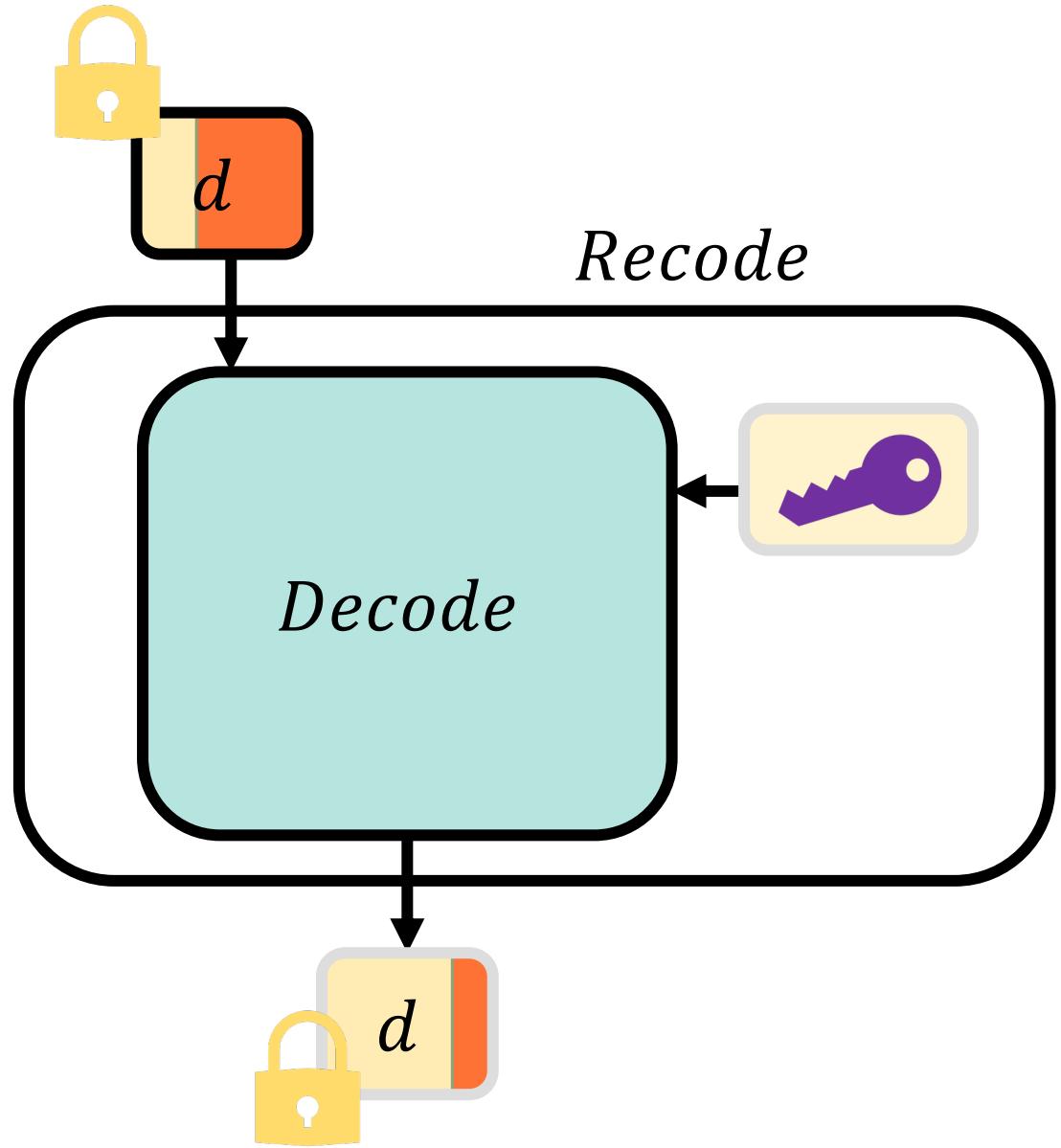
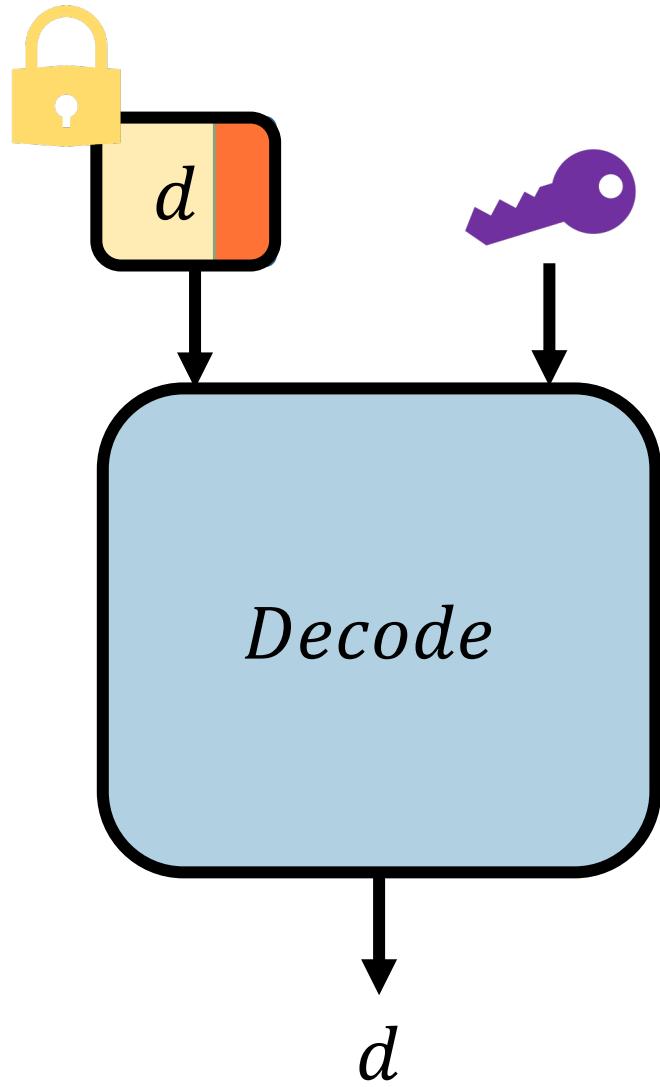
$$\begin{array}{c} \text{Yellow lock} \\ \text{---} \\ \boxed{d_1} \end{array} \cdot \begin{array}{c} \text{Yellow lock} \\ \text{---} \\ \boxed{d_2} \end{array} = \begin{array}{c} \text{Yellow lock} \\ \text{---} \\ \boxed{d_1 \cdot d_2} \end{array}$$

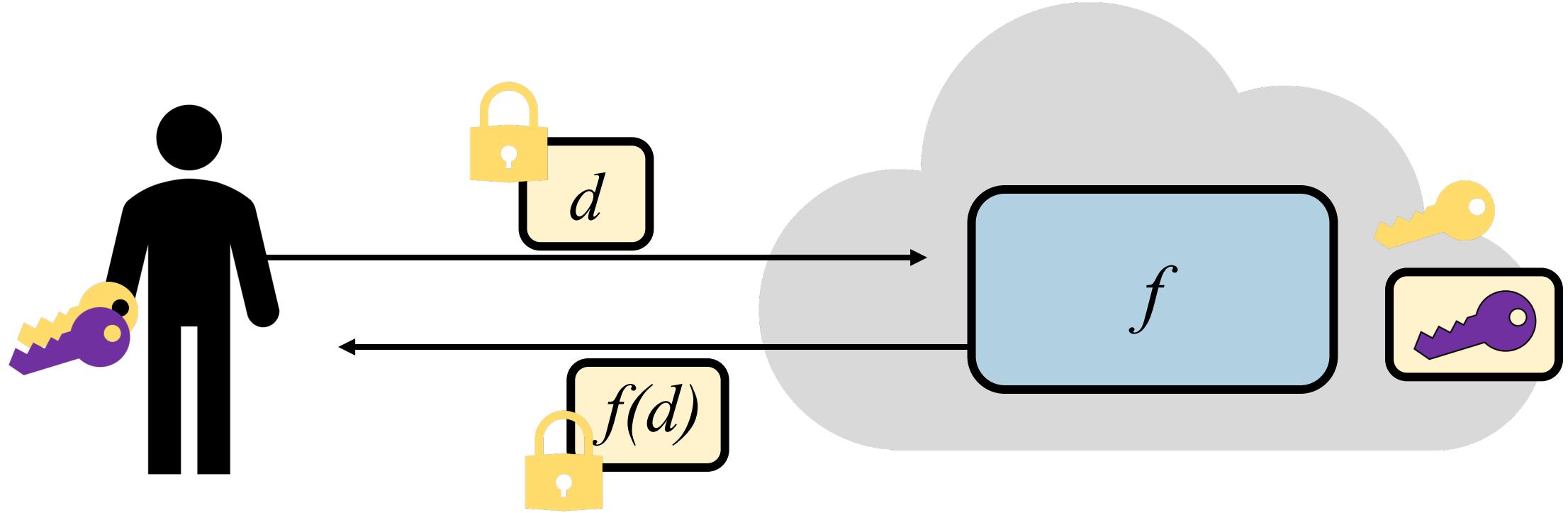
THE SCHEME

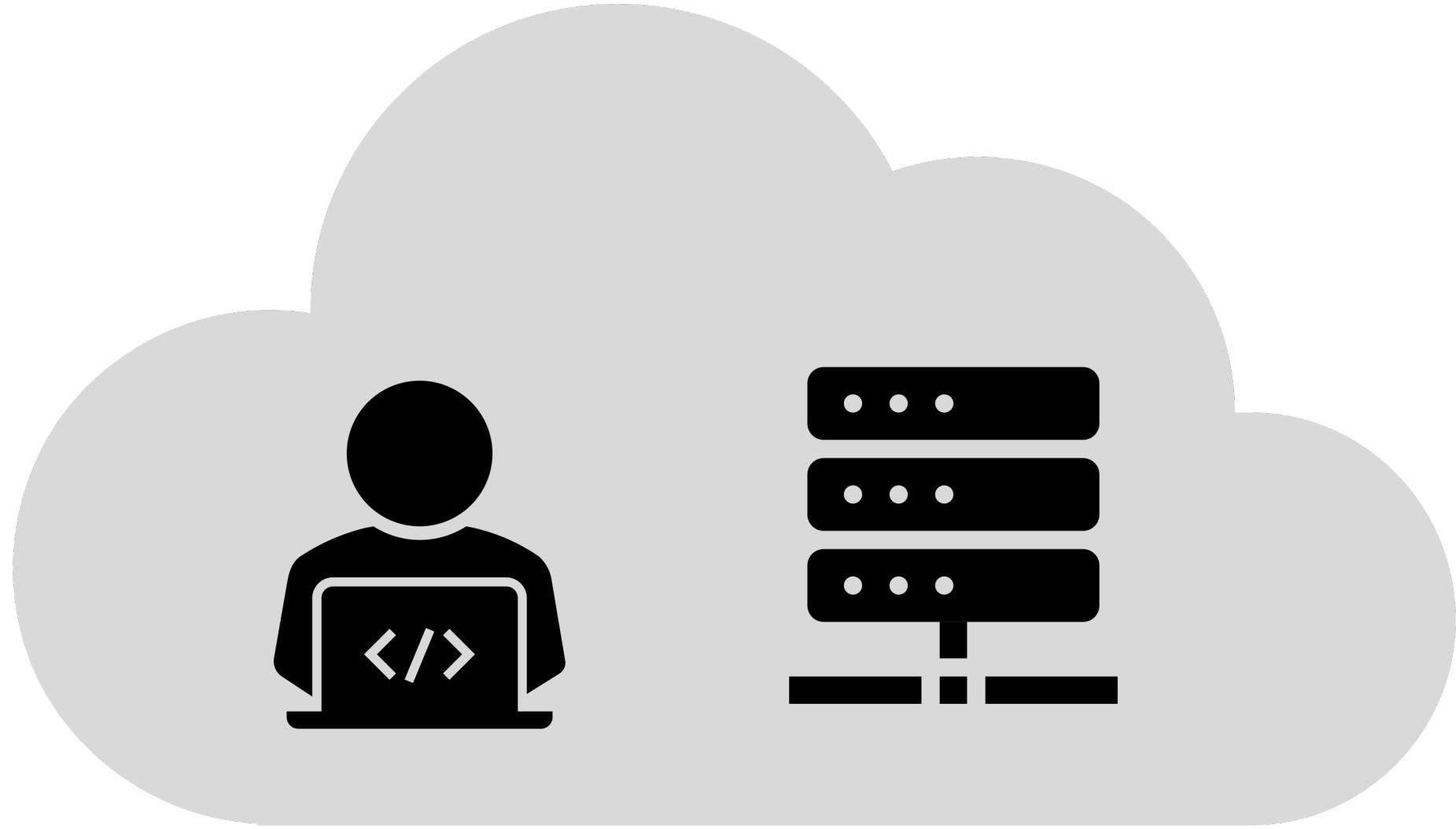




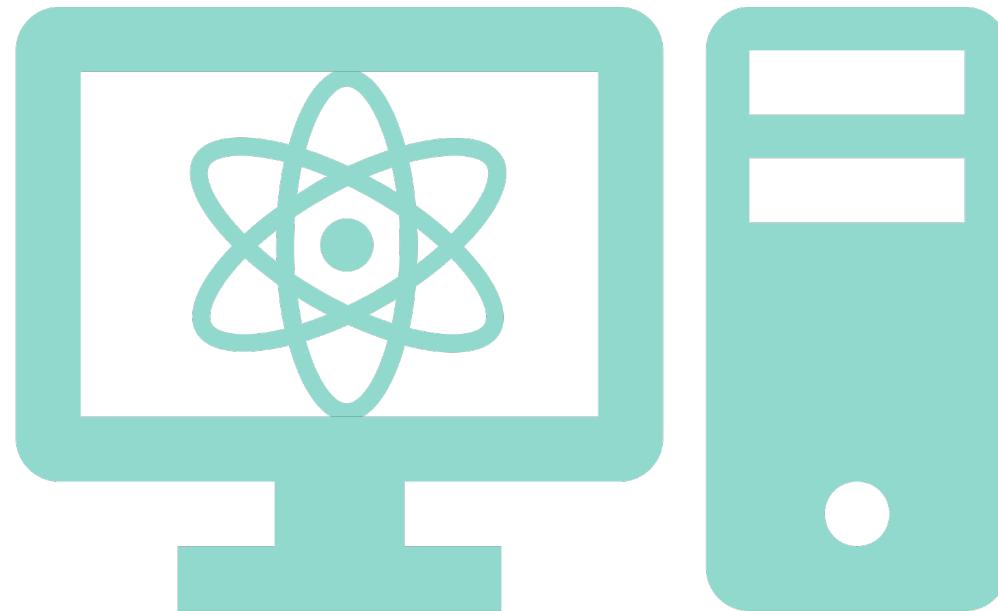




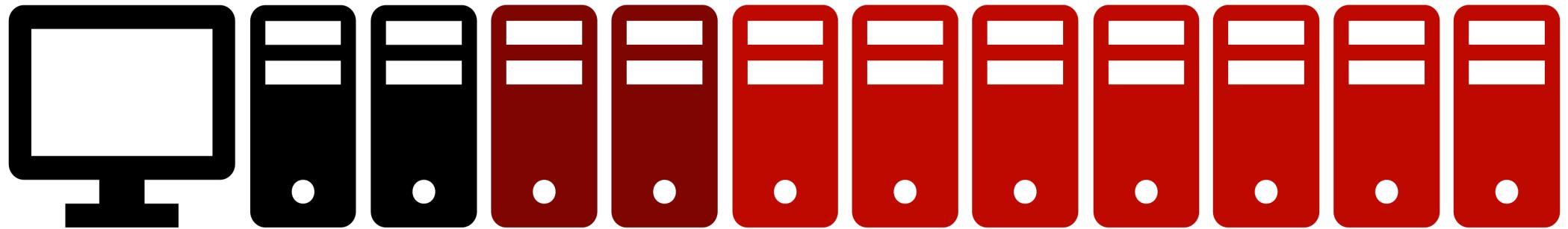








PROBLEMS



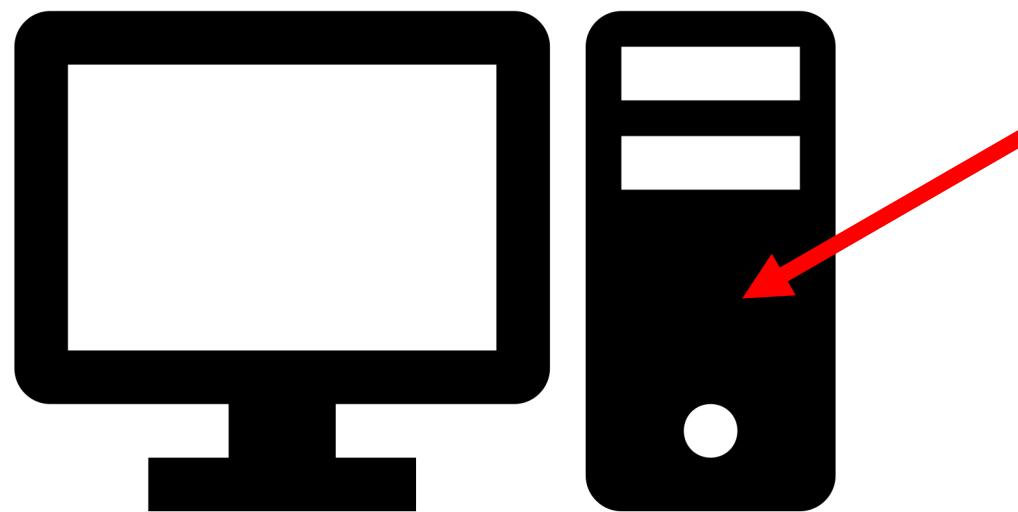
THEORY SOLUTION: BATCHING

1

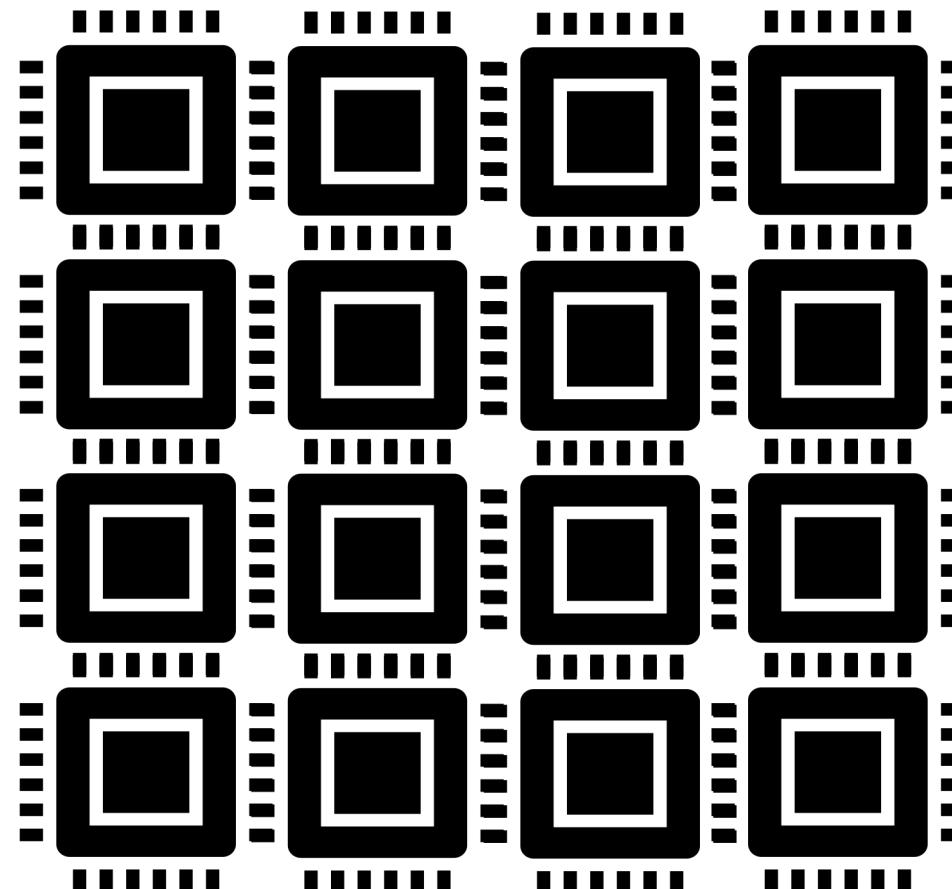
0 1 1 0 1 0 1 0

$$\begin{array}{r} \times + \\ \hline \end{array} \quad \begin{array}{c} \boxed{\begin{matrix} 0 & 1 & 1 & 0 & 1 & 0 & 1 & 0 \end{matrix}} \\ \boxed{\begin{matrix} 0 & 1 & 1 & 1 & 0 & 1 & 1 & 1 \end{matrix}} \\ \hline \boxed{\begin{matrix} 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 \end{matrix}} \end{array}$$

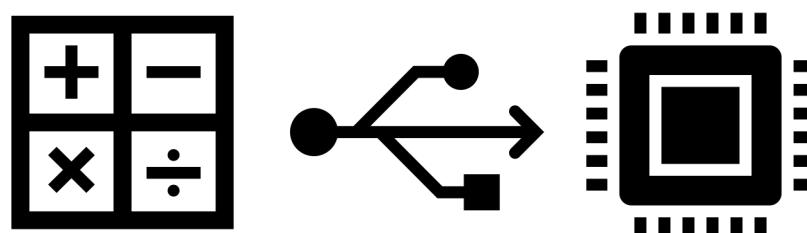
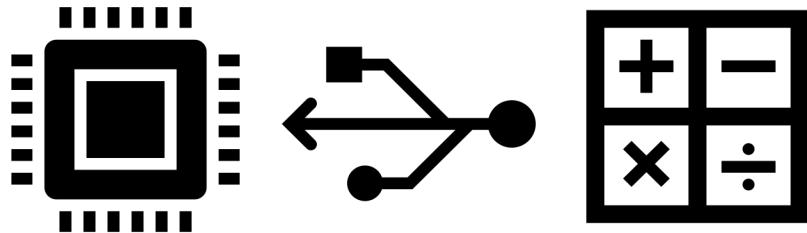
IMPLEMENTATION SOLUTION: CPU



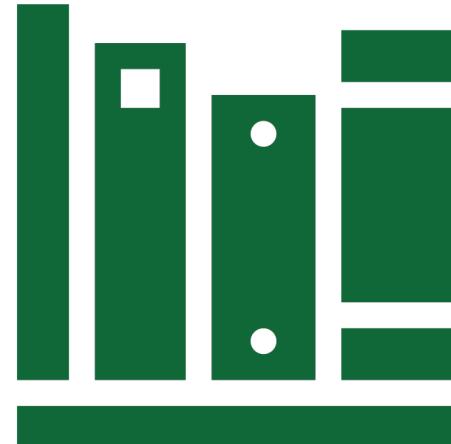
IMPLEMENTATION SOLUTION: GPU



IMPLEMENTATION SOLUTION: FPGA



MY RESEARCH



U.S. DEPARTMENT OF
ENERGY

Office of
Science