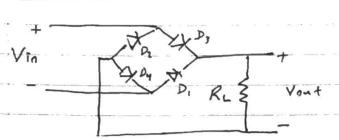
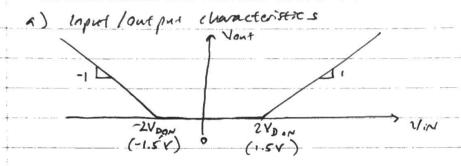
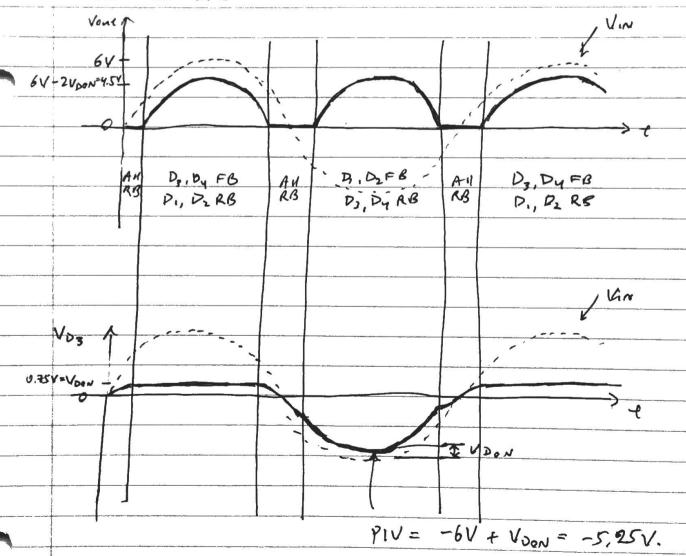
auiz 2.



 $V_{per} = 750 \text{mV}$ $V_{IN} = V_{p} \cos 2\pi f t$ $V_{p} = 6V$ f = 60 Me.



Vont Vs. Eme



Josephan Land Prof. Shinyan ECE241 Electronics I 4/20/20.

b) Vinox = 0.4 V, RL = 50 Th, add smeething cap. in parallel to load, Vr = ZL f=2x60Hz=12011z full wave rectifier doubles ripple freq. I, = VL = Vp-2VOON = 4.5V = 40MA (= \frac{1}{V_r f} = \frac{90mA}{(0.4V)(120Hz)} = 1.875mF (or greater) Vous 2/0 smoothing copacitis c) R,=1001, Ds: VD.ON = 800MV, Dz: VDz = 3.2V, rdz=51 Calculate line ? load regulation. Line regulation: ID = 100 D = 3 mA $C_d = \frac{V_r}{I_D} = \frac{0.026 \, \text{V}}{0.903 \, \text{A}} = 8.69 \, \Omega$ Load regulation: (assume 50.52 load) approximate w/ small signal model: AVroaded = (DIgordal) (Pd) \$ 13.67. Nont = (-80MA) (13.67.12) Vont = (13.67-100) S. Vin Volenge regulation = $\frac{\Delta V_{out}}{\Delta V_{in}} = \frac{13.67}{113.67} \times 1009_9 = 12.03% = -1.09V = -27.3%$

ECE 241 Electronics 1 4/20/20. VD,ON = 0.7V VIN = Vp cos we, Vp = 6V in put lout put characteristic: Vont Vs. time 5,7V > V.N 5.7V V2 =2V Viv = (same as apove) Vant vs-time: just Vin-5V 20) Vont always ViN Vout vs. time : input/output characteristus Von 6v 7 ١Y SV -5V -5V

Jonathan Lun Prof. Shlayan