

2 Computer Circuit Analysis Exam 1

Professor: Brian Frost

(11 points) Consider the circuit in Figure 1. Using nodal analysis, write a matrix equation which

will solve the circuit upon solution.

Fall 2019

(6 points) Consider the circuit in Figure 2. Count the number of nodes, meshes and branches.

1 Theoretical Questions (3 points each)

1. What can be said about two voltage sources in parallel with one another?
2. What can be said of two current sources in series with one another?
3. Draw a circuit which mesh analysis would fail to solve.
4. The passive sign convention, which we use in class, determines the sign of power across a circuit element. The active sign convention makes the exact opposite choice (all power values under the active sign convention are negatives of those under the passive sign convention). Describe, in words, the active sign convention without mentioning the passive sign convention.
5. With respect to the active sign convention, write an inequality which defines a passive element (including an integral). Is a resistor still passive?
6. Consider a resistor with resistance $R < 0$ in parallel with a voltage source of voltage $V > 0$. Does the resistor generate or dissipate power?
7. Consider a circuit with a complex resistive network, and a second circuit where this network is replaced by one equivalent resistor. Which of the following are necessarily the same across the network and the single resistor in their respective circuits: Voltage, current, power?
8. Figure 1 shows a branch in a circuit. Write its branch constituent equation such that the branch current is in terms of the branch voltage.
9. Write a brief argument using only Kirchoff's laws for the equivalent conductance formula for N conductors in parallel.
10. Write an argument for the equivalent resistance formula for N resistors in parallel, either by arguing from your above answer or by deriving it from Kirchoff's laws.
11. Spike failed to pay attention in circuits class, and is now at a loss when building his newest invention. He has access to only a 5V battery, but wishes to apply 2.5V to a load resistor with resistance 250Ω . Spike can use two resistors to provide this voltage to the load, and he wants to make it as precise as possible. Draw the circuit to "divide" Spike's voltage, and write a small note about the size of resistors Spike should choose.

2 Computational Questions (33 points total)

12. (11 points) Consider the circuit in Figure 2. Using mesh analysis, write a matrix equation which will solve the circuit upon solution.
13. (6 points) Consider the circuit in Figure 2. Count the number of nodes, meshes and branches.
14. (11 points) Consider the circuit in Figure 3. Using modified nodal analysis, write a matrix equation which will solve the circuit upon solution.
15. (5 points) Consider the resistive network in Figure 4. Find the equivalent resistance of the network assuming all resistors have the same value R .

3 Expository Example (34 points total)

16. (2 points) This question will introduce you to the concept of voltage gain. In a circuit which is interpreted as having a voltage input and a voltage output, the voltage gain is defined as V_{OUT}/V_{IN} . For Spike's circuit in question 11, what is the desired voltage gain?
17. (4 points) With only resistors, what is the possible range of voltage gains one can achieve? Briefly explain.
18. (2 points) Theoretically, can one achieve voltage gains outside of this range using only passive elements? Hint: Does the integral definition of passive preclude this?
19. (4 points) For the circuit in Figure 5, what is the voltage gain?
20. (2 point) There are elements other than resistors in this device – is it still limited to the range determined in question 17?
21. (1 point) The effective voltage gain of Spike's circuit is changed by the addition of a load. If you add a resistive load at the output of the circuit in Figure 5, does the gain change?
22. (3 points) This is related to a concept called output impedance. Considering the input to a circuit as an open circuit, one can compute the equivalent resistance of the network across the output terminals. In Spike's circuit, the output impedance is simply the resistance of the resistor across the output terminals. When a load is added, it is effectively placed in parallel with the output impedance of the circuit. For a load to have minimal effect on the gain, should the output impedance be small or large relative to the load?
23. (4 points) Inspired by your above answers, what do you think the output impedance of a dependent voltage source is? More specifically, what is the output impedance of the circuit in Figure 5.
24. (5 points) What is the voltage gain of the circuit in Figure 6?

25. (2 points) Is this circuit impacted by a load?
26. (2 points) What is this circuit's output impedance?
27. (1 points) What does the sign of gain represent?
28. (2 points) In reality, do you think we can build circuits with gains that are not impacted by loads? Explain.

1. Theoretical Questions (3 points each)

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Figures

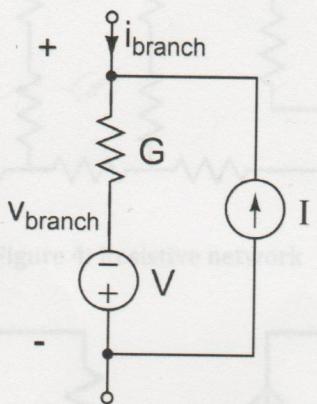


Figure 1: A given branch

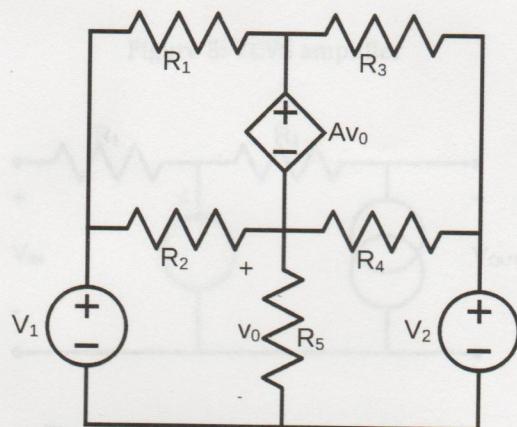


Figure 2: Using Mesh Analysis

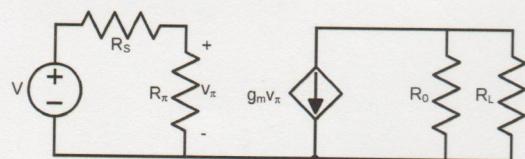


Figure 3: Using MNA

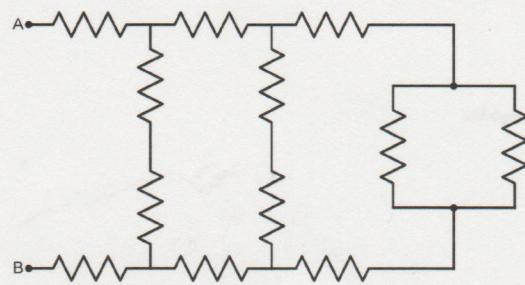


Figure 4: Resistive network

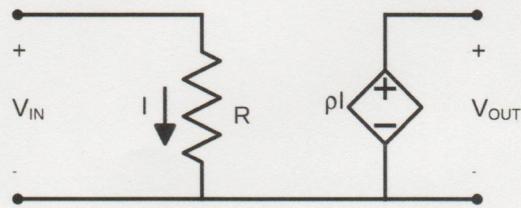


Figure 5: VCVS amplifier

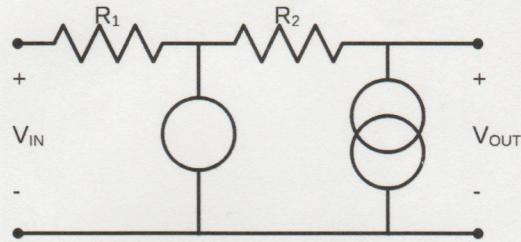
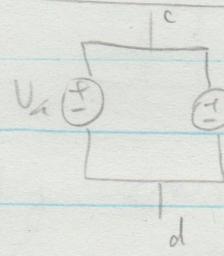


Figure 6: A nullator/norator amplifier

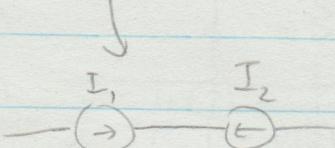
✓ (1) Theoretical Q's -



$V_b = V_{cd} = V_a$,
Since the BCES are:

✓ (2)

Since current is equal for 2 circuit elements in series, only possible if $I_1 = I_2$ (if facing same direction) or $I_1 = -I_2$ (if facing opposite directions)



✓ (3)

✓ (4)

If, in the diagram on the left, we specify $P = iV_{ab}$ as negative then the active sign convention is in use. i.e., this convention means a drop in voltage (and therefore power dissipated) is negative power.

perfect, first great answer
I've seen for this problem

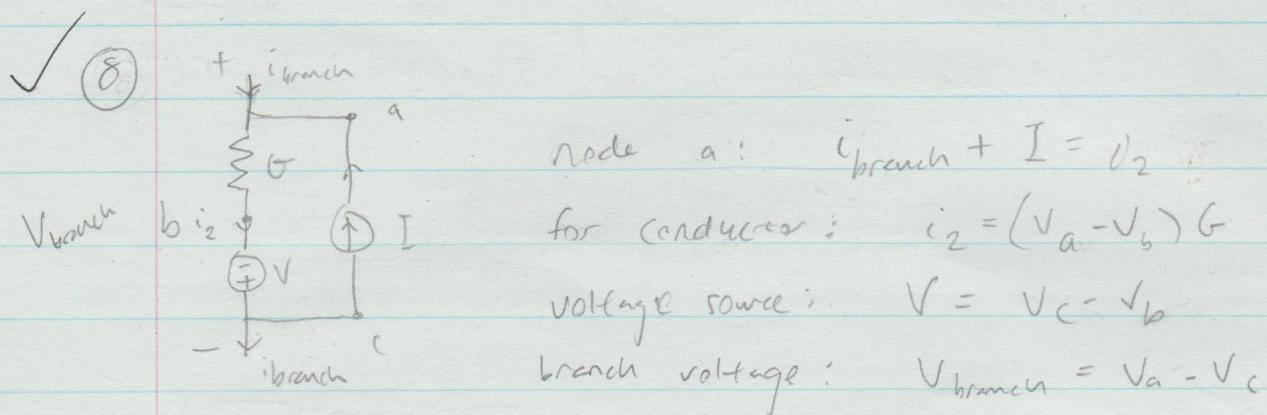
cannot generate power, so $P \neq 0$. Thus

✓ ⑤ $\int_0^T P dt \leq 0$. (i.e., reverse sign from passive sign convention)

Yes, a resistor is still passive

✓ ⑥ If generates power (and is thus active)

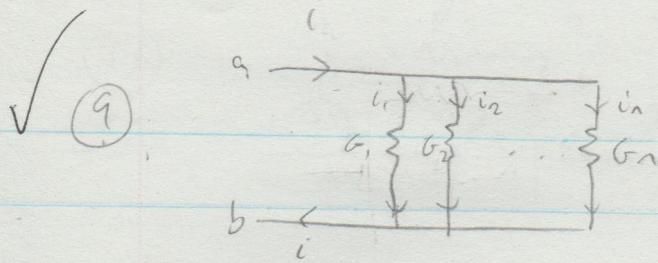
✓ ⑦ Between the start and end node of the more complex resistive circuit, the power, voltage, and current would all be equal to the single resistor



Solve for i_{branch} in terms of v_{branch}

$$\begin{aligned} i_{\text{branch}} &= i_2 - I \\ &= (v_a - v_b)G - I \\ &= ((v_{\text{branch}} + v_c) - v_b)G - I \\ &= (v_{\text{branch}} + (v_c - v_b))G - I \end{aligned}$$

$$i_{\text{branch}} = (v_{\text{branch}} + V)G - I$$



by KCL at node a, $i = i_1 + i_2 + \dots + i_n$
(same at node b)

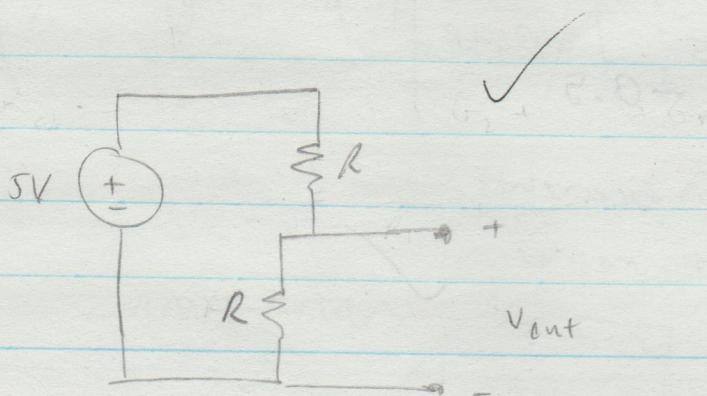
Since $i_1 = V_{ab}G_1$ (by BCL of conductor),

$$i = V_{ab}G_1 + V_{ab}G_2 + \dots + V_{ab}G_n$$

divide both sides by V_{ab}

$$\frac{i}{V_{ab}} = G_1 + G_2 + \dots + G_n = G_{eq} \quad (\text{since } G = \frac{i}{V})$$

✓ (10) $R_{eq} = \frac{1}{G_{eq}} = \frac{1}{G_1 + G_2 + \dots + G_n} = \frac{1}{\frac{1}{R_1} + \frac{1}{R_2} + \dots + \frac{1}{R_n}}$



(resistor values are equal)

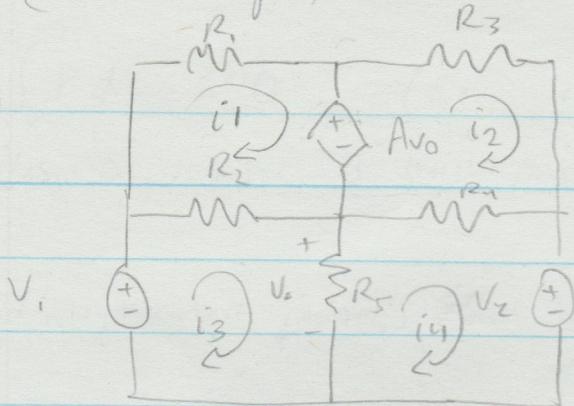
If splice connects his circuit to the +/- pins of V_{out} ,
then he can power his circuit with roughly 2.5 V.

Note that to be more accurate means choosing
smaller R_s (i.e., R should be much smaller than
resistive load), but choosing too small an R
could mean a very high current.

PERFECT
theoretical

(mesh analysis)

(12)



$$V_x = (i_3 - i_4)R_5 \checkmark$$

$$\text{mesh 1: } R_1 i_1 + A(i_3 - i_4)R_5 + (i_1 - i_3)R_2 = 0 \checkmark$$

$$\text{" 2: } R_3 i_2 + (i_2 - i_4)R_4 - A(i_3 - i_4)R_5 \checkmark$$

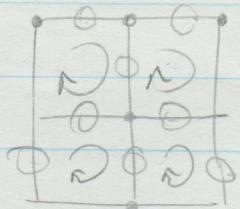
$$\text{" 3: } (i_3 - i_4)R_2 + (i_3 - i_4)R_5 - V_1 = 0 \checkmark$$

$$\text{" 4: } (i_4 - i_2)R_4 + V_2 + (i_4 - i_3)R_5 = 0 \checkmark$$

$$\begin{pmatrix} R_1 + R_2 & 0 & AR_5 - R_2 & -AR_5 \\ 0 & R_3 + R_4 & -AR_5 & -R_4 + AR_5 \\ -R_2 & 0 & R_2 + R_5 & -R_5 \\ 0 & -R_4 & -R_5 & R_4 + R_5 \end{pmatrix} \begin{pmatrix} i_1 \\ i_2 \\ i_3 \\ i_4 \end{pmatrix} = \begin{pmatrix} 0 \\ 0 \\ V_1 \\ -V_2 \end{pmatrix}$$

-0.5

(13)



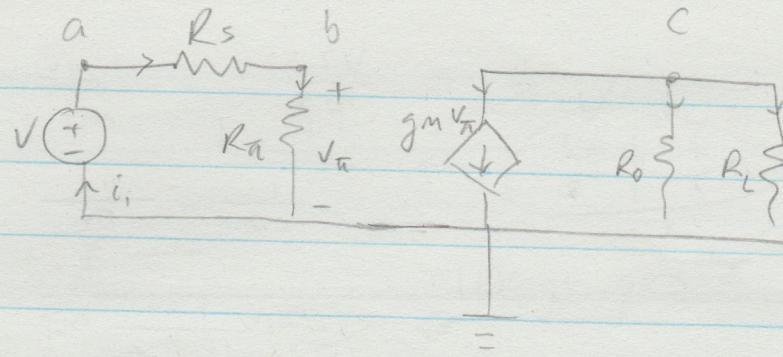
8 branches

4 meshes

5 nodes

$\boxed{-0.5}$

(14)



E.5

$$V_{\bar{A}} = V_b - V_{\text{gnd}} = V_b \quad \checkmark$$

$$a: i_i = (V_a - V_b) G_S \quad \checkmark$$

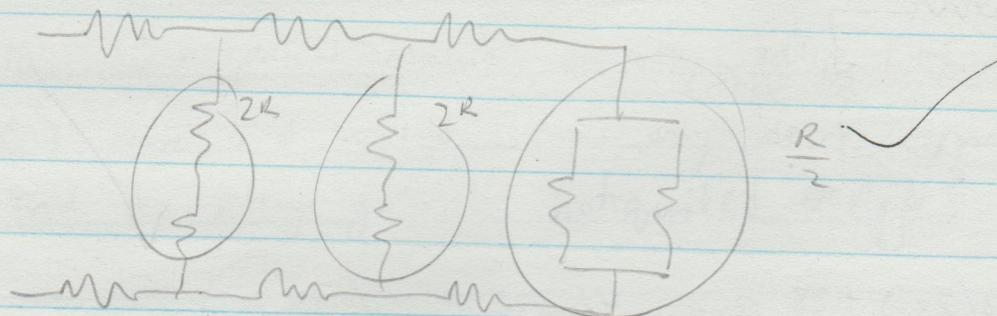
$$b: (V_a - V_b) G_S = V_b G_T \quad \checkmark$$

$$c: g_m (V_b) + V_c G_o + V_c G_i = 0 \quad \checkmark$$

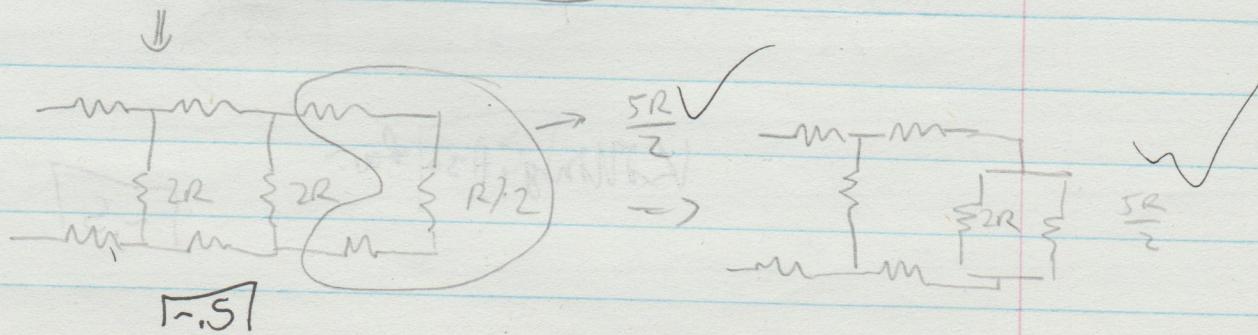
$$V = V_a \quad \checkmark$$

$$\left(\begin{array}{c|ccccc} -G_S & G_S & 0 & 1 & & \\ G_S & -G_S - G_T & 0 & 0 & & \\ 0 & g_m & G_o + G_L & 0 & & \\ 1 & 0 & 0 & 0 & & \end{array} \right) \left(\begin{array}{c} V_a \\ V_b \\ V_c \\ i_i \\ V \end{array} \right) = \left(\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ V \end{array} \right) \quad \checkmark$$

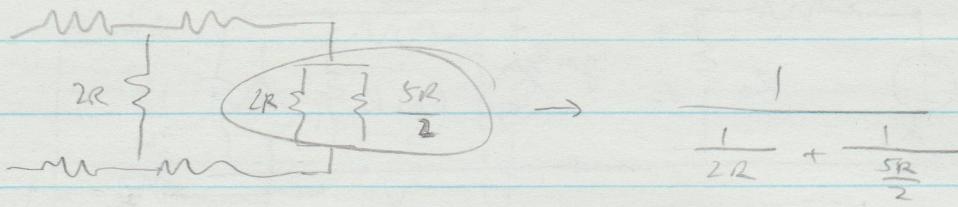
(15)



$$\frac{R}{2}$$



E.51



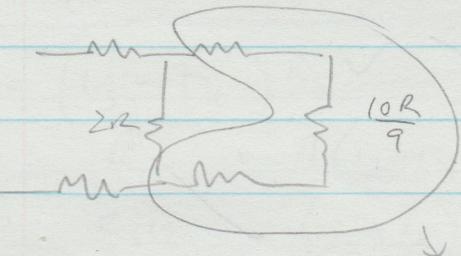
$$\frac{1}{2R} + \frac{1}{\frac{5R}{2}}$$

$$= \frac{1}{\frac{1}{2R} + \frac{2}{5R}}$$

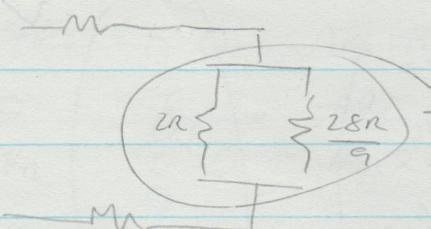
$$= \frac{1}{\frac{5}{10R} + \frac{4}{10R}}$$

$$= \frac{1}{\frac{9}{10R}}$$

$$= \frac{10R}{9}$$

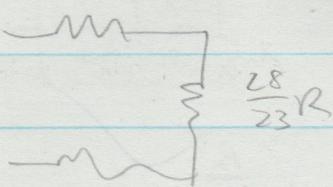


$$\frac{28R}{9}$$



$$R_{eq} = \frac{(2R) \left(\frac{28R}{9} \right)}{2R + \frac{28R}{9}}$$

$$= \frac{\cancel{56R}}{\cancel{46R}} = \frac{56}{46} = \frac{28}{23}$$



↓

$$\frac{2(23) + 28}{23} R = \frac{46 + 28}{23} =$$

$$\left(\frac{74}{23} R \right)$$

✓

Killing it so far

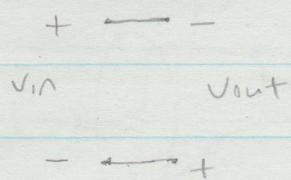
~.51

L-8

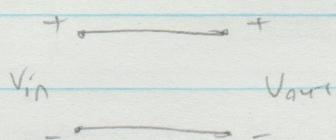
16.

$$\frac{V_{out}}{V_{in}} = \frac{2.5V}{5V} = \frac{1}{2} \checkmark$$

17.



$$\text{Voltage gain} = \frac{-V_{in}}{V_{in}} = -1,$$



$$\text{Voltage gain} = \frac{V_{in}}{V_{in}} = 1.$$

- It is not possible to create energy (and thus a larger voltage potential) using resistors \nwarrow so largest voltage gain is 1 (where output is equal to input voltage).

Conversely, since this is the largest possible magnitude for a voltage gain, flipping V_{out} gives the largest negative voltage gain of -1. Thus the range of voltage gains is [-1, 1]. Any value in between can be

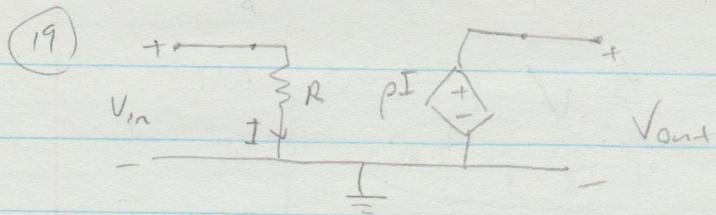
achieved with a voltage divider like in question 11, and a

voltage gain of 0 can be created by connecting V_{out}^+ to V_{out}^- with a S.C.

-1 ~~18~~ (18) No, since passive elements can only drop (the magnitude of), power and voltage, thus only decreasing V_{in} and the voltage gain, not quite! $V \uparrow \downarrow$?

$\boxed{-1.5}$

L-1.S]



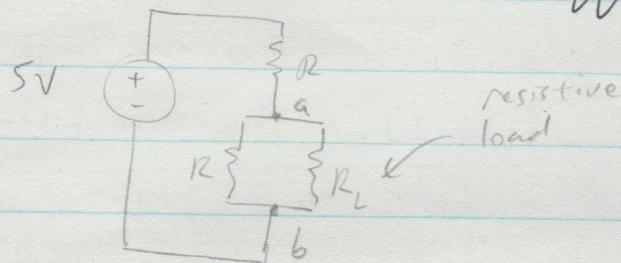
$$I = \frac{V_{in}}{R}, \quad pI = p \left(\frac{V_{in}}{R} \right) = V_{out}$$

$\checkmark \Rightarrow \frac{V_{out}}{V_{in}} = \frac{p}{R}$ is the voltage gain

- ✓ (20) No, since there is an active element (the voltage source, which is able to supply voltage and thus increase V_{out} by an indeterminate amount, allowing voltage gain to exceed $[-1, 1]$).

- S (21) Yes.

wrong circuit?



Right answer
for this
one
though?

$$R_{eq} = R + \frac{RR_L}{R+R_L}$$

$$i = \frac{V}{R_{eq}} = \frac{V}{R + \frac{RR_L}{R+R_L}}$$

$$V_{ab} = iR_{RL} = \left(\frac{V}{R + \frac{RR_L}{R+R_L}} \right) \left(\frac{RR_L}{R+R_L} \right) = \frac{V(RR_L)}{R(R+R_L) + RR_L}$$

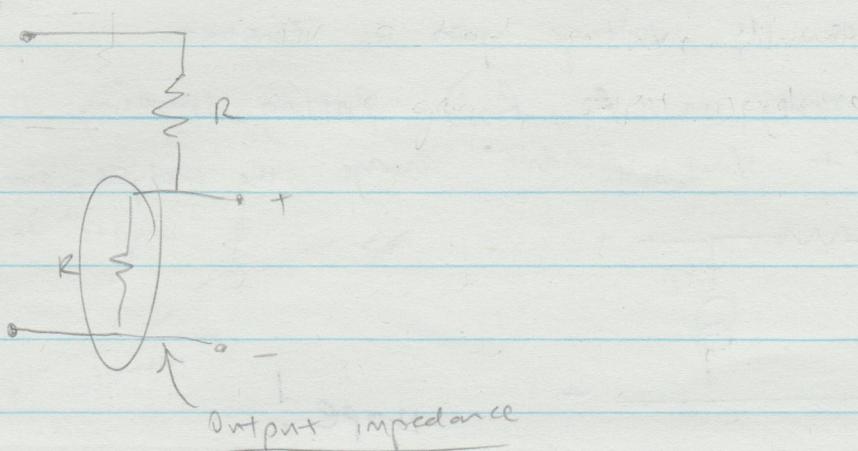
L-2

$$= V \left(\frac{RR_L}{R^2 + 2RR_L} \right) < V \left(\frac{1}{2} \right), \text{ since}$$

$RR_L < \frac{1}{2}(R^2 + 2RR_L)$. Therefore V_{out} is lower than $\frac{1}{2}$, so the gain drops.

✓ [2]

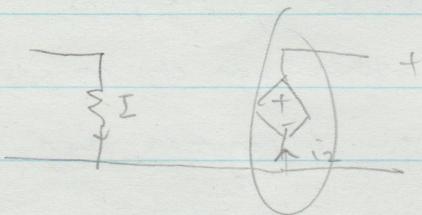
(22)



- Similar to Hw #2, if the output impedance (R) is small, then the voltage gain remains close to the desired value. Therefore the output impedance should be small relative to the load,

-1

(23)



equivalent resistance across a dependent voltage source is

$$R = \frac{V}{I_2} = \frac{P}{I_2} \text{ by ohm's law}$$

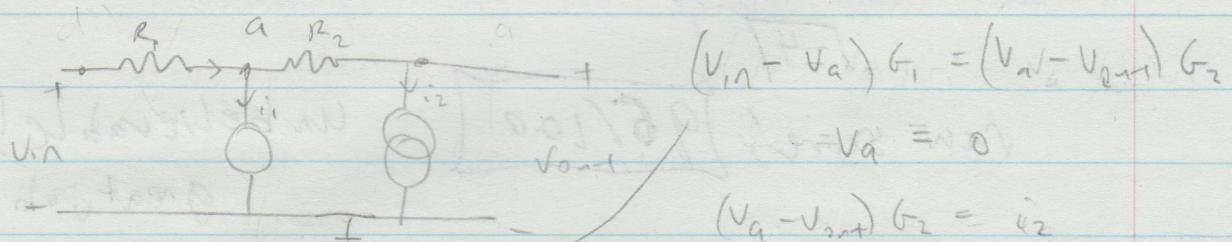
I_2 arbitrary. It's zero

"output impedance" here

✓

(24)

yes!



$$(V_{in} - V_a) G_1 = (V_a - V_{out}) G_2$$

$$(V_a - V_{out}) G_2 = i_2$$

$$\frac{G_1}{G_2} (V_{in} - V_a) = (V_a - V_{out}) \Rightarrow V_{out} = 0 \quad -\frac{G_1}{G_2} (V_{in} - 0) = -\frac{G_1}{G_2} V_{in}$$

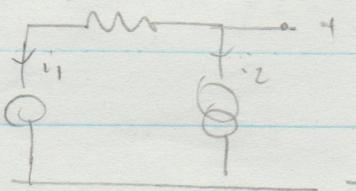
$$\Rightarrow \text{voltage gain} = \frac{-\frac{G_1}{G_2} V_{in}}{V_{in}} = -\frac{G_1}{G_2} = -\frac{R_2}{R_1}$$

✓ [3]

[-3]

- ✓ 25) No, since its voltage gain is constant (dependent only on R_1, R_2). Putting something in parallel to V_{out} wouldn't change the analysis, since the norator

✓ 26)



"adapts" to the new resistance

nope!

○ again

$$R = \frac{V}{i} = \frac{V_{out}}{i_1 + i_2} = -\frac{1}{f_i},$$

↑

by Ohm's Law

- ✓ 27) Sign of gain represents whether output +/- is oriented same way as input +/-, i.e., if V_{in} and V_{out} have same sign, then sign of gain is positive; if 0, then $V_{out} = 0$; if negative, then V_{out} has oppsite sign of V_{in} .

- ✓ 28) I don't think so. A load always changes the constraints of a real system (e.g. equivalent resistance) and thus should change the circuit's parameters.

[-4]

raw score: 96/100unbelievable!
great job