Electronic compressibility of layer-polarized bilayer graphene

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We report on a capacitance study of dual gated bilayer graphene. The measured capacitance allows us to probe the electronic compressibility as a function of carrier density, temperature, and applied perpendicular electrical displacement \overline{D} . As a band gap is induced with increasing \overline{D} , the compressibility minimum at charge neutrality becomes deeper but remains finite, suggesting the presence of localized states within the energy gap. Temperature dependent capacitance measurements show that compressibility is sensitive to the intrinsic band gap. For large displacements, an additional peak appears in the compressibility as a function of density, corresponding to the presence of a one-dimensional van Hove singularity (vHs) at the band edge arising from the quartic bilayer graphene band structure. For $\overline{D} > 0$, the additional peak is observed only for electrons, while for $\overline{D} < 0$ the peak appears only for holes. This asymmetry can be understood in terms of the finite interlayer separation and may be useful as a direct probe of the layer polarization.

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The unique band structures of monolayer (MLG) and bilayer graphene^{1,2} (BLG) offer unprecedented tunability in a high-quality two-dimensional electron system (2DES). Within the independent electron approximation, both MLG and BLG are gapless, chiral systems. The gapless spectra are related to the pseudospin degeneracy, which is tied to the symmetry between the two sublattices constituting the honeycomb. Whereas in the monolayer a gap can be opened only by a potential modulation on the spatial scale of the lattice constant,³ in BLG the relevant sublattices are located on different layers, allowing a gap to be induced by a modulation of the interlayer imbalance via the application of an electric field perpendicular to the BLG planes. 4-6 Although the fieldeffect tunable gap in BLG has been observed optically, 7-9 transport measurements show hopping conductivity at low temperatures. 10-13

In a parallel plate capacitor made up of imperfect conductors, adding charge n to the plates costs the sum of the classical electrostatic energy, the kinetic energy due to the resulting change in the chemical potential μ , and the potential energy of Coulomb interactions between the charge carriers. The measured differential capacitance, $C = \delta n/\delta V$, in such a system reflects this finite electronic compressibility by manifesting a lowered effective capacitance, $C^{-1} = C_0^{-1} +$ $(e^2Av)^{-1}$, where C_0 is the geometric capacitance, A is the area of the device, and $v \equiv \partial n/\partial \mu$ is the electronic compressibility, which corresponds to the density of states in the noninteracting, zero temperature limit. In low-dimensional systems the contribution of the compressibility to the capacitance termed "quantum capacitance" 14—can be small even when the conductivity remains large, providing a powerful tool in the study of both one-15 and two-dimensional 16 electronic systems. Capacitance measurements are particularly powerful in the study of disordered systems, as they are able to detect localized states whose contribution to transport is suppressed. Capacitance measurements are, as a result, crucial in understanding biased bilayer graphene, a system in which localization is known to play a role. Moreover, the small but finite interlayer separation ($d \sim 3.4 \text{ Å}$) allows the layer polarization to be probed through electrostatic measurements, as described in a companion paper.¹⁷

To produce dual gated graphene devices with high geometric capacitance, we utilize single crystal hexagonal boron nitride (h-BN) flakes¹⁸ as the top gate dielectric fabricated by the process described in Ref. 19. Briefly, both graphene and single crystal h-BN, an insulating isomorph of graphite, are exfoliated onto n-Si/SiO₂ wafers [Figs. 1(a) and 1(b)]. A thin (5-7 nm) h-BN flake is transferred on top of the graphene using a wet etch process and micromechanical manipulation, ²⁰ followed by electron beam lithography to form contact electrodes and a local top gate [Fig. 1(c)]. The heavily doped silicon substrate, coated with 285 nm oxide, serves as the bottom gate. The double gated geometry allows independent control of the electronic density n and the displacement $\overline{D} = \frac{\varepsilon_B}{d_B}[(V_B - V_B^0) - \frac{C_T^0}{C_B^0}(V_T - V_T^0)]$ through the top and bottom gate voltages, V_T and V_B . Here, ε_B and d_B are the dielectric constant and thickness of the back gate dielectric layer, $C_{T(B)}^0$ is the geometric capacitance of the top (bottom) gate, and $V_{T(R)}^0$ is the voltage offset required to obtain minimal density and displacement in the dual-gated region. We find that h-BN is an excellent gate dielectric, with $\varepsilon \sim 3-4$ and breakdown fields comparable (\sim .8 V/nm) to SiO₂ thin films. In addition, we observe minimal degradation of graphene samples, with no additional doping contributed by the presence of the top gate and typical post h-BN transfer mobilities of $\mu \sim 5\,000-10\,000$ cm²/V sec for graphene monolayers and $\mu \sim 2\,000$ –3 000 cm²/V sec for bilayers.

Low temperature capacitance measurements were performed using a capacitance bridge circuit [Fig. 1(e)] with a cold reference capacitor. All wires were shielded, and the sample package was encased in a Faraday cage to further reduce parasitic capacitances, which represent an additive constant

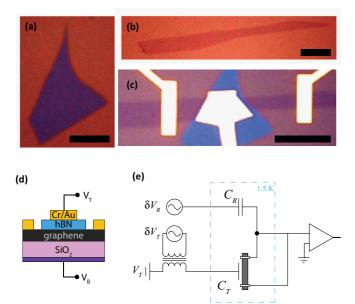


FIG. 1. (Color online) (a)–(c) Optical microscope images. (a) A thin hBN single crystal is transferred onto a (b) mono- or bilayer graphene flake and (c) contacts and gate electrodes deposited by electron beam lithography. Scale bars are 10 μ m. (d) Cross section schematic of resulting dual gated device. (e) Schematic circuit diagram of the capacitance bridge. A reference capacitor (Johanson Technology R14S) is mounted on the probe, and a reference voltage is chosen to balance the capacitance bridge. A small AC excitation signal is added to the DC gate bias through a transformer (Triad Magnetics SP67).

to the measured value of C_T . A ceramic multilayer capacitor with minimal temperature dependence was chosen for the reference capacitor (C_R) and connected near the sample at low temperature. The noise level of the bridge was $\sim 25 e/\sqrt{Hz}$, allowing subfemtofarad resolution with averaging times of less than 30 seconds for our typical top gate AC excitation voltage $\delta V_T \sim 15\text{--}50$ mV. Measurements were performed off balance. For the variable temperature capacitance measurements, we applied $\delta V_T = 50 \text{ mV}$ AC excitation voltage on top of the DC gate bias and measured the current through the graphene device directly. Although this method results in poorer signal to noise than the bridge measurement, it eliminates calibration errors stemming from the small temperature dependence of the reference capacitor. The ability to measure the quantum contributions to the capacitance relies on the use of a thin top gate dielectric layer. The 7-nm-thick hBN dielectric results in values of C_T^0 that are comparable to the quantum capacitance C_Q , so that variations in the measured C_T with changing electronic compressibility are easily detectable.

We check the efficacy of this measurement scheme using monolayer graphene capacitors. MLG is expected to display monotonically increasing ν as a function of absolute density |n|. In the presence of a strong magnetic field, ν is further modulated due to the formation of Landau levels. Figure 2 shows the measured capacitance C and conductance C of MLG as a function of top gate voltage C at both zero and finite magnetic field. The lowered compressibility stemming from the linear spectrum of MLG can be inferred from a depression in the capacitance at zero density, while at high

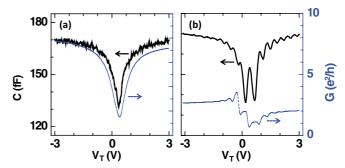


FIG. 2. (Color online) Measured conductance and capacitance of monolayer graphene at 2 K and B=0 (a) and B=9 T (b). In (a), capacitance and conductance resemble each other closely due to the fact that both contain a spurious contribution adding "in parallel"; $G=(1/R+1/R_C)^{-1}$ and $C_T=(1/C_Q+1/C_T^0)^{-1}$ where R is the device resistance, C_Q is the compressibility, or "quantum capacitance," C_T^0 is the geometric capacitance, and R_C is the contact resistance of our two-terminal devices.

B the formation of the zero Landau level leads to a peak at charge neutrality.²¹ The high magnetic field capacitance traces show compressibility oscillations due to the formation of higher LLs, while conductance shows the electron-hole asymmetry that is the signature of edge state transport in graphene heterojunctions, ^{22–24} which form due to the partial coverage of the graphene channel by the top gate. The peak inversion at zero density and electron-hole symmetry in high magnetic field together confirm that the measured signal is indeed capacitive, and is sensitive to density of states rather than lateral resistance effects.

We now turn our attention to bilayer graphene (BLG) samples. Figure 3 shows the capacitance of a BLG sample at 1.5 K measured with the cold bridge. Tuning external gates adjusts both n and \overline{D} . For small values of $\overline{D} \approx 0$, the measured capacitance exhibits a minimum at n = 0 as expected for ungapped bilayer graphene, which has a hyperbolic band structure. As $|\overline{D}|$ increases, the n=0 minimum gets deeper, corresponding to the formation of a gap in the energy spectrum. The n = 0 minimum does not go to zero at high values of $|\overline{D}|$, and in fact the capacitance modulation is only 10% with respect to the D=0 value. In addition, a distinct local maximum develops next to the minimum. As we argue below, the presence of both the dip and local peak in ν at high $|\overline{D}|$ can be understood, at least qualitatively, from the band structure of gapped BLG, once the effects of disorder²⁷ and the interlayer separation¹⁷ are taken into account.

Within the nearest-neighbor tight binding approximation, the energy spectrum of pristine, Bernal stacked bilayer graphene with finite interlayer asymmetry Δ is gapped and has a "Mexican hat" structure.^{2,5} Even in the presence of disorder, the absence of a positive quadratic term in the energy spectrum turns the problem of gapped, disordered bilayer graphene into one of a heavily doped semiconductor with *quartic* energy bands.²⁸ We thus expect vestiges of a $\nu \propto 1/\sqrt{E}$ vHs-like feature to be present even in our low mobility bilayer samples, manifesting as a nonmonotonic-in-density feature at the band edge.^{27,28} In addition to smearing the band edge vHs, disorder has a dramatic effect on compressibility at charge neutrality in the presence of a large gap. In contrast to clean semiconductors,

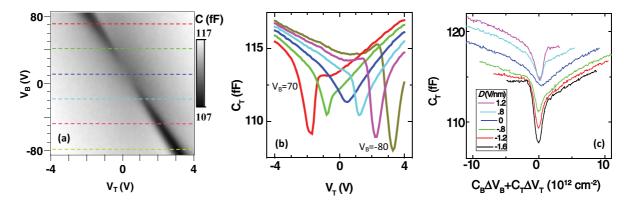


FIG. 3. (Color online) Capacitance at B=0 and 1.5 K as a function of V_T and V_B . Colored traces in (b) are taken at 30 volt intervals in V_B , corresponding to the colored lines in (a). (c) Traces at constant \overline{D} , extracted from the data set shown in (a). Data is plotted as a function of $C_T^0 \Delta V_T + C_B^0 \Delta V_B$, which would correspond to the density if the bilayer were perfectly two dimensional and perfectly compressible. Curves in (c) are offset for clarity by 2 fF per V/nm in \overline{D} .

in which the depleted system is incompressible, our measured capacitance remains finite and large even for large \overline{D} , a fact we attribute to tails in the density of states representing localized intergap states.²⁹ This explains the discrepancy between energy scales that govern transport lo-13 and the gap energies observed optically.⁷⁻⁹ It also suggests that recently predicted topological edge conduction³⁰ is not the dominant reason for incomplete "turn-off" in BLG devices of typical quality.

Quantitative analysis of the capacitance data requires extracting the compressibility ν from the measured signal C_T . For a perfectly two-dimensional electron system ($C_{\rm BL} \to \infty$), the measured top gate capacitance is

$$C_T^{-1} = \left(\frac{1}{C_T^0} + \frac{1}{Ae^2\nu}\right)^{-1} + C_{\text{para}},$$
 (1)

where C_{para} are all parasitic capacitances between gate and contact electrodes, and terms $\mathcal{O}(\frac{C_B^o}{C_s^0}) \sim .034$ have been neglected. Extracting ν thus requires subtracting both parallel (C_{para}) and series (C_T^0) capacitances, and dividing by A = $31 \mu m^2$ (determined by optical microscopy). We determined C_T^0/C_R^0 from the ratio of the back and top gate capacitances, measured by tracking the charge neutrality point in the V_T - V_B plane [the dark diagonal belt in Fig. 3(a)]. For the BLG device presented in this paper, we measured $C_T/C_B = 29.5 \pm .4$, where $C_B = 115 \text{ aF}/\mu\text{m}^2$ is the geometric capacitance of the bottom gate. As disordered BLG devices cannot be turned off completely, C_{para} cannot be measured in situ as is common practice in depletable semiconductors³¹ and semiconducting carbon nanotubes. 15 Instead, we determine $C_P = 16 \pm 1$ fF by removing the graphene through a short oxygen plasma etch and measuring the remaining capacitance between the metal contacts and the top gate. $C_{\rm para}$ constitutes about 10% of the total capacitance signal. Due to the subsequent subtraction of the (series) geometric capacitance C_T^0 , the error in determination of C_{para} is least important when the capacitance differs considerably from the geometric value. This is the regime in which we perform a quantitative analysis of the compressibility of gapped BLG.

Near overall charge neutrality at $|\overline{D}| \gg 0$, our samples show a hopping conductivity similar to that observed in Ref. 10

from 1 K to \sim 150 K. Capacitance instead shows no significant temperature dependence up to 50 K, thereafter slowly rising as might be expected for a spectrum with a gap in 50–100 meV range (Fig. 4). This is consistent with the presence of disorder-induced tails in the density of states throughout the band gap: Whereas temperature dependent transport is dominated by the

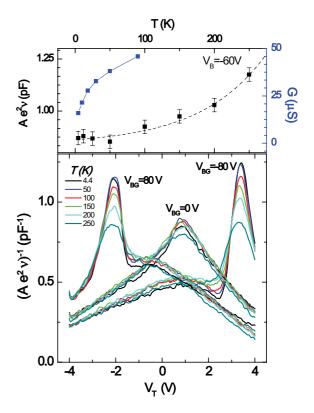


FIG. 4. (Color online) Bottom panel: Temperature dependence of the inverse compressibility $(Ae^2\nu)^{-1}$ for $V_B=80$, 0, and -80 V. A single value of $C_{\rm para}$ is chosen for all gate voltages at a given temperature, but a different $C_{\rm para}$ is chosen for each temperature so that the curves match at high density. We attribute the variation in $C_{\rm para}$ to thermal expansion of the bonding wires, the capacitance of which constitute the majority of $C_{\rm para}$. Top panel: comparison between the temperature dependence of the minimal compressibility and minimal conductivity at $V_B=-60$ V. The dashed line is a guide for the eye.

hopping between these localized states, temperature dependent capacitance is dominated by thermal population of the much larger density of states near the band edge.

The most interesting, and unexpected, features of the experimental data are the local maxima observed at the band edge, which are associated with the 1D vHs characteristic of the BLG band structure. 27,28 Interestingly, this feature is only present on one edge of the gap, appearing on the electron side for $\overline{D} > 0$ and the hole side for $\overline{D} < 0$. This inversion symmetry in the variables (\overline{D},n) was observed in all devices measured, including those fabricated using a resist free shadow mask metallization as well as seeded atomic layer deposition of HfO₂.³² Understanding this asymmetry requires taking into account the three-dimensional structure of BLG, ¹⁷ which consists of two strongly coupled but spatially distinct layers of carbon atoms. The charge distribution on a BLG flake is sharply concentrated on the two layers, $n(z) \simeq n_1 \delta(z) +$ $n_2\delta(z+d)$, so that the system can be modeled as a four plate capacitor. Solution of this electrostatic problem leads to the modified relation for the measured capacitance in which C_T depends on both the *interlayer* capacitance, v_{21} , and intralayer capacitances, v_{11} and v_{22} , where $v_{ij} \equiv \partial n_i / \partial v_j$ with n and v represent layer indexed density and potential, and the indices i, j = 1, 2 denote the top or bottom layer. Crucially, while v_{21} is symmetric with respect to layer interchange, $\frac{\partial n_2}{\partial v_2}$ is obviously asymmetric in the presence of interlayer asymmetry. Penetration field²⁶ measurements of bilayer graphene depend only on layer-symmetric quantities and thus probe fundamentally different physical quantities.

As elaborated in Ref. 17, the $1/\sqrt{E}$ divergence associated with the 1D band edge singularity resides principally on the low energy layer within the BLG flake. The vHs manifests more strongly in the measured capacitance when the vHshosting layer is closest to the top gate; conversely, the gate sees a far-layer vHs only through the screened field penetrating the near layer. Counterintuitively, disorder enhances this effect not only by smearing the total density of states 27,28 but by populating the normally depleted non-vHs layer, thereby enhancing its ability to screen. While an ideal experimental geometry would permit the simultaneous measurement of capacitance from two sides of the BLG flake, this is effectively accomplished in our single local gate geometry by reversing the sign of \overline{D} , thus reversing the order of the vHs bearing and nonbearing layers.

In order to better compare our experimental data with theory based on a parabolic two band model, it is convenient to subtract a background taken at $\overline{D}=0$. Because the high energy behavior depends only weakly on the displacement, this has the effect of isolating the low energy part of the measured capacitance, and in addition removes the effects of electronhole asymmetric elements of the band structure. ²⁶ The results of this subtraction resemble theoretical calculations which take

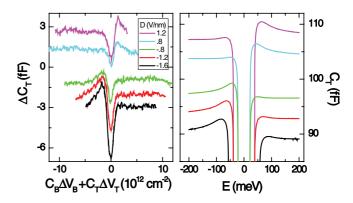


FIG. 5. (Color online) Left panel: Subtracted capacitance, $\Delta C_T = C_T(\overline{D}) - C_T(\overline{D} = 0)$, as a function of approximate density for different applied displacements. Curves are offset by 1 fF per V/nm in \overline{D} . Right panel: Calculated top gate capacitance for disordered bilayer graphene, following Ref. 17. The colors correspond to the displacements in the left panel; the disorder parameter is $\gamma = 4$ for all curves. Curves are offset by 3 fF per V/nm in \overline{D}

into account both the interlayer separation as well as weak, short-range disorder (Fig. 5). In particular, the asymmetric appearance of the van Hove singularity can be understood as the effect of disorder enhanced interlayer screening. However, quantitative understanding of the role of disorder in bilayer graphene will require experiments that independently control the disorder, as well as a more sophisticated theory taking into account a wider variety of effects including long range scattering and electron-electron interactions.

In conclusion, we study the broken symmetry state of bilayer graphene induced by an electrical displacement field applied perpendicular to the bilayer. We observed the formation of the displacement induced gap and its accompanying 1D vHs, and estimate the gap size from temperature dependent measurements. We also discussed how different measurement geometries make capacitance a probe of layer-pseudospin polarization in the bilayer. Layer symmetry breaking in BLG can also be expected to occur spontaneously, as a result of electronic interactions at both finite 19,33-36 and zero 37 magnetic fields. Similar measurements performed on high-quality bilayers manifesting these effects will allow spontaneous layer polarization to be probed directly through comparison of measurements in various capacitance geometries, as discussed at length in a companion paper. 17

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