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Jose L. Honorato

Experience

July 2011 - Present
Embedded Systems Engineer - Co-founder, *Satelinx*, Santiago, Chile.
- Hardware design, mainly focused on GPS-tracking devices and satellite communications. Grew from zero to stable revenue in 12 months. Shipped the first product in 6 weeks (Design + manufacturing + testing).
- Tasks: firmware programming, PCB design and hardware automated testing.

December 2012 - Present
Software Developer, *Beelinkme*, Santiago, Chile.
- Developed a generic platform for wireless sensor networks. Increased battery life by 10x, decreased cellular data usage by 4x. Used Node.js to achieve actuator responses faster than 0.5s.
- Tasks: firmware programming, task automation, system reliability.

August 2011 - Present
Part-time lecturer, *Pontificia Universidad Católica*, Santiago, Chile.
- Lecturer for the Computers Architecture Lab course. Theoretical/practical content involving embedded systems, such as msp430 devices and embedded boards (BeagleBoard, Gumstix, RaspberryPi). Redesigned the entire course to include more up-to-date hardware.

June 2010 - August 2011
Researcher, *Biomedical Imaging Center*, Santiago, Chile.
- Designed and implemented a new processing technique that generates better fat and water images in MRI.
- Published my work on the most prestigious magazine of the subject.

Winter 2010
Software Engineering Intern, *Infosys*, Bangalore, India.
- Developed a business plan regarding the mobile advertising industry. Worked on the technical guidelines for the implementation.

Education

June 2010 - August 2011
Master of Science in Electrical Engineering, Pontificia Universidad Católica, *Santiago, Chile.*
Graduated *Summa Cum Laude*

March 2005 - June 2010
Electrical Engineering, Pontificia Universidad Católica, *Santiago, Chile.*
Top 5% of the class. Direct GPA conversion does not apply.

Computer Skills

Languages: C (2.5 years), Matlab (4 years), Python (2.5 years), Bash (2 years).

Hardware: PCB layout experience, RF Design. Protocols: SPI, I2C, USB, UART.
msp430 (2 years), AVR ATmega (1 year), PIC 16 and 18 (2 years), ARM Cortex M4 (1 year).

Platforms: Mac OS X and Ubuntu Linux.

Others: Image Processing, sensor integration.

Awards

- Best Electrical Engineer, *Chilean College of Engineers*, June 2012
- Magna Cum Laude Award, *ISMRM*, May 2012
- Best Electrical Engineering Thesis, *EE Department, PUC*, September 2011
- Best Intern, *Infosys*, March 2010

Extras

- LinkedIn profile: http://www.linkedin.com/in/jlhonora
- Github account: http://github.com/jlhonora