

Jose L. Honorato

Electrical Engineer, M. Sc.

869 San Damian
Las Condes, Santiago, Chile
☎ +56 9 92619739
✉ jlhonora@ing.puc.cl

Education

- June 2010 - August 2011 **Master of Science in Electrical Engineering, Pontificia Universidad Católica, Santiago, Chile.**
Graduated *Summa Cum Laude*
- March 2005 - June 2010 **Electrical Engineering, Pontificia Universidad Católica, Santiago, Chile.**
Top 5% of the class. Direct GPA conversion does not apply.

Experience

- July 2011 - Present **Embedded Systems Engineer - Co-founder, Satelinx, Santiago, Chile.**
Hardware design, mainly focused on GPS-tracking devices and Iridium network communications. Grew from zero to stable revenue in 12 months. Developed the first commercial version of our main product in 6 weeks.
Responsible for the firmware programming and PCB design. Worked closely with the manufacturing team.
- August 2011 - Present **Part-time lecturer, Pontificia Universidad Católica, Santiago, Chile.**
Lecturer for the Computers Architecture Lab course. Theoretical/practical content involving embedded systems, such as msp430 devices and embedded boards (BeagleBoard, Gumstix, RaspberryPi). Redesigned the course to include more up-to-date hardware.
- June 2010 - August 2011 **Researcher, Biomedical Imaging Center, Santiago, Chile.**
Designed and implemented a new processing technique that generates better fat and water images in MRI.
Published my work on the most prestigious magazine of the subject.
- Winter 2010 **Software Engineering Intern, Infosys, Bangalore, India.**
Developed a business plan regarding the mobile advertising industry. Worked on the technical guidelines for the implementation.

Major school projects

- Co-founded the Mobile and Embedded Computing Laboratory, PUC. The purpose of this laboratory is to give students a hands-on experience on mobile apps development.
- Participant at the Sika Solar Experience. Trip to Photovoltaic plants throughout Europe.

Computer Skills

- Languages: C (1.5 years), Matlab (4 years), Python (2 years), C# (4 years), Bash (1 year)
Hardware: PCB layout experience, RF Design. CAD software: Eagle, Proteus
Platforms: Windows 7 (user), Mac OS X and Ubuntu Linux (intermediate)
Software: AutoCAD, SketchUp (intermediate)

Awards

- Best Graduated Electrical Engineer, *Chilean College of Engineers*, June 2012
- Magna Cum Laude Award, *ISMRR*, May 2012
- Best Electrical Engineering Thesis, *EE Department, PUC*, September 2011
- Best Intern, *Infosys*, March 2010

Extras

- LinkedIn profile: <http://www.linkedin.com/in/jlhonora>
- BitBucket repositories: <http://bitbucket.org/jlhonora>
- Satelinx: <http://satelinx.com>
- Fork my resume here: <http://bitbucket.org/jlhonora/resume/src>

Scan the code for my
contact information

