

Jose L. Honorato

Experience

- August 2019 - Present **Software Engineer, Stripe - LATAM Expansion, Seattle, WA.**
- o Launched critical initiatives spanning 30+ technical and non-technical teams. Interfaced with regulators and key external partners to onboard 100k+ merchants in Mexico and Brazil.
 - o Modified stack end-to-end from payments capture to payouts in order to onboard our largest users in the region, accounting for 50% of total volume.
 - o As a founding team member, I contributed to processes like team strategy and vision and oncall norms.
- July 2016 - July 2019 **SDE - Sr. SDE, Amazon.com - Marketplace, Seattle, WA.**
- o Lead multi-disciplinary team (engineering, product, operations, compliance) to streamline offline processes into well defined software products. Designed and implemented systems used by 10+ internal teams.
 - o Launched a new seller verification process that reduced bad actor complaints by 23 percentage points.
 - o Deprecated a critical-path distributed ecosystem in favor of a new service architecture improving flexibility, maintainability, and stability.
- December 2012 - June 2016 **CTO - Co-founder, Beetrack, Santiago, Chile.**
- o Co-founded and scaled a bootstrapped startup with clients in 20 different countries. In charge of design and architecture of web and native mobile apps. Lead engineer for native Android app. Enabled team to use modern technologies such as React, Swift, Elasticsearch, RabbitMQ. Managed a team of 5 devs.
- July 2011 - December 2012 **Embedded Systems Engineer - Co-founder, Satelinx, Santiago, Chile.**
- o Hardware design, mainly focused on GPS-tracking devices and satellite communications. Grew company from zero to stable revenue in 12 months. Shipped the first product in 6 weeks (Design + manufacturing + testing).
 - o Tasks: firmware programming, PCB design and hardware automated testing.
- August 2011 - December 2014 **Part-time lecturer, Pontificia Universidad Católica, Santiago, Chile.**
- o Lecturer for the Computers Architecture and Comp. Arch. Lab course. Theoretical/practical content involving embedded systems, such as msp430 devices and embedded boards (BeagleBoard, Gumstix, RaspberryPi). Redesigned course to include more up-to-date hardware.

Education

- June 2010 - August 2011 **Master of Science in Electrical Engineering, Pontificia Universidad Católica, Santiago, Chile.**
Graduated *Summa Cum Laude*
- March 2005 - June 2010 **Electrical Engineering, Pontificia Universidad Católica, Santiago, Chile.**

Skills

- Languages: Java, Ruby, Python, Bash
- Others: AWS (EC2/S3/SQS/SNS/SWF/Redshift/Kinesis/DynamoDB/Lambda/Step Functions), Operations Research/Optimization (minizinc, Matlab), Image Processing (OpenCV, AWS Rekognition/Texttract).

Awards

- o Best Electrical Engineer / Best Thesis, *Chilean College of Engineers*, June 2012
- o Magna Cum Laude Award, *International Society for Magnetic Resonance in Medicine*, May 2012
- o Best Electrical Engineering Thesis, *EE Department, PUC*, September 2011

Extras

- o Github account: <http://github.com/jlhonora>
- o LinkedIn profile: <http://www.linkedin.com/in/jlhonora>