Jose L. Honorato

4383 Pdte. Errazuriz, dep 902
Las Condes, Santiago, Chile
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Company's recruiting team
Address 1
Address 2
Ref: Job # 1234 - Job Title

Dear Sir or Madam,

PARAGRAPH ONE: State the reason for the letter, name the position or type of work you are applying for and identify the source from which you learned of the opening (i.e. career development center, newspaper, employment service, personal contact).

PARAGRAPH TWO: Indicate why you are interested in the position, the company, its products, services - above all, stress what you can do for the employer. If you are a recent graduate, explain how your academic background makes you a qualified candidate for the position. If you have practical work experience, point out specific achievements or unique qualifications. Try not to repeat the same information the reader will find in the resume. Refer the reader to the enclosed resume or application which summarizes your qualifications, training, and experiences. The purpose of this section is to strengthen your resume by providing details which bring your experiences to life.

PARAGRAPH THREE: Request a personal interview and indicate your flexibility as to the time and place. Repeat your phone number in the letter and offer assistance to help in a speedy response. For example, state that you will be in the city where the company is located on a certain date and would like to set up an interview. Alternatively, state that you will call on a certain date to set up an interview. End the letter by thanking the employer for taking time to consider your credentials.

Sincerely,
Your name

Jose L. Honorato

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Education

June 2010 - August 2011
Master of Science in Electrical Engineering, Pontificia Universidad Católica, *Santiago, Chile*.
Graduated *Summa Cum Laude*

March 2005 - June 2010
Electrical Engineering, Pontificia Universidad Católica, *Santiago, Chile*.
Top 5% of the class. Direct GPA conversion does not apply.

Experience

July 2011 - Present
Embedded Systems Engineer - Co-founder, *Satelinx*, Santiago, Chile.
- Hardware design, mainly focused on GPS-tracking devices and Iridium network communications. Grew from zero to stable revenue in 12 months. Developed the first commercial version of our main product in 6 weeks.
- Responsible for the firmware programming and PCB design. Worked closely with the manufacturing team.

August 2011 - Present
Part-time lecturer, *Pontificia Universidad Católica*, Santiago, Chile.
- Lecturer for the Computers Architecture Lab course. Theoretical/practical content involving embedded systems, such as msp430 devices and embedded boards (BeagleBoard, Gumstix, RaspberryPi). Redesigned the course to include more up-to-date hardware.

June 2010 - August 2011
Researcher, *Biomedical Imaging Center*, Santiago, Chile.
- Designed and implemented a new processing technique that generates better fat and water images in MRI.
- Published my work on the most prestigious magazine of the subject.

Winter 2010
Software Engineering Intern, *Infosys*, Bangalore, India.
- Developed a business plan regarding the mobile advertising industry. Worked on the technical guidelines for the implementation.

Other projects

- Co-founded the Mobile and Embedded Computing Laboratory, PUC. The purpose of this laboratory is to give students a hands-on experience on mobile apps development.
- Participant at the Sika Solar Experience. Sponsored trip to photovoltaic plants throughout Europe.

Computer Skills

Languages:	C (2.5 years), Matlab (4 years), Python (2.5 years), Bash (2 years)
Hardware:	PCB layout experience, RF Design. Protocols: SPI, I2C, USB, UART.
Microcontrollers:	msp430 (2 years), AVR ATmega (1 year), PIC 16 and 18 (2 years), ARM Cortex M4 (1 year)
Platforms:	Mac OS X and Ubuntu Linux (intermediate)
Misc:	Image Processing, sensor integration.

Awards

- Best Graduated Electrical Engineer, *Chilean College of Engineers*, June 2012
- Magna Cum Laude Award, *ISMRM*, May 2012
- Best Electrical Engineering Thesis, *EE Department, PUC*, September 2011
- Best Intern, *Infosys*, March 2010