

# When will we see Aarch64 in the top-ten supercomputers?

56  
1-3 Years

26  
3-5 Years

10  
5+ Years

8  
Never



# What do you see as the biggest challenge to Arm in taking the lead in HPC?



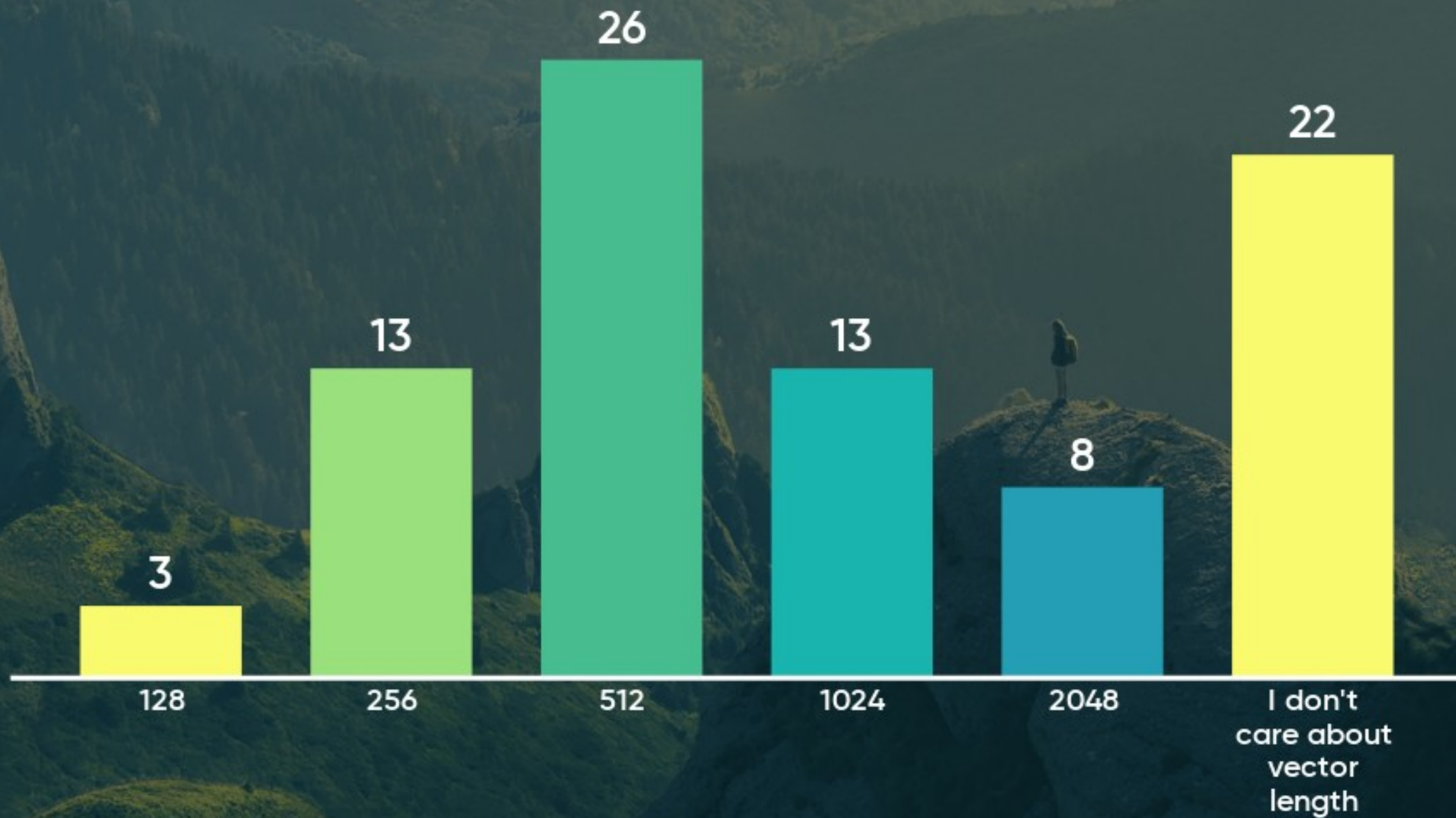


# What characteristics are most performance critical for your applications?





# What is the best vector length?





# What do you want to ask the panel?

What (if any) role will accelerators have in your clusters? What kind of accelerator's do you favor and why?

What can ARM do in HPC to address beyond Exascale?

Are you HPC Guru?

Why do you think that ARM had any advantage when Intel and AMD have great micro architectures for x86?

None

If you had a magic wand, what one thing change about the Arm HPC ecosystem?

What type of benefits unique to arm makes arm a competitive choice in the Hpc space?

Where can I buy an Arm Development Workstation?  
Hint: Avantek Computer :)

Can you provide more detail about the status of IB on ARM?



# What do you want to ask the panel?

When do you expect a 2048 bit SVE enabled chip to be created?

What's the biggest technological advantage of ARM64 over x86-64?

Who will win the World Series in 2020?

what kind of parallel application workload do you think best fit to ARM architecture?

Which one of you is @HPC\_Guru?

How they find the breadth of Arm HW offerings (platforms, platforms, etc.) and what would they like to see next?

What are the driving factors for legacy x86 based application to be ported to ARM?

How can you overcome the small cache size.

Do we actually want a Cambrian explosion in HPC hardware ecosystem? The value of the cloud is ease of use, how do we maintain this, whilst facilitating hardware specialisation.



# What do you want to ask the panel?

If HPC is a small slice of the server market, is it actually important or good to have a diverse choice of Arm CPUs being successful?

With the variety of Arm chip vendors, how specialized will the HPC Arm chips become? CFD-Arm? AI-Arm? Graph-Arm? High-speed-trading-Arm?

do we need to have arm based desktop to test hpc programs on container?

Pick, arm+sve, arm+gpu, arm+sve+gpu, arm + highly specialized accelerators? All of above?

What source constraints do US Government agencies face on the procurement of HPC processors, and when will a leading edge chip be available from a US manufacturer?

For the HPC users, what would be their incentive to use ARM HPC?

More cores, HBM, and wide vector lanes sounds like Intel's KNL architecture. How is ARM's direction different?

With direct GPU/FPGA/Accelerator connectivity to SmartNIC does the CPU really matter now?

Can please elaborate about the collaboration between nVidia and Arm? So we heard about the Arm based supercomputer, but we didn't hear about the result of this collaboration. For example, will nVidia shared the nvlink with as with the power9 CPU?



# What do you want to ask the panel?

What is the performance impact to application runs on containers on Astra.

Hbm?