

A

B

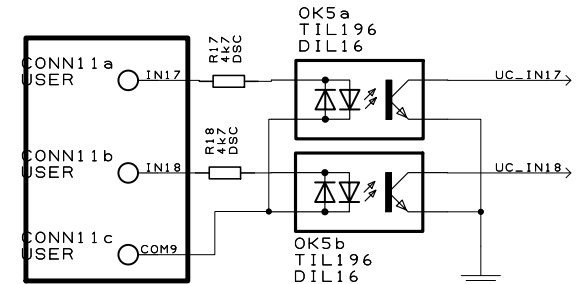
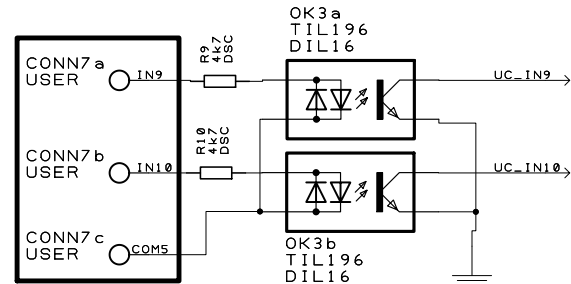
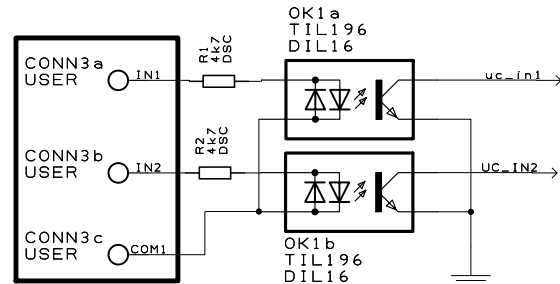
C

D

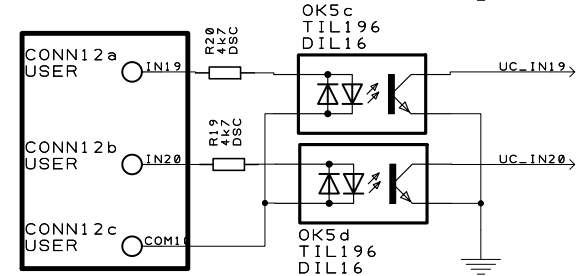
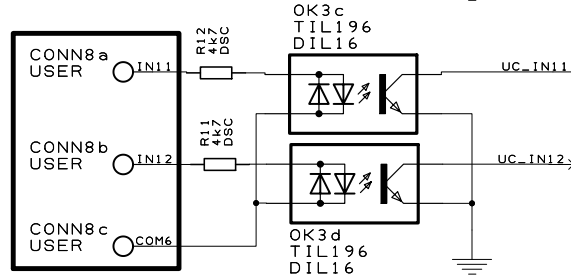
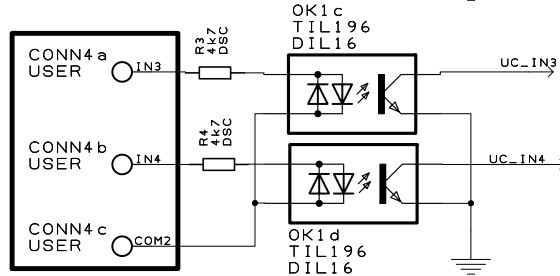
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F

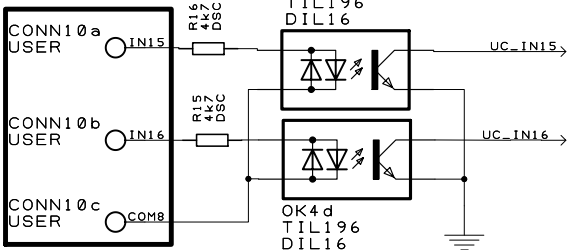
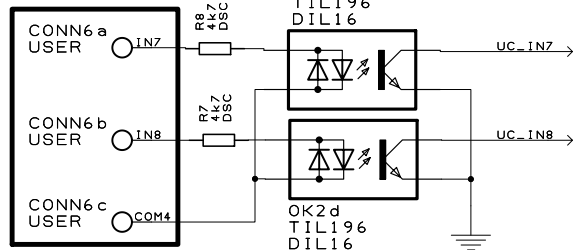
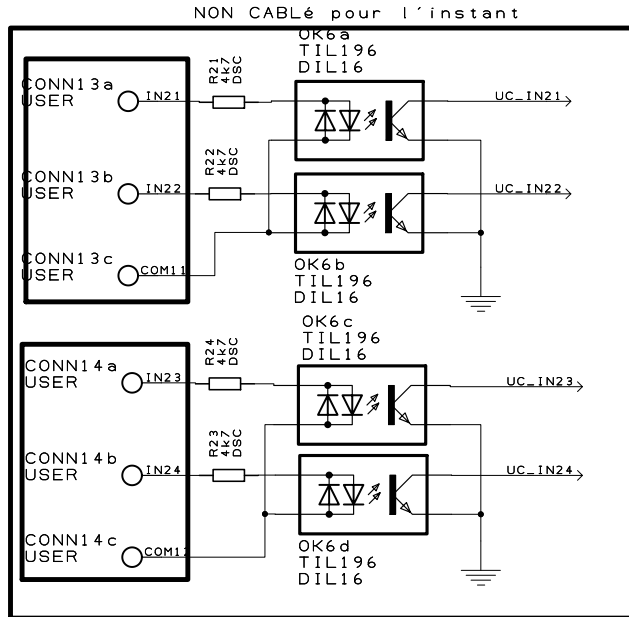
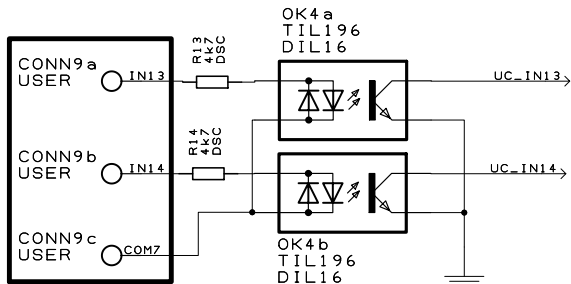
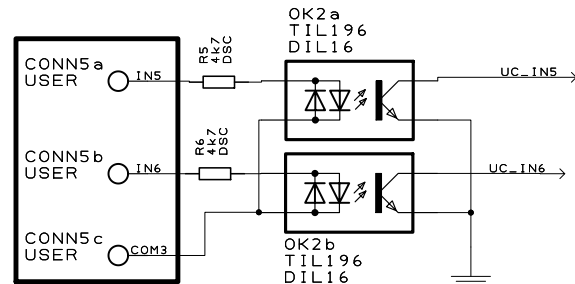
1



2



3



4

24 inputs opto-coupled

PROJECT: SAE_FLIPPER
 DRAWING TITLE: 24 INPUTS
 DRAWN BY: JLS

REVISION: 1

SHEET: 1 OF 3

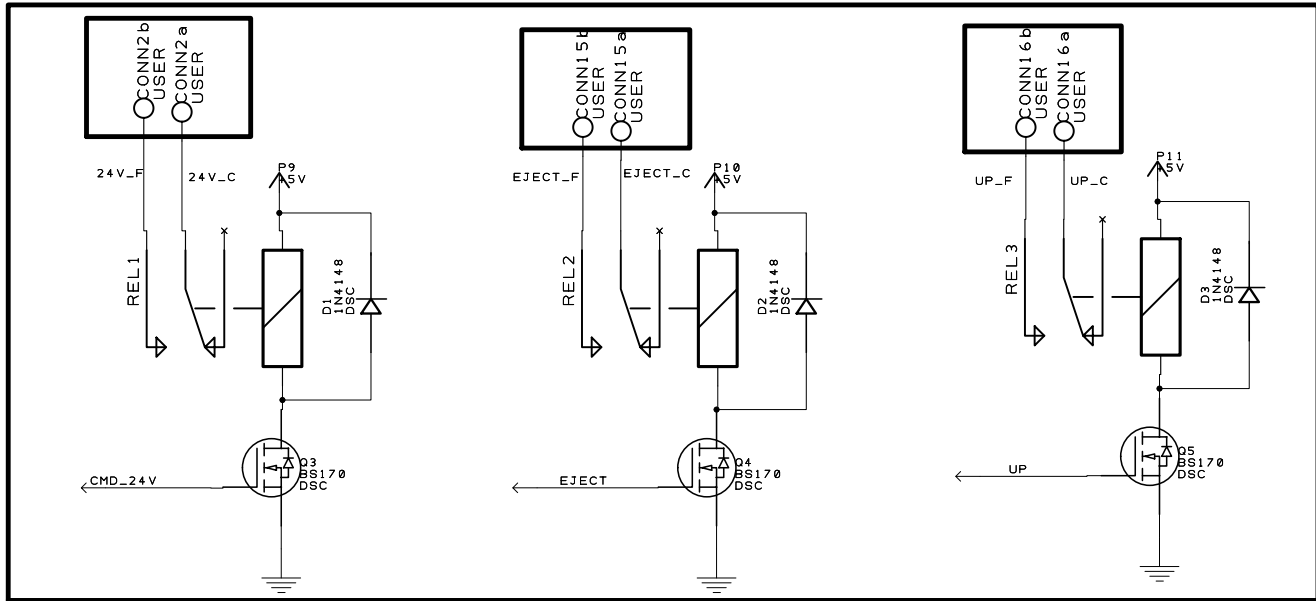
STATUS:

DATE: 02/01/2022

IUT GEII Nice

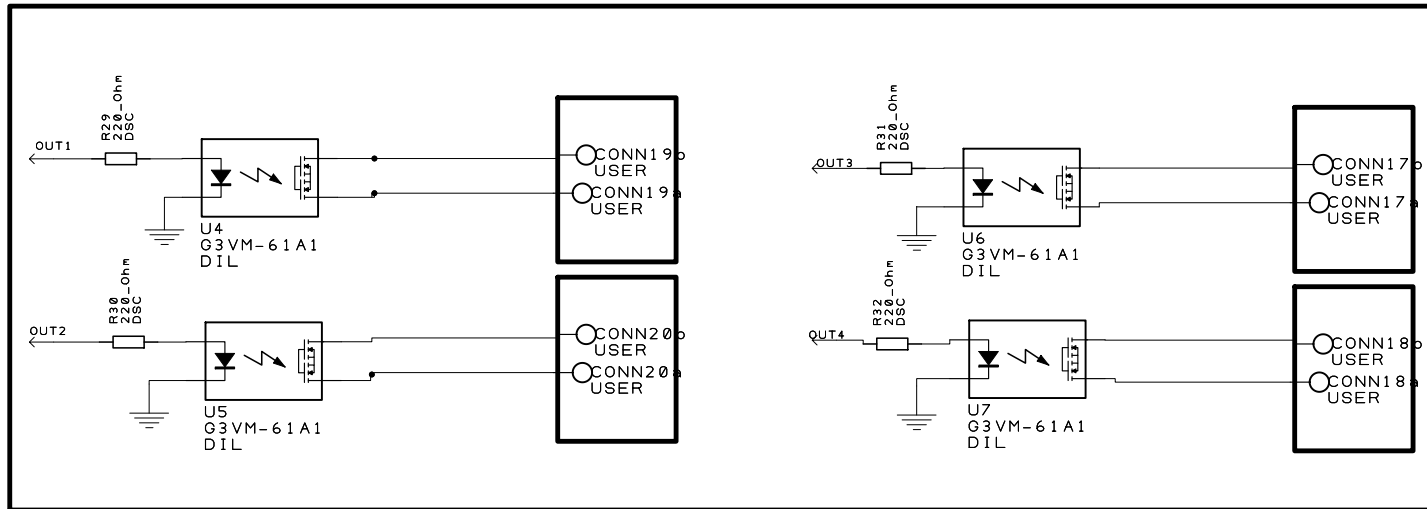
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3 relais de puissance 10A



2

4 relais statiques 60V 500mA

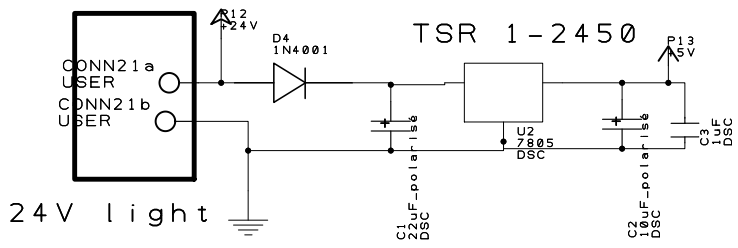


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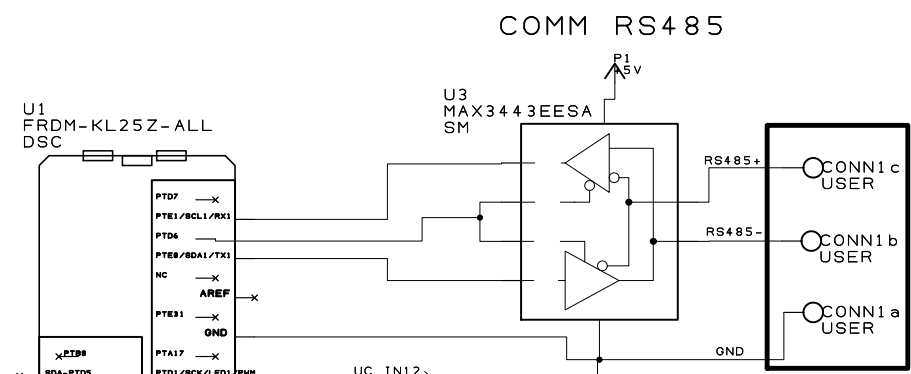
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| PROJECT: SAE_FLIPPER | | REVISION: 01 |
| DRAWING TITLE: OUTPUTS | | |
| DRAWN BY: JLS | | STATUS: |
| SHEET: 1 OF 3 | | |
| DATE: 02/01/2022 | | IUT GEII Nice |

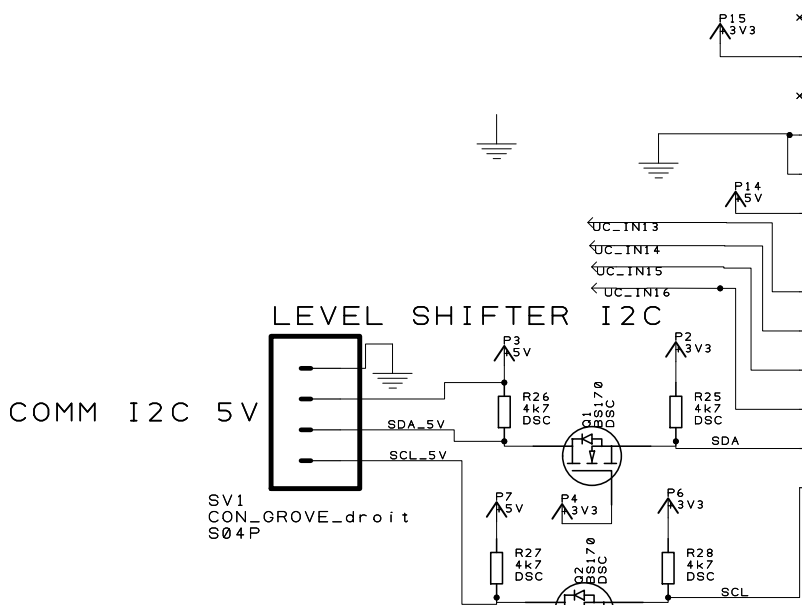
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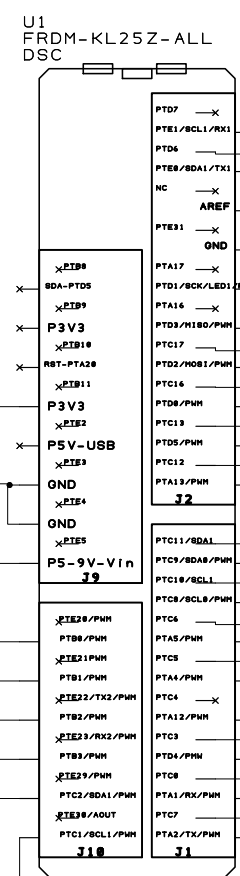
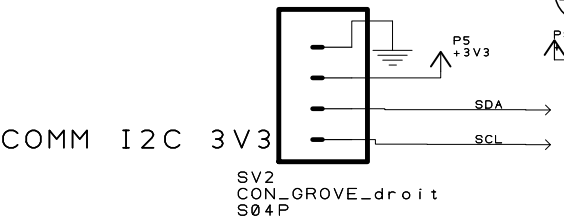
2



3



4



for all inputs
need FRDM mode pull-up

| | | |
|---------------------------------|---------------------|---------------------|
| PROJECT: SAE_FLIPPER | | |
| DRAWING TITLE: UC+ POWER | | |
| DRAWN BY: JLS | REVISION: 01 | |
| SHEET: 3 | OF 3 | STATUS: |
| DATE: 02/01/2022 | | IUT GEI Nice |