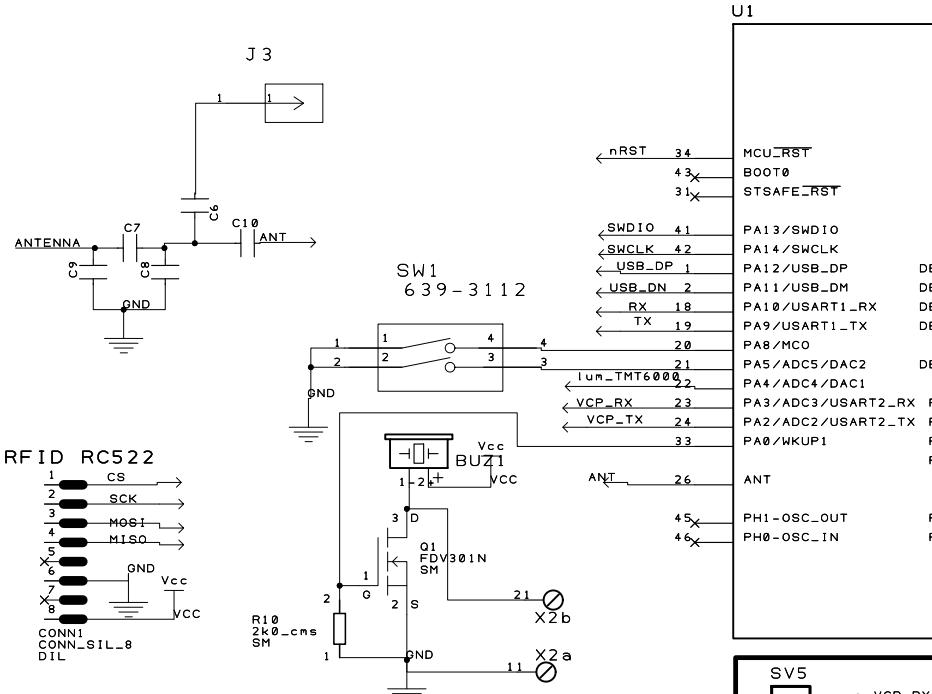


Charger battery and VCC



J2 Pinout:

Pin	Function
1	Vcc
2	SWDIO
3	GND
4	SWCLK
5	GND
6	SWO
7	GND
8	nRST
9	GND
10	X0
11	VCP_RX
12	nRST
13	GND
14	VCP_TX

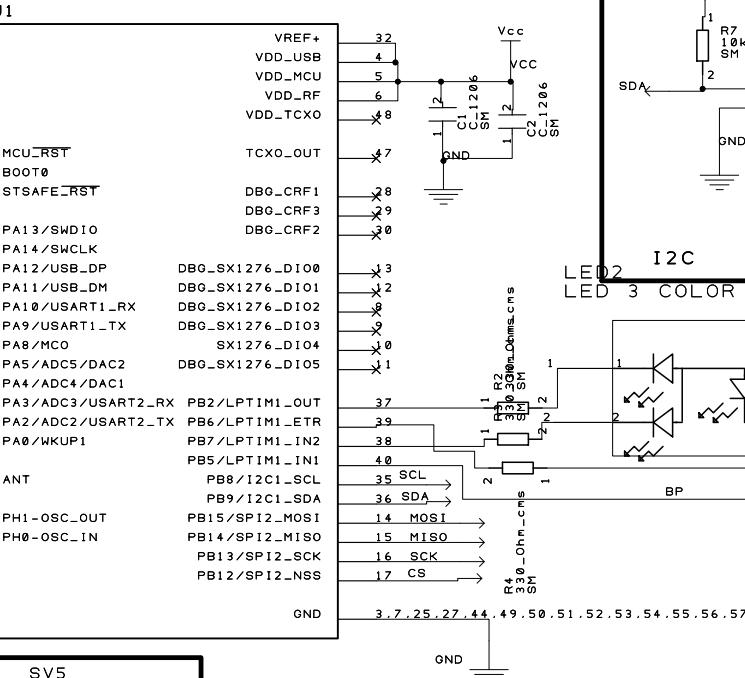
Debug et UART

debuc STLINK-V3MINIE

SV1

4.4	nRST
33	SWDIO
22	SWCLK
11	GND

from NUCLEO/STLINK

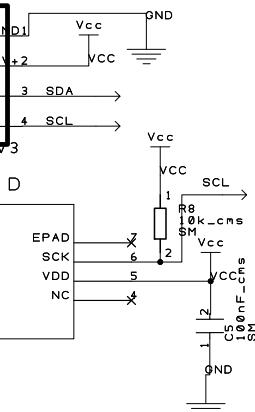


The diagram illustrates the pinout for two serial ports, UART2 and UART1. Both ports share the same pin assignments:

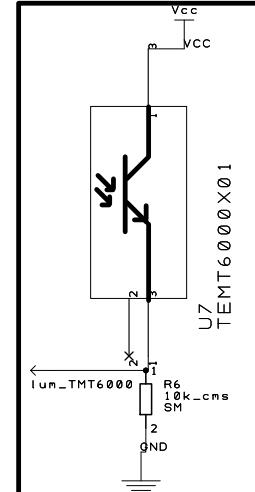
- Pins 3.3, 22, and 11 are connected to the VCP pins (RX and TX).
- Pins 3.3, 22, and 11 are also connected to ground (GND).

PROJECT:
DRAWING TITLE:
DRAWN BY:

REVISION



capteur lum analog



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