

A

B

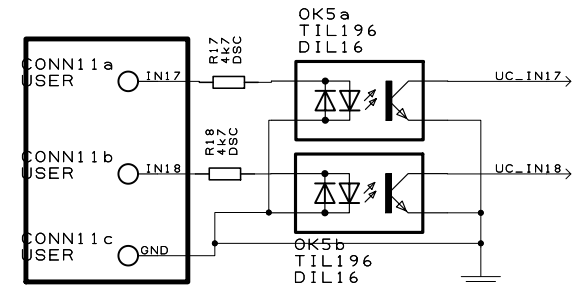
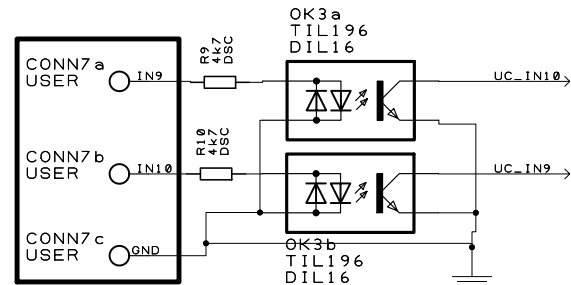
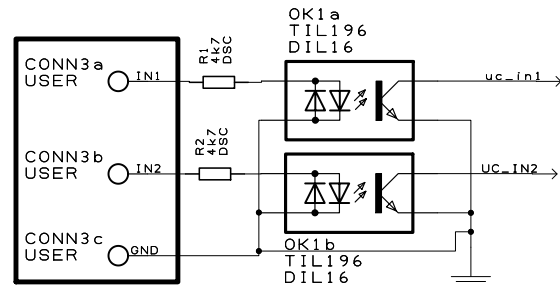
C

D

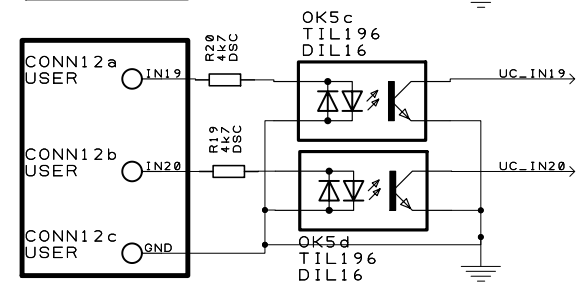
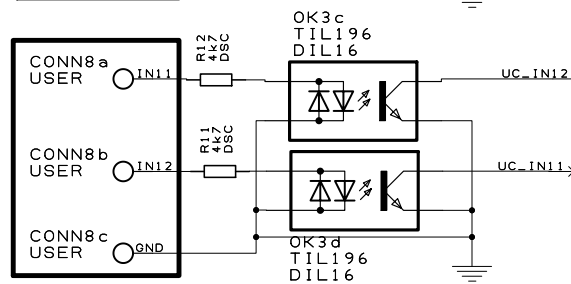
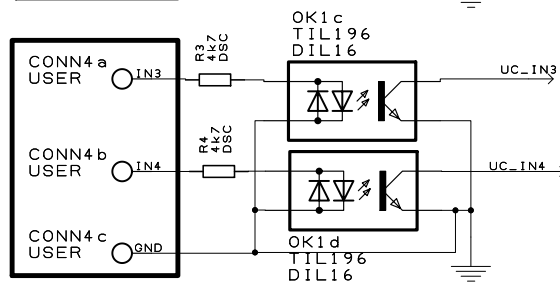
E

F

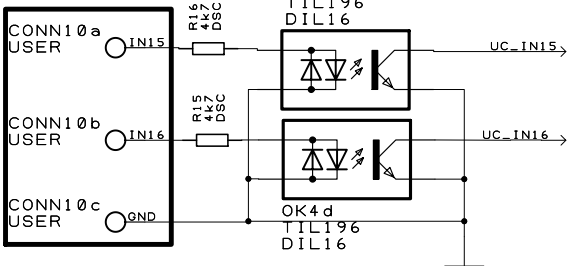
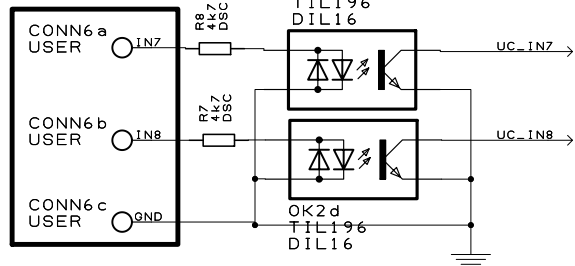
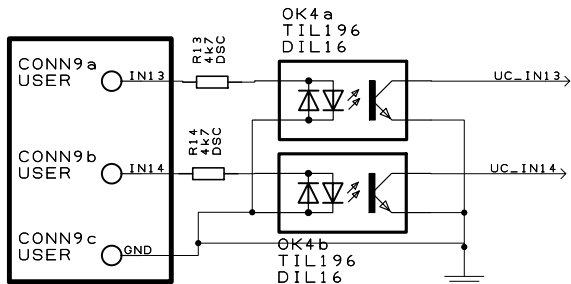
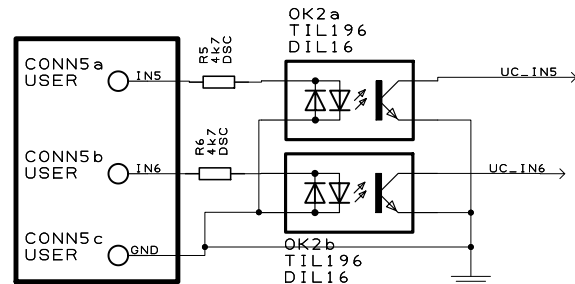
1



2



3



20 inputs opto-coupled

4

PROJECT: SAE_FLIPPER
 DRAWING TITLE: 24 INPUTS
 DRAWN BY: JLS

REVISION: 2

SHEET: 1 OF 3

STATUS:

DATE: 02/01/2022

IUT GEII Nice

1

2

3

4

A

B

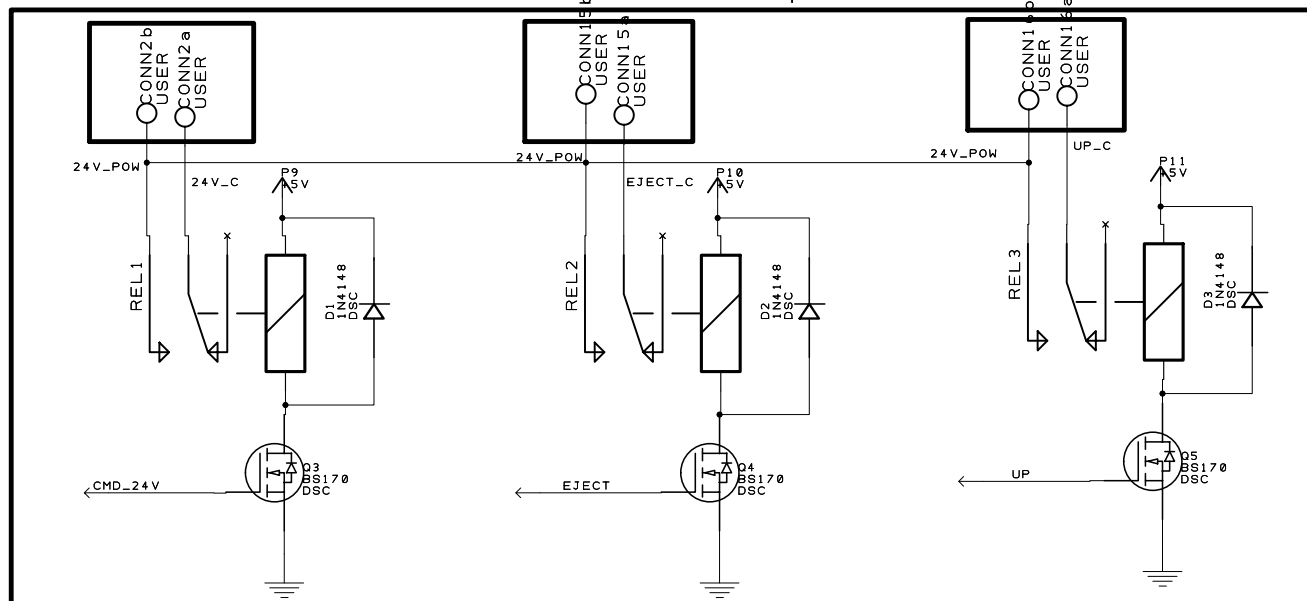
C

D

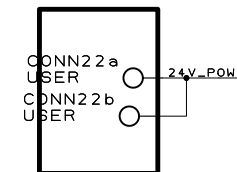
E

F

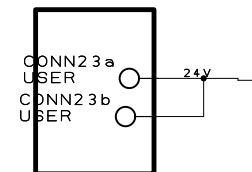
3 relais de puissance 10A



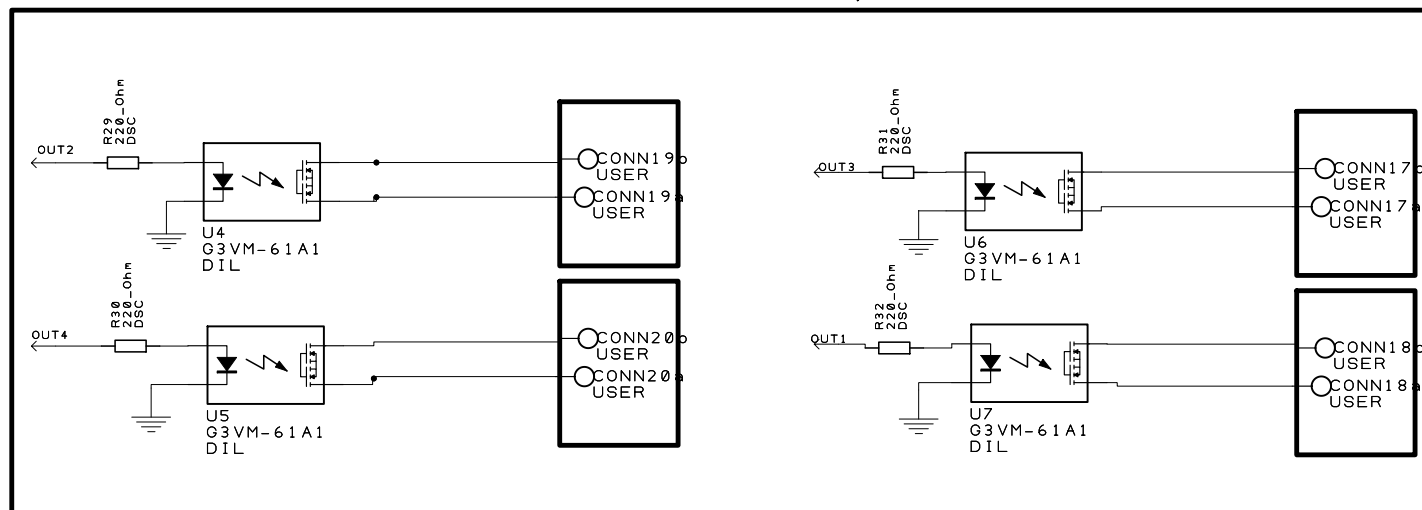
24 V_POWER



24 V_1 A



4 relais statiques 60V 500mA



PROJECT: SAE_FLIPPER
DRAWING TITLE: OUTPUTS
DRAWN BY: JLS

REVISION: 02

SHEET: 1 OF 3

STATUS:

DATE: 02/01/2022

IUT GEII Nice

A

B

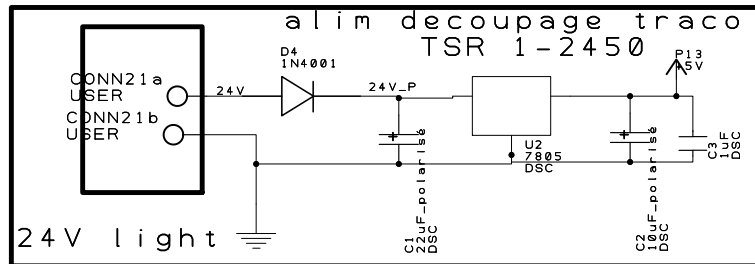
C

D

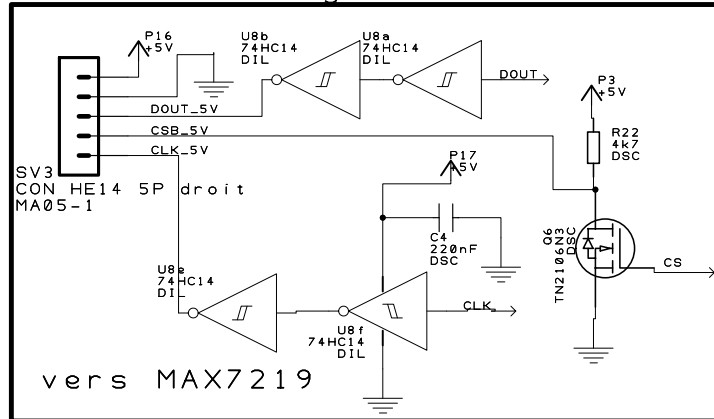
E

F

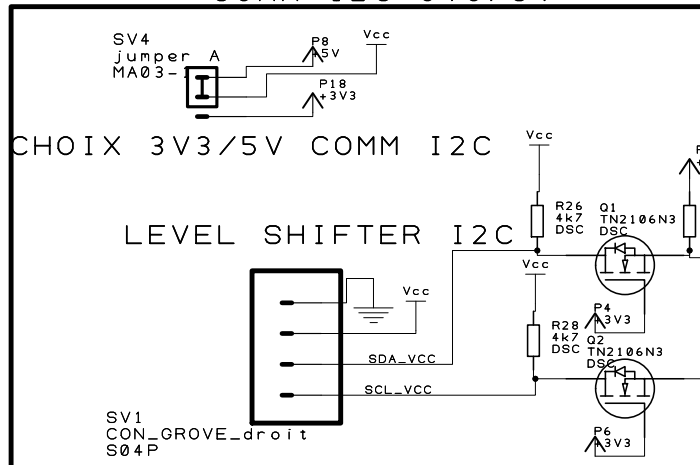
5V/1A



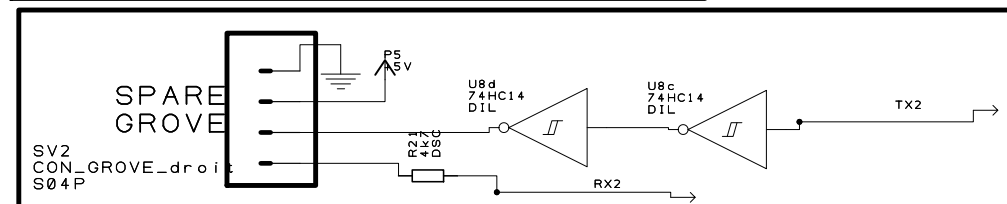
affichage SPI 8x8



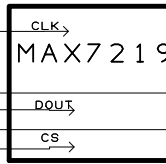
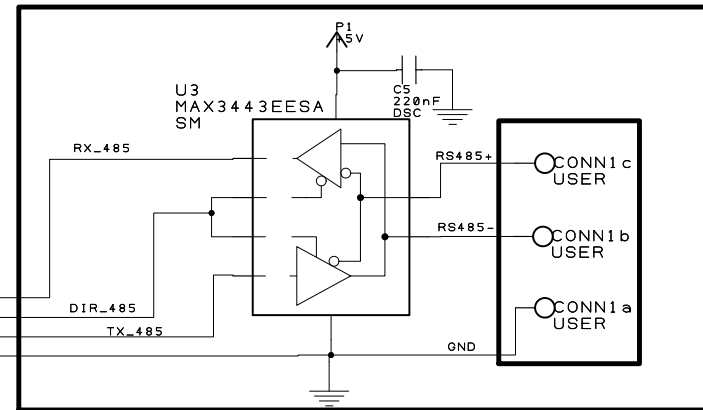
COMM I2C 3V3/5V



TO WS2812B AFF



COMM RS485 vers automate



to flip_outputs.sch

for all inputs
need FRDM mode pull-up
to flip_inputs.sch

to flip_outputs.sch

PROJECT: SAE_FLIPPER
DRAWING TITLE: UC+ POWER
DRAWN BY: JLS

REVISION: 03

SHEET: 3 OF 3 STATUS:

DATE: 02/01/2022

IUT GEII Nice