

## TSPi Quality Plan - Form SUMQ

Name **ECOS**  
 Team **EAIT**  
 Assembly **SYSTEM**

Date **10/05/2014**  
 Instructor **Luis Daniel Ben**  
 Cycle **2**

### SYSTEM Percent Defect Free

	Actual
In Compile	100,0%
In Unit Test	100,0%
In Build and Integration Test	100,0%
In System Test	90,9%
In Acceptance Test	100,0%
In Product Life	100,0%

### Defects/Page

	Plan	Actual
REQ Inspection	0,00	0,00
HLD Inspection	0,00	0,00

### Defects/KLOC

	Plan	Actual
DLD Review	0,39	2,48
DLD Inspection	0,12	22,33
Code Review	0,04	0,00
Compile	0,01	0,00
Code Inspection	0,01	4,96
Unit Test	0,90	0,00
Build and Integration Test	0,08	0,00
System Test	0,02	4,96
Total Development	2,85	67,00
Acceptance Test	0,00	0,00
Product Life	0,00	0,00
Total	2,86	67,00

### Defect Ratios

	Plan	Actual
DLD Review/Unit Test	0,43	0,00
Code Review/Compile	4,67	0,00

### Development Time Ratios

	Plan	Actual
REQ Inspection/Requirements	0,00	0,00
HLD Inspection/High-Level Design	1,00	0,00
Detailed Design/Code	0,00	0,33
DLD Review/Detailed Design	0,00	16,00
Code Review/Code	0,00	0,00

### Inspection/Review Rates

	Plan	Actual
REQ Inspection	0,00	0,00
HLD Inspection	0,00	0,00
DLD Review	0,00	25,19
DLD Inspection	0,00	0,00
Code Review	0,00	0,00
Code Inspection	269,00	201,50

### A/FR

	Plan	Actual
	0,25	3,00

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## Phase Yields

	Plan	Actual
Planning	0%	0%
Requirements	0%	0%
System Test Plan	0%	0%
REQ Inspection	70%	0%
High-Level Design	0%	78%
Integration Test Plan	0%	0%
HLD Inspection	70%	0%
Detailed Design	0%	31%
DLD Review	70%	9%
Test Development	0%	0%
DLD Inspection	70%	90%
Code	0%	20%
Code Review	70%	0%
Compile	50%	0%
Code Inspection	70%	50%
Unit Test	90%	0%
Build and Integration Test	80%	0%
System Test	80%	100%
Acceptance Test	65%	0%

## Process Yields

	Plan	Actual
% Before Compile	99%	85%
% Before Unit Test	100%	93%
% Before Build and Integration Test	97%	93%
% Before System Test	99%	93%
% Before Acceptance Test	100%	100%

## Defect Injection Rates (Defects Injected Per Hour)

	Plan	Actual
Planning	0	0,00
Requirements	0,25	0,00
System Test Plan	0	0,00
REQ Inspection	0	0,00
High-Level Design	0,25	9,00
Integration Test Plan	0	0,00
HLD Inspection	0	0,00
Detailed Design	0,75	14,00
DLD Review	0	0,00
Test Development	0	0,00
DLD Inspection	0	0,00
Code	2	1,33
Code Review	0	0,00
Compile	0,3	0,00
Code Inspection	0	0,00
Unit Test	0,067	0,00
Build and Integration Test	0	0,00
System Test	0	0,00

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### Defect Removal Rates

	Plan	Actual
Planning	0,00	0,00
Requirements	0,00	0,00
System Test Plan	0,00	0,00
REQ Inspection	0,00	0,00
High-Level Design	0,00	7,00
Integration Test Plan	0,00	0,00
HLD Inspection	0,18	0,00
Detailed Design	0,00	5,00
DLD Review	0,00	0,06
Test Development	0,00	0,00
DLD Inspection	0,00	0,00
Code	0,00	0,33
Code Review	0,00	0,00
Compile	0,00	0,00
Code Inspection	0,00	1,00
Unit Test	0,06	0,00
Build and Integration Test	0,00	0,00
System Test	0,00	0,67