

Jeremiah Mahler

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Experience

- **McLeod Institute of Simulation Sciences**

Research Assistant

Chico, CA

March 2012 – Present

- background
 - * CSU Chico Research project led by Dr. Roy Crosbie, Dr. John Zenor, Dr. Kurtis Kredo II and others.
 - * Develops FPGA based electric motor simulations.
 - * Grants from The Office of Naval Research.
- Developed PCIe interfaces for Xilinx FPGAs.
- Developed Ethernet interfaces for Xilinx FPGAs.
- Developed Matlab/Simulink interfaces for motor simulations.
- Led transition to Git for version control.
 - * Resulted in a substantial increase in productivity of the entire project.

- **Open Source Developer**

github.com/jmahler

2007 – Present

- mips-cpu - MIPS CPU implemented in Verilog.
- ldd - Linux device driver examples.
- sprinklerpi - Sprinkler control with Linux on a RaspberryPI.
- libtext-loopuptable-perl - Text based look up table operations for Perl.

- **Chico Volkswagen**

Master Technician

Chico, CA

February 2007 – May 2009

- Performed all types of automotive repair and diagnosis.
- ASE Master Certified with Advanced L1
- BAR California Certified, Basic Area 2008
- Volkswagen Certified for 2007, 2008
- Reached stage 3/5 of 2008 Volkswagen Service Qualification World Championship

Education

- **California State University Chico**

B.S. Electrical Engineering

Chico, CA

2011 – 2014

- Graduating Spring 2014.
- Nominated to Phi Kappa Phi (2012).