

MEMS Reconfigurable Broadband Patch Antenna for Conformal Applications

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Abstract—An MEMS reconfigurable broadband pixelated patch antenna is introduced for conformal applications, such as in aircrafts or vehicles. The proposed design considers structural fiberglass composite, Rohacell foam, structural epoxy, ultrathin graphite fibers, and conductive epoxy to be the materials of choice. A nonconventional method of MEMS switch installation is developed and used, which requires connecting the MEMS chips to the antenna or feed traces using very small amounts of conductive epoxy and then curing the specimen in an oven. The proposed fabrication and assembly process is a low temperature process and is not limited in terms of specimen size. The proposed reconfigurable antenna has three reconfiguration states that allow it to operate from 1.13 to 1.7 GHz with 3–7 dBi gain, excellent radiation patterns, and low cross polarization.

Index Terms—Broadband, conformal, MEMS, patch, reconfigurable, structural.

I. INTRODUCTION

ANTENNA frequency, pattern, and polarization reconfiguration have been attempted using discrete p-i-n diode, photodiode, RF MEMS, and GaAs switches [1]–[37]. Among the classes of reconfigurable antennas, the pixelated reconfigurable antenna [11], [23], [28]–[35] has drawn interests among researchers and engineers because of the massive scope or opportunity for reconfiguration.

The DARPA Reconfigurable Aperture Program allowed reconfiguring elements of metal patches to form broadside and endfire patterns [11] with the help of discrete GaAs semiconductor switches. In [28], a pixelated monopole antenna reconfigured using RF MEMS switches was introduced. In [29], discrete MEMS reconfigurable pixelated patch antennas were designed for beam direction change. In [23], a pixelated patch

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antenna, including monolithically integrated MEMS, was proposed for multifrequency reconfiguration within 3–5 GHz.

Unlike previous works on pixelated reconfigurable antennas, this paper focuses on the design and development of an RF MEMS reconfigurable, conformal patch antenna, which can be integrated into a mechanical structure and become a part of the structure, such as an aircraft wing or fuselage. Unlike traditional bolt on antennas, the proposed antenna is designed under structural geometrical constraints. Furthermore, likely for the first time, the integration of a multitude of RF MEMS switches into a structural environment to reconfigure such an antenna is proposed. Our previous works in this topic area can be found in [32]–[35].

The work presented in this paper builds on and goes far beyond our prior works. The specific advances presented as compared to our own conference papers are the experimental design and development of an MEMS reconfigurable pixelated structurally embeddable broadband patch antenna that includes the following:

- 1) a new nontraditional method of flip-chip assembly of RF MEMS switches using minute amounts of conductive epoxy that are subsequently cured in an oven;
- 2) the investigation and evaluation of the electromagnetic interference (EMI) between the dc bias traces/wires and the antenna;
- 3) the mitigation of such EMI using another new nontraditional technique of embedding ultrathin (diameter less than 0.1 mm) graphite dc bias traces into the antenna and feed substrate specimens.

These approaches together demonstrate that an MEMS reconfigurable structurally embeddable antenna can be built for wide contiguous frequency reconfiguration (1.13–1.7 GHz) and good gain (3–7 dBi). The antenna has an effective volume of $0.3 \lambda \times 0.26 \lambda \times 0.08 \lambda$, where λ is the free-space wavelength corresponding to the lowest frequency of operation.

This paper is organized as follows. First, the antenna geometry and reconfiguration states are described followed by the rationale for the choice of materials and device to develop a structurally embeddable antenna. Second, reconfigurable antenna fabrication and integration techniques are presented and their associated outcomes and challenges are discussed. Experimental results of a structural MEMS reconfigurable antenna are presented. Third, the performance degradation of the reconfigurable antenna caused by dc bias traces and wires

is investigated. Finally, the results of an experimental prototype of a reconfigurable antenna containing RF MEMS switches and graphite dc bias traces are presented that demonstrate performance over the frequency band outlined above.

II. ANTENNA CONFIGURATION, MATERIALS, AND DEVICE INTEGRATION

A. Antenna Configuration

Fig. 1 shows the general geometry of the reconfigurable structural antenna. It contains three substrates. Substrate 1 (1.6 mm thick fiberglass epoxy) contains a slot and a $50\ \Omega$ microstrip feedline. The feedline contains two MEMS switches that control two matching stubs [see Fig. 1(c)] that will be described later. The middle substrate, i.e., substrate 2 is a 17 mm thick Rohacell foam core ($\epsilon_r = 1.06$ and $\tan\delta = 0.0003$). Substrate 3 (0.4 mm thick fiberglass epoxy) contains conducting pixels that can be joined or disconnected to achieve the various antenna frequency reconfiguration states. The top, middle, and bottom substrates are bonded together using thin (1–2 mil) layers of structural epoxy. A fourth dielectric material which will be a superstrate (also fiberglass) would eventually be needed to be placed on a finished antenna, e.g., on top of substrate 3 [34]. It is called the outer mold line for a structurally embeddable antenna.

Substrate 1 contains 11×9 conducting copper pixels each $5\text{ mm} \times 5\text{ mm}$. Each pixel is separated from its neighbor by a 2.5 mm distance. The slot on the ground plane of substrate 1 is 45 mm long and 4.5 mm wide for the frequency of interest. The inner 9×7 pixels (shown in green color) are all connected to each other by 1 mm wide copper bridges. There are four rows of pixels that are shown in black color that can be reconfigured using four rows of MEMS switches [see Fig. 1(b)]. With no switches ON, the inner 9×7 pixels create the Highband configuration of the pixel patch. With Rows 1 and 2 of switches ON, the antenna achieves its Midband configuration. Finally, with all four rows of switches ON, the antenna achieves its Lowband configuration. For the Highband configuration, the two switches on the feedline remain OFF [see Fig. 1(c)]. For the Midband configuration, the first switch that is near the feedline is turned ON. And finally for the Lowband configuration, both switches on the feedline are turned ON. In total, substrate 1 contains 2 MEMS and substrate 3 contains 36 MEMS.

The substrate materials, their thicknesses, the pixels, their sizes, the slot size, and the stub dimensions were optimized using HFSS simulations considering ideal switches in our prior work [33], [34]. A summary of all materials and dimensions is listed in Table I.

B. Materials, Devices, and Their Assembly or Integration

Fiberglass composites have been reported to develop structural antennas that form sandwich structures [38]. A dielectric substrate that has similar RF properties compared with fiberglass composites is FR4 ($\epsilon_r = 4.4$ and $\tan\delta = 0.02$). Since FR4 is readily available, it was selected to fabricate the proposed antenna. RF MEMS (RMSW101) switches were considered for the proposed reconfigurable antenna because

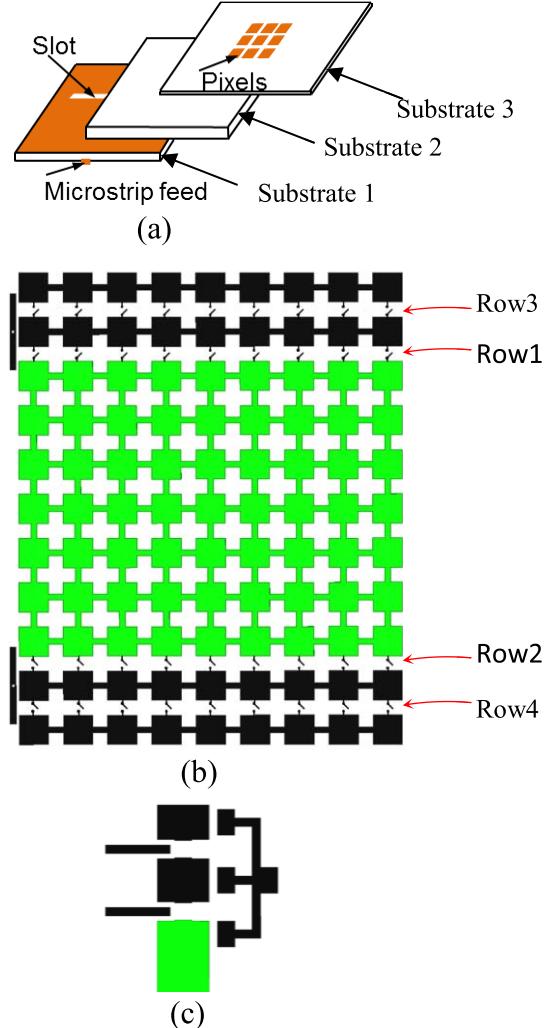


Fig. 1. General geometry of the MEMS reconfigurable structural antenna. Reconfigured pixels are shown in black color. (a) Stack-up. (b) Pixels. (c) Stubs.

TABLE I
MATERIALS AND THEIR PROPERTIES

Item	Type	Thickness	Properties
Substrate 1	FR4	1.6 mm	$\epsilon_r = 4.4$, $\tan\delta = 0.02$
Substrate 2	Rohacell	17 mm	$\epsilon_r = 1.06$, $\tan\delta = 0.0003$
Substrate 3	FR4	0.4 mm	$\epsilon_r = 4.4$, $\tan\delta = 0.02$
Switch	RMSW101	N/A	0.2 dB insertion loss, 40 dB isolation
Conductive Epoxy	EPO-TEK H20E	N/A	N/A

of their low insertion loss (0.2 dB) and high isolation (greater than 20 dB) [40].

The RMSW101 switch was installed and integrated to the antenna and feedline traces using the flip chip method using conductive epoxy (EPO-TEK H20E). The substrate containing all switches was then placed in an oven and cured at 120 °C for 1 h. The details on the materials, devices, and switch integration can be found in [39].

DC bias voltage is supplied to the RF MEMS through four dc busbars, which are located on the bottom of the patch

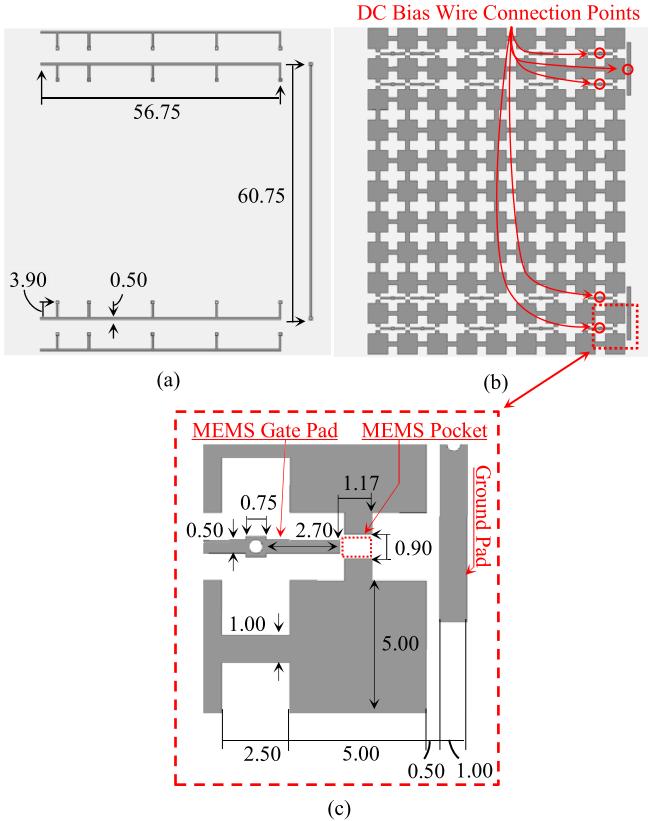


Fig. 2. Pixel patch substrate trace dimensions. (a) Underside of substrate with dc busbars. (b) Upper surface of substrate with pixel patch traces. (c) Area of pixel patch showing traces in the vicinity of the MEMS pocket.

substrate or substrate 3. This is shown in Fig. 2. Each busbar biases all switches in that row, where the busbar is connected to the individual gate pads of the MEMS through conducting vias. Utilizing the busbars this way allows all 36 RF MEMS to be controlled through a total of five dc bias wires (see Fig. 3), one wire for each row of RF MEMS, and one ground wire which is connected to both ground pads through the dc ground busbar. There are five $40\text{ k}\Omega$ dc bias resistors for the four rows of switches on substrate 1. Similarly, there are $3\ 40\text{ k}\Omega$ dc bias resistors for the MEMS on the feed substrate.

III. RESULTS

A. Experimental Antenna Prototype 1 and Measured Results

To ensure that the electrical connection of RF MEMS using conductive epoxy is acceptable, several $50\ \Omega$ microstrip transmission line specimens were fabricated on 1.6 mm thick FR4 substrates, where MEMS switches could be installed using the above technique. A dummy transmission line containing no switch was also fabricated and measured. The maximum, per switch, insertion loss measured was between 0.075 and 0.2 dB, which is no worse than the insertion loss measured using wire bonding methods for MEMS [40]. The measured return loss was better than 20 dB, while the isolation was more than 40 dB.

Based on the methods and procedures described above, a prototype reconfigurable antenna was fabricated and tested. We will refer to this antenna as Antenna Prototype 1.

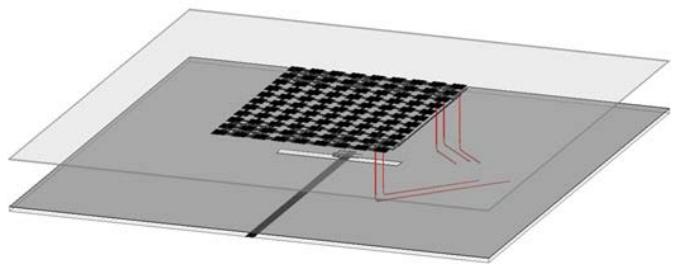


Fig. 3. Illustrating the five dc bias wires in red color. The foam substrate is hidden to show the wires.

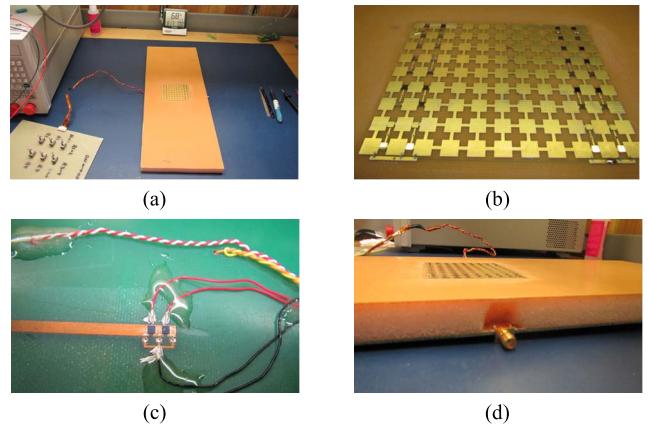


Fig. 4. Photographs of Antenna Prototype 1. (a) Structural antenna connected to switch control board. (b) Pixelated patch with MEMS. (c) Feedline with dc bias wires and MEMS. (d) Side view of structural antenna.

Photographs of Antenna Prototype 1 (470 mm \times 178 mm \times 19 mm) can be seen from Fig. 4. To control the MEMS switches, an external circuit board with toggle switches was also fabricated [see Fig. 4(a)].

Measured S11 versus frequency data of Antenna Prototype 1 is shown in Fig. 5. It is clear that the antenna has excellent S11 performance and bandwidth in the Highband (-10 dB bandwidth of 1.3–1.7 GHz or 26.7%). When the switches in Rows 1 and 2 (see Fig. 1) are turned ON by applying 90 V to the MEMS in those rows, the antenna reconfigures to the Midband shown using the red trace in Fig. 5. Antenna state reconfiguration from Highband to Midband is clear, but the S11 results are poor. The S11 is less than -10 dB only within a very narrow frequency range. Similar situation arises when all of the rows of switches are turned ON. The antenna does reconfigure to the Lowband as seen from the blue trace but again has poor S11 performance. Clearly, the Midband and the Lowbands have problems with impedance matching to the $50\ \Omega$ feedline.

Measured gain and radiation patterns of this antenna are shown in Figs. 6 and 7, respectively. Gain and patterns were measured for a few inside a Satimo anechoic chamber. Since there were three reconfiguration states that constituted three separate measurements, we selected fewer data points to conserve measurement costs. Gain versus frequency data was curve fitted in MATLAB to generate the plots shown in Fig. 6 where the actual measured gain data appear as discrete points on the curves. As seen, the gain results follow

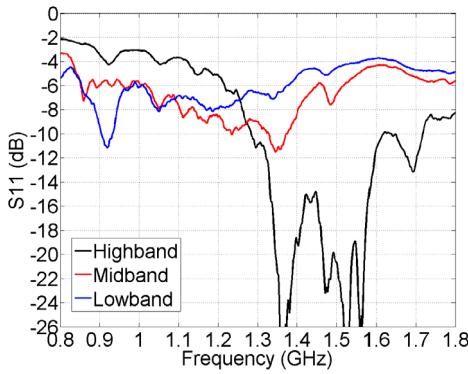


Fig. 5. Measured S11 as a function of frequency for Antenna Prototype 1.

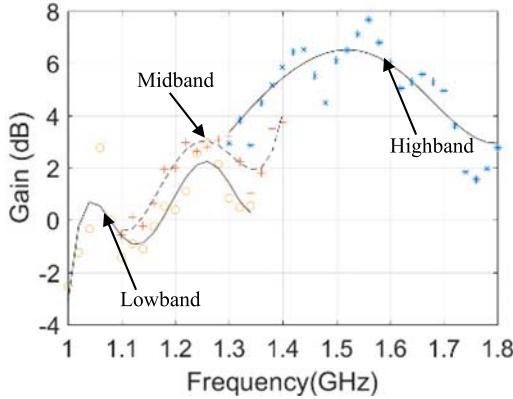


Fig. 6. Measured gain of Antenna Prototype 1.

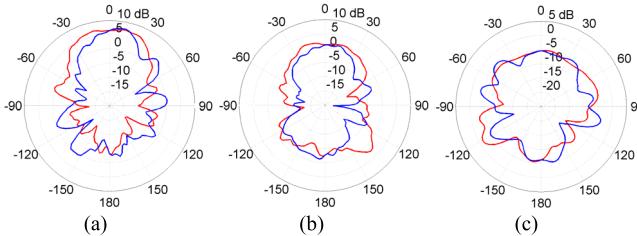


Fig. 7. Measured radiation patterns for Antenna Prototype 1. (a) Highband at 1.56 GHz. (b) Midband at 1.30 GHz. (c) Lowband at 1.06 GHz. Red: E-plane. Blue: H-plane.

the S11 results shown in Fig. 5. The antenna in the Highband has a gain between 3 and 7.5 dB within 1.3–1.7 GHz. Since the antenna S11 results are poor in both the Midband and Lowband, the gain plots do not exhibit clear signs of separate reconfigured bands.

The radiation patterns in Fig. 7(a) reveal that although the Highband exhibits greater than 5 dB gain, the E- and H-plane patterns show signs of deformation and asymmetry. The Midband gain is low and the patterns show asymmetry and deformation [see Fig. 7(b)]. The Lowband patterns do not appear to look like the pattern of a directional patch antenna.

It is clear from the measured results of Antenna Prototype 1 that the reconfigurable antenna had low gain, poor S11, and deformed patterns especially in the reconfigured Midband and Lowband. During the measurements of S11, it was determined that the antenna performance degradations observed were

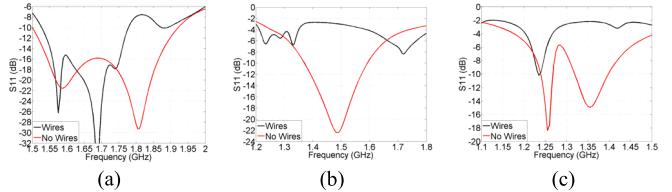


Fig. 8. Simulated S11 data showing the effect of including the dc bias wires to the antenna. (a) Highband. (b) Midband. (c) Lowband.

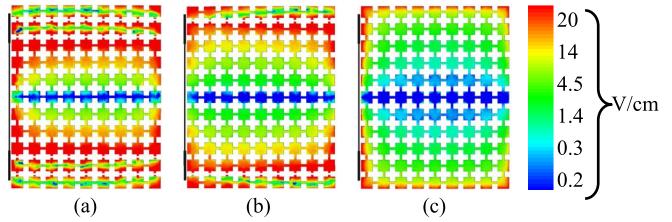


Fig. 9. Simulated electric field distributions for the pixel patch and dc bus bar traces without the introduction of dc bias wiring. (a) Highband at 1.70 GHz. (b) Midband at 1.49 GHz. (c) Lowband at 1.35 GHz.

caused by the EMI caused to the antenna by the dc bias traces and wiring. Any movement of the bias wires that were connected to the wires shown in Fig. 3 would change the S11. Our previous HFSS simulation studies [33], [34] up to the fabrication of Antenna Prototype 1 did not fully consider the effects of the presence of the dc bias traces and wires.

B. Identifying the Cause of Performance Degradation

The EMI caused by the dc bias wiring in the presence of the dc busbars was studied from an EM field point of view using HFSS. Ideal switches were considered for the model (short for ON and open for OFF). Shorts were created using copper strips. The dc bias wires were modeled using 0.25 mm diameter copper wires.

Simulated S11 plots of the Highband, Midband, and Lowband before and after including the dc bias wires are shown in Fig. 8. The Highband, although affected by the wires, still shows S11 bandwidth within −10 dB S11. The wires significantly deteriorate the Midband and Lowband, as can be seen from Fig. 8(b) and (c).

Field simulation results elucidate the deleterious effects of the dc bias wires further. Fig. 9 shows the simulated electric field distributions for the three reconfiguration states before the inclusion of the dc bias wires. Although the electric field distributions in Fig. 9 do not precisely conform to the standard electric field distribution of a microstrip patch antenna due to the pixelated antenna geometry and dc busbars on the underside of the substrate, they do show the outlines of three antennas, small, medium, and long in size. The maximum field intensity occurs mostly along the edges of the last rows of pixels intended for that reconfiguration state, and the minimum intensity occurs at the center of the pixel patch for all reconfiguration states.

Fig. 10 shows the electric field magnitude distributions after the inclusion of the dc bias wires. As seen, the Highband E-fields are deformed, but still retain some semblance to the original E-field magnitude distribution. The field distributions

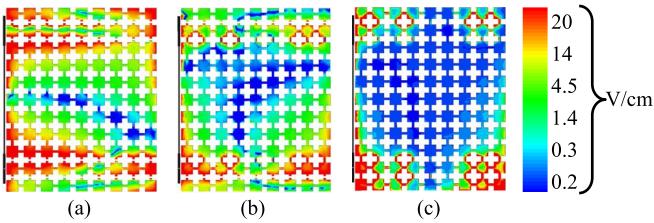


Fig. 10. Simulated electric field distributions for the pixel patch after the introduction of the dc bias wiring. (a) Highband at 1.7 GHz. (b) Midband at 1.49 GHz. (c) Lowband at 1.35 GHz.

for the Midband and Lowband are completely deformed and are no longer recognizable as the electric fields of a typical patch antenna. These detrimental effects persisted for any reasonable length of bias wires. These subsequent findings from HFSS simulations were presented in a conference paper [35]. Attempt to reduce the EMI using 40 k Ω chip resistors between the dc busbars and the wires was unsuccessful.

C. Performance Improvement Using Graphite DC Bias Traces—Simulation Results

In the literature, there are reports of RF MEMS switches being biased using dc traces made from high sheet-resistance alloys, such as Ni-Chrome [23]. Typically, these are extremely thin traces that are fabricated in a microelectronic cleanroom environment using high-temperature processes on small semiconductor substrates, e.g., high-resistivity Si. Such a method is not feasible for a structural reconfigurable antenna fabrication because composite panels require low-temperature fabrication (less than 150 °C) in order to prevent delamination. For a structural antenna, a compromise choice will be to use manually installed graphite fibers (conductivity of approximately 70 000 S/m) to provide the dc bias to the MEMS. Graphite is not an ideal choice from an RF point of view because it does not provide very high sheet-resistance. However, due to its widespread usage in composite structures and the fact that it can be installed manually led us to investigate its feasibility to fabricate the dc busbars using it.

Considering 0.1 mm diameter graphite fibers, the resistance is 46.2 Ω /in. We hypothesized that long bias lines with optimized geometry should provide high enough resistance to allow dc biasing of the MEMS while keeping the RF isolated from it. Multiple geometries and arrangements of dc bias traces using graphite were considered to optimize antenna gain and S11 performance. Attention was also paid to geometries that can be manually fabricated. Although the dc biasing using straight graphite fibers is preferred a bias layout consisting of both straight and zigzag geometries (see Fig. 11) turned out to be optimum. Notwithstanding that many other bias geometries can also be found to allow similar or perhaps a better performance. The layout shown in Fig. 11(b) comprises of a series of 10.5 mm sections of graphite fibers alternating at 45° and -45° angles to the E-plane of the antenna. The length of the fourth cycle of ±45° was shortened to 5.2 mm to account for the closer spacing of the two vias at that position.

Using the graphite busbar schemes shown and described above, simulations were conducted for the low, mid, and high bands. A Rohacell foam core was added to the new models.

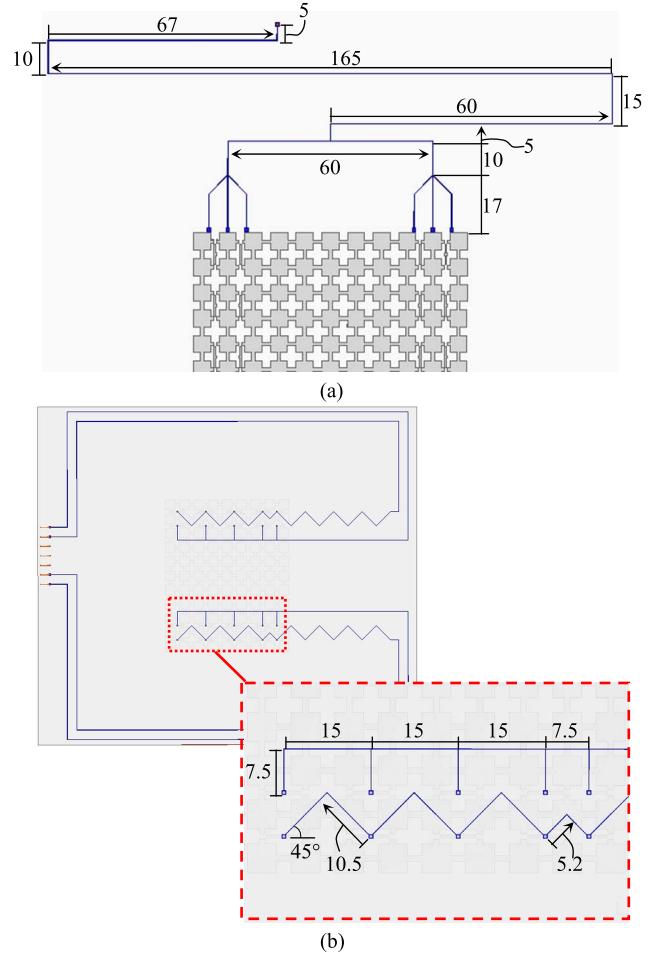


Fig. 11. Layout of the sawtooth-shaped graphite dc bias lines (all dimensions in millimeters). (a) Simplified pixel patch ground connection graphite line on the top side of the patch substrate. (b) New sawtooth arrangement for the graphite lines on the underside of the patch substrate.

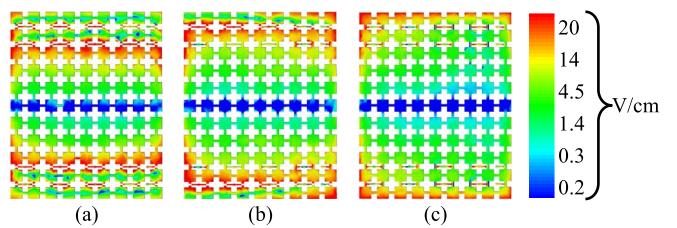


Fig. 12. Electric field magnitudes for the pixelated patch with high-impedance sawtooth-shaped graphite dc bias lines. (a) Highband at 1.6 GHz. (b) Midband at 1.4 GHz. (c) Lowband at 1.2 GHz.

The foam core was bounded on the upper and lower surfaces by 2 mil thick dielectric layers to approximate structural epoxy. The RF MEMS switches were approximated as shorts and opens to represent the ON and OFF states, respectively.

The simulated electric field magnitudes of Fig. 12 clearly show that the fields for all bands now in general conform to what is expected from the pixelated patch antenna. This confirms that the EMI effects due to copper dc bias traces and wires can be mitigated using traces made using thin graphite fibers.

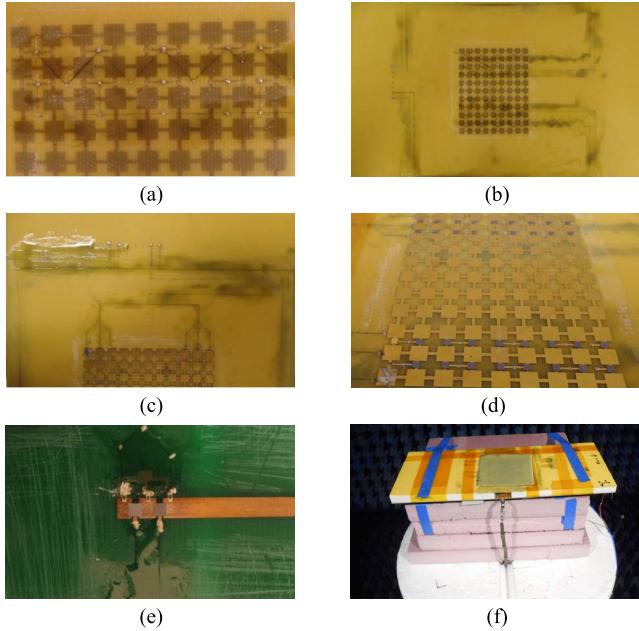


Fig. 13. Photographs of Prototype 2 with high-impedance sawtooth-shaped graphite dc bias lines. (a) Section of graphite lines on underside of patch substrate before protective coat of epoxy. (b) Underside of patch substrate with graphite lines coated in epoxy. (c) Patch ground connection with protective coat of epoxy. (d) Pixel patch with 36 MEMS installed. (e) Feedline with both MEMS installed. (f) Completed antenna on testing pedestal at WRCNC [41].

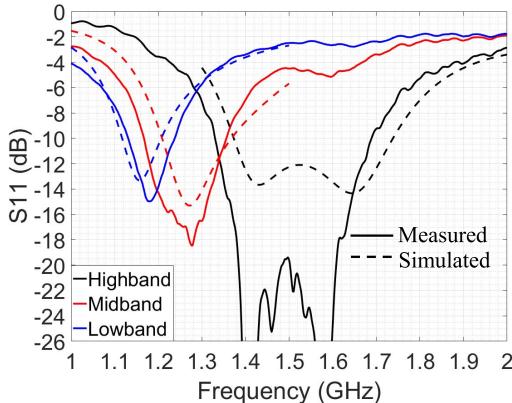


Fig. 14. Simulated and measured S₁₁ for all three reconfiguration states for the aperture coupled MEMS reconfigurable pixelated patch antenna with high-impedance sawtooth-shaped graphite dc bias lines.

D. Experimental Prototype Antenna 2 and Measured Results

An experimental prototype Antenna 2 with all the graphite dc busbars, MEMS switches, and copper wires was fabricated (see Fig. 13) and tested. Structural epoxy was used to glue the patch substrate to the foam core in the vicinity of the pixel patch.

On top of the antenna, a 2 mm thick Rohacell foam followed by a 1.3 mm thick fiberglass dielectric superstrate were added as suggested by our previous work [34].

Measured S₁₁ results for Antenna 2 are shown in Fig. 14. For comparison, simulations were also performed representing this completed specimen using HFSS. The “ON” and “OFF” states of each MEMS switch were modeled using a 2 Ω resistor and a 20 fF capacitor, respectively, to reflect the measured insertion loss and isolation of each switch described in Section III-A.

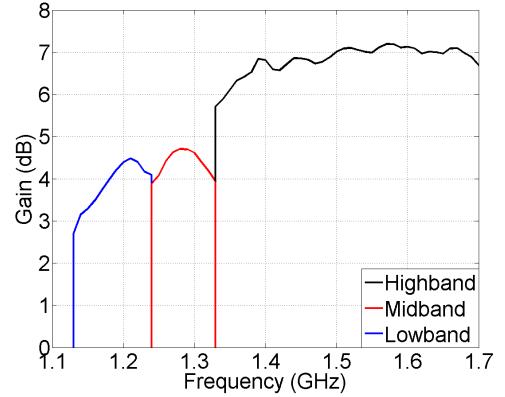


Fig. 15. Measured gain versus frequency for the reconfiguration states.

Equation (1) [42] for insertion loss or isolation was used to obtain the equivalent resistance or capacitance to represent the MEMS

$$IL = -20 \log_{10} \left| \frac{2Z_0}{2Z_0 + Z_d} \right| dB \quad (1)$$

where $Z_0 = 50 \Omega$ and Z_d is the MEMS impedance in the ON or OFF state.

Part of the structural epoxy (in the form of a film) bled into the foam while being cured. To account for that the dielectric constant of the foam adjacent to the joints was increased in the simulation model. The simulation model contained the following dielectric layers in sequence from the top: 1) 1.3 mm thick FR4 superstrate; 2) 2 mm thick foam with $\epsilon_r = 1.2$; 3) 0.05 mm thick structural epoxy with $\epsilon_r = 4.2$; 4) 0.4 mm thick FR4 (containing the pixel patch, MEMS, and dc bias); 5) 2 mm thick foam with $\epsilon_r = 2.1$; 6) 13 mm thick foam with $\epsilon_r = 1.06$; 7) 2 mm thick foam with $\epsilon_r = 2.1$; 8) 0.05 mm thick structural epoxy with $\epsilon_r = 4.2$; and 9) 1.6 mm thick FR4 (containing the slot, the microstrip feedline, MEMS, and matching stubs).

From Fig. 14, it is clear that the simulated resonances are in good agreement with the measured S₁₁ responses. From either sets of data, it can be seen that the antenna shows three clear reconfiguration states each under -10 dB S₁₁. These data are a significant leap from what was observed before in Fig. 5.

Antenna radiation patterns and gain were measured inside a Satimo anechoic chamber [41]. The gain data of each reconfiguration state are plotted at frequencies where $S_{11} \leq -9.5$ dB and are shown in Fig. 15. As seen, every reconfiguration state has gain above 3 dBi, and each reconfiguration state provides value to the antenna in terms of total frequency coverage. Clearly, the antenna has measured gain between 3 and 7 dBi from about 1.13 to 1.7 GHz, i.e., a frequency ratio of 1.5:1. As a matter of fact, gain continues beyond 1.7 GHz but was not measured because S₁₁ exceeded -9.5 dB.

Simulated gain (not shown in Fig. 15) had peak values of 6.5, 5.5, and 4.7 dBi at 1.6, 1.28, and 1.16 GHz, respectively. Measured peak gain values are 7.2, 4.7, and 4.5 dBi at 1.6, 1.28, and 1.22 GHz, respectively. Simulated minimum gain was 3.4 dBi at 1.1 GHz, while measured minimum gain was 3 dBi at 1.14 GHz.

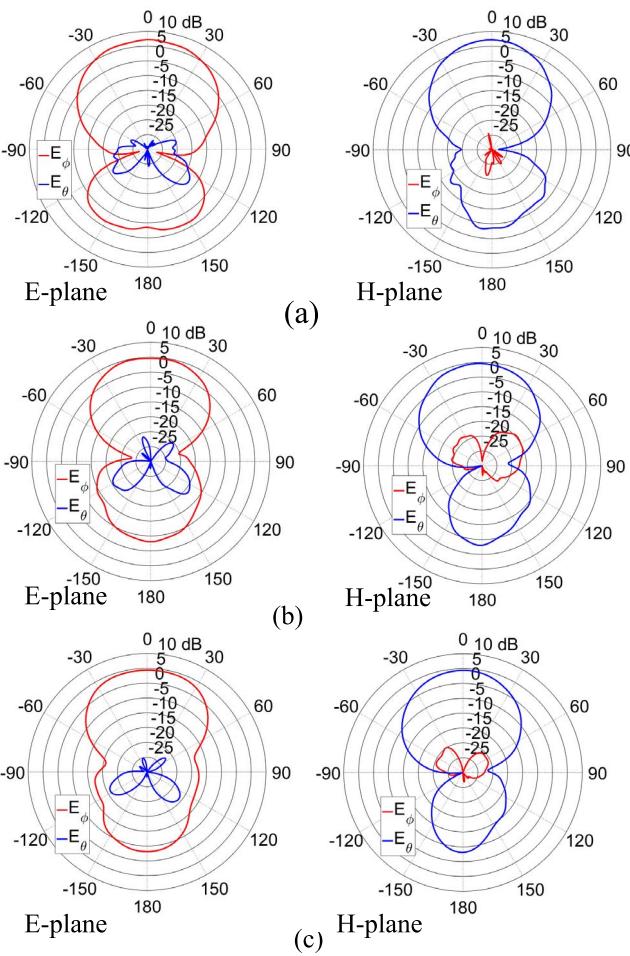


Fig. 16. Measured radiation patterns. (a) Highband at 1570 MHz. (b) Midband at 1280 MHz. (c) Lowband at 1220 MHz.

Clearly, more bandwidth can come from the high band by further optimizing the substrate thicknesses (especially the foam thicknesses), slot, and stubs. An additional high-band state can be created by adding two more rows of switches making a new 5×9 pixel state. Measured radiation patterns are shown in Fig. 16. Plots show that every reconfiguration state maintains a radiation pattern expected from an aperture coupled patch antenna over the entire span of its respective frequency range.

IV. DISCUSSION

Comparing the simulation and measurement results in Fig. 14, it can be seen that there are small frequency shifts between the two. This can be attributed primarily to the effect of the structural epoxy that bled into the foam core. Also there perhaps was some variability in the dielectric constant of the substrate material that was actually used in the fabrication compared to what was used in the simulation model. Also there were assumptions made about the nature of the graphite busbars in the simulation model which may have been slightly different from what was actually fabricated.

Also from Fig. 15, it is apparent that the Lowband is not as well separated from the Midband. It should be clear from this

paper that the bulk of our effort was placed on developing methods and techniques to realize an MEMS reconfigurable antenna that can be implemented into a structural environment. Fine-tuning antenna resonances was not a focus. Nevertheless, several approaches can be taken to make the low and midbands separated further to achieve even more bandwidth in the lower frequencies. These include redesigning the pixels, adding more pixels to the low band, redesigning the matching stubs, and increasing the thickness of the foam core.

V. CONCLUSION

This paper presents the experimental demonstration of an MEMS reconfigurable pixelated patch antenna for conformal applications such as that can be integrated into an aircraft or vehicle. The antenna design considers structural fiberglass composite, Rohacell foam, structural epoxy, ultrathin graphite fibers, and conductive epoxy to be the materials of choice. A nonconventional method of MEMS switch installation is introduced that entails connecting the MEMS chips to the antenna or feed traces using minute amounts of conductive epoxy and then curing the specimen in an oven. The whole fabrication and assembly process is a low-temperature process. Also the process is not limited in terms of specimen size.

The proposed reconfigurable antenna has three reconfiguration states that allow it to operate from 1.13 to 1.7 GHz. Within this bandwidth, the antenna has 3–7 dBi gain, good radiation patterns, and low cross polarization. The potentials to increase the bandwidth even further exist. This can be attempted by adding more reconfiguration states with the constraints that the slot length, foam height, stub dimensions, and so on are optimized.

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