

Project 5 Report

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Overview

The purpose of these experiment was to compare the properties and performance of different page replacement algorithms when used with a series of sample programs that access memory in different patterns. Gathering the test data and plotting it allowed us to make the following observations detailed in the observation section below.

Experimental Setup

We ran our programs using a bash script that called the command

```
./virtmem 100 $x <program> <algorithm>
```

and outputted the results to a text file. We then took the data from the text file and created the graphs shown in Appendix 1. These experiments was run in the linux environment on the student machines.

Our Algorithm

Our algorithm, which is similar to an LRU, could be described as *Least Often Faulted*. We implemented a counter for each page in the page table that was incremented every time a fault occurred for any given page. When the frame was filled and a fault occurred, the page fault handler would return the page in the frame with the lowest fault count as the page to be overwritten by the incoming page.

Observations

Looking at the graph to the right (Fig. 1), which is characteristic of all of our data, rand (blue) and fifo (red) had very similar performances in these experiments, both in terms of number of faults and in the relative frequency of reads and writes (Fig. 2-3). Our custom algorithm (green) began with significantly more faults, but began to beat both fifo and rand after frame size increased over 40. The custom algorithm also had a much lower frequency of faults resulting in a disk write than the other two algorithms. In general the number of reads and writes went down as frames went up because less pages had to be overwritten.

Appendix 1:

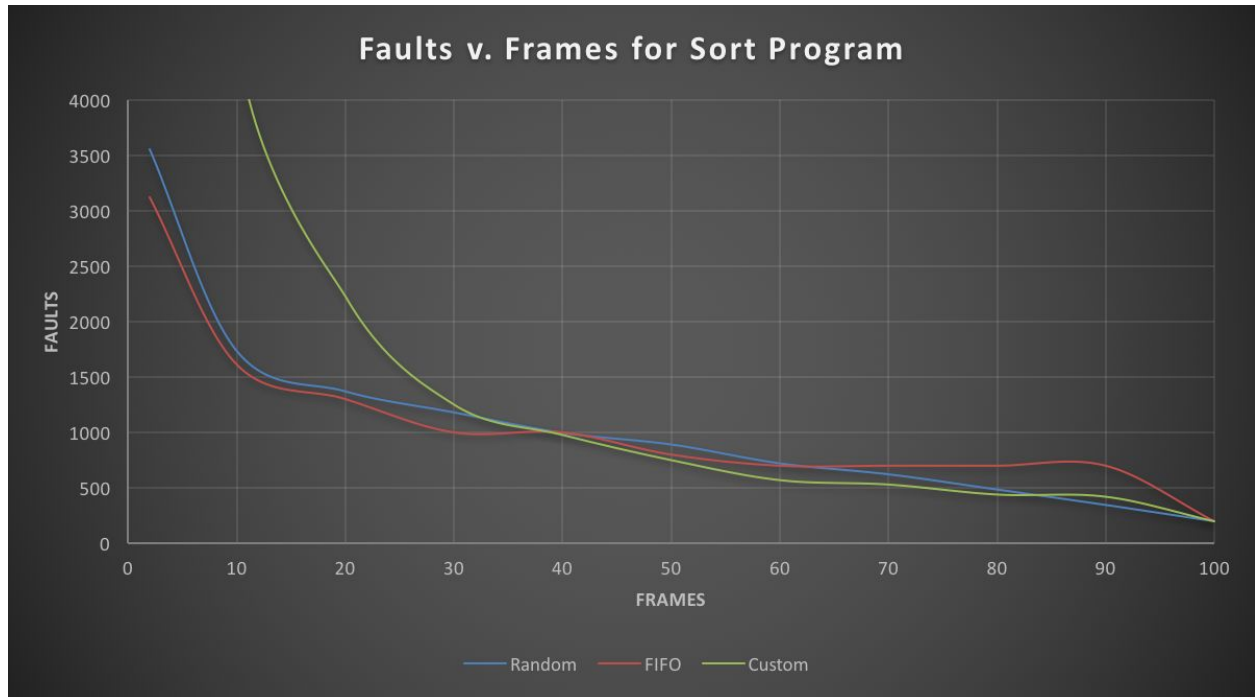


Fig. 1

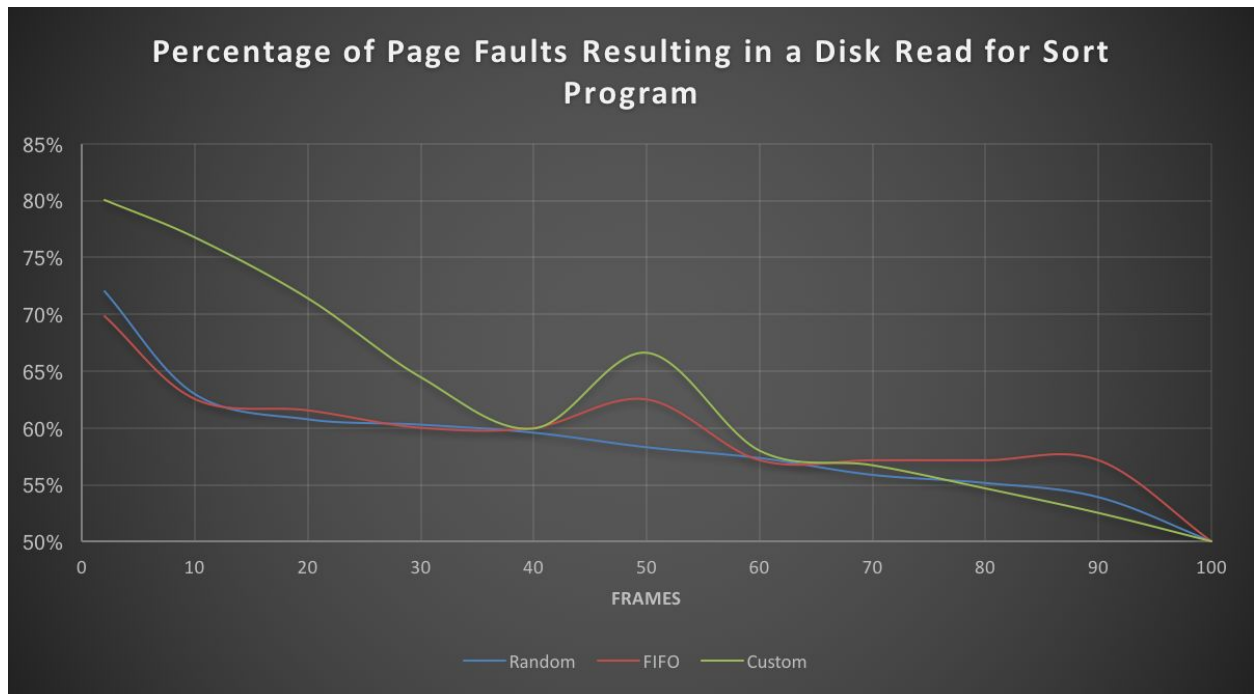


Fig. 2

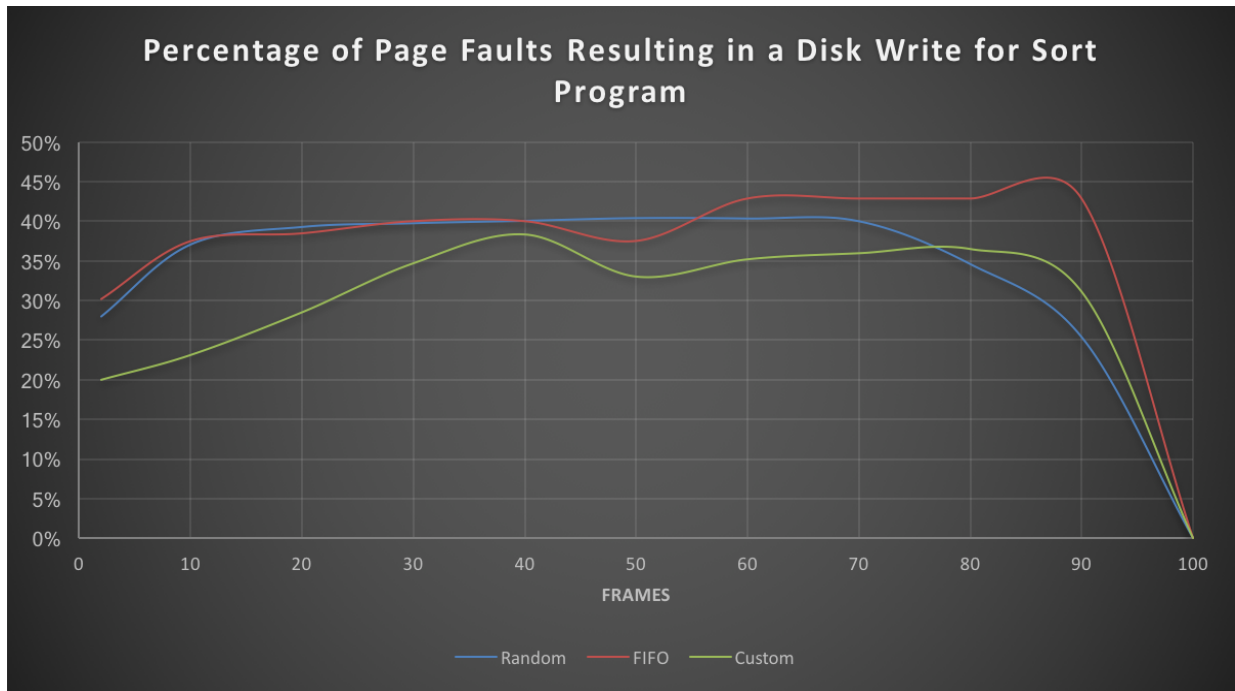


Fig. 3

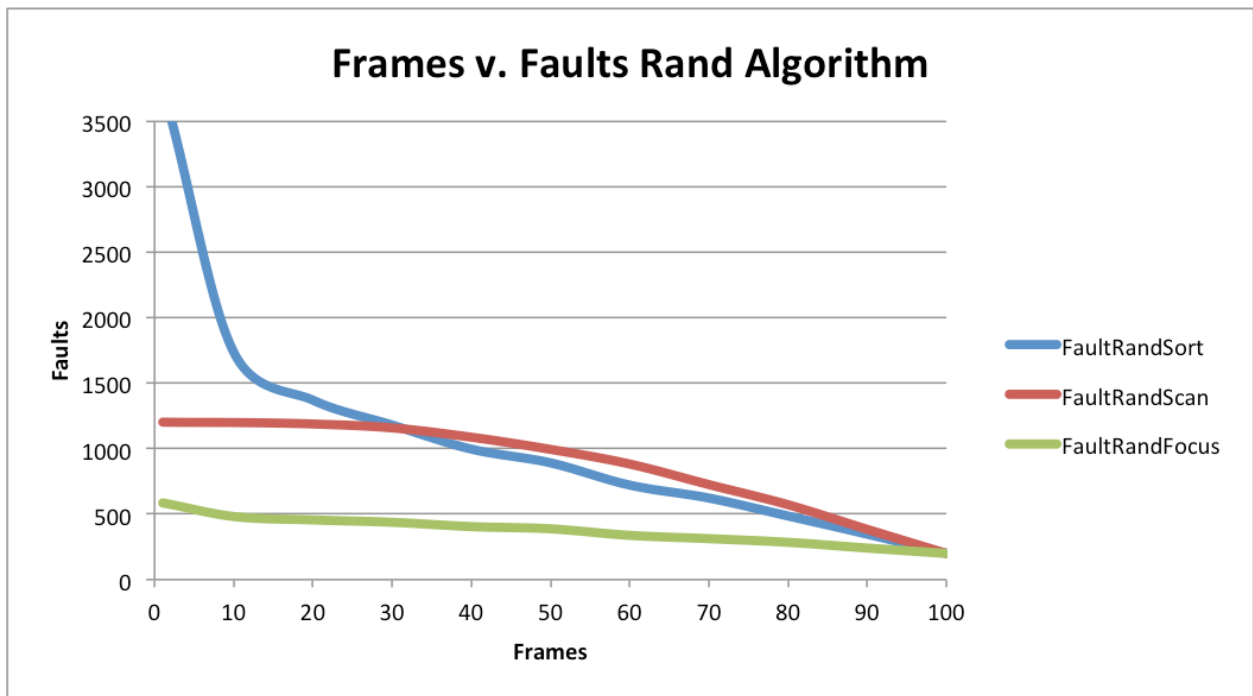


Fig. 4

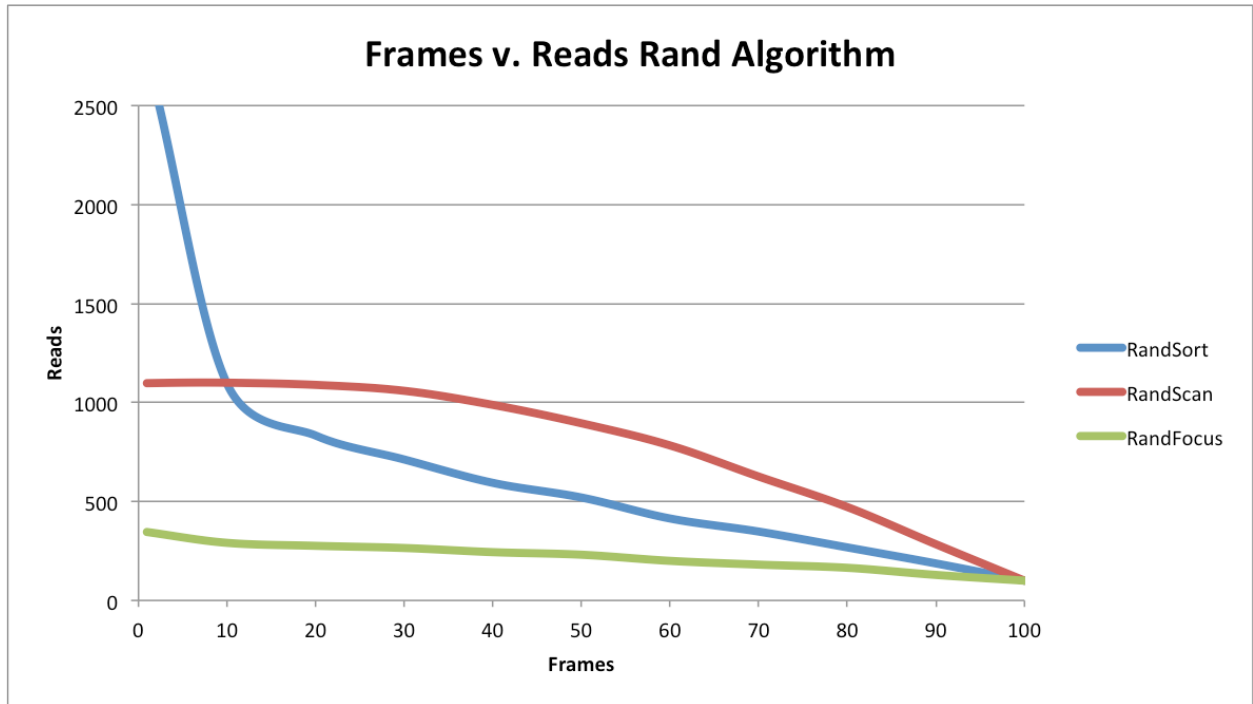


Fig. 5

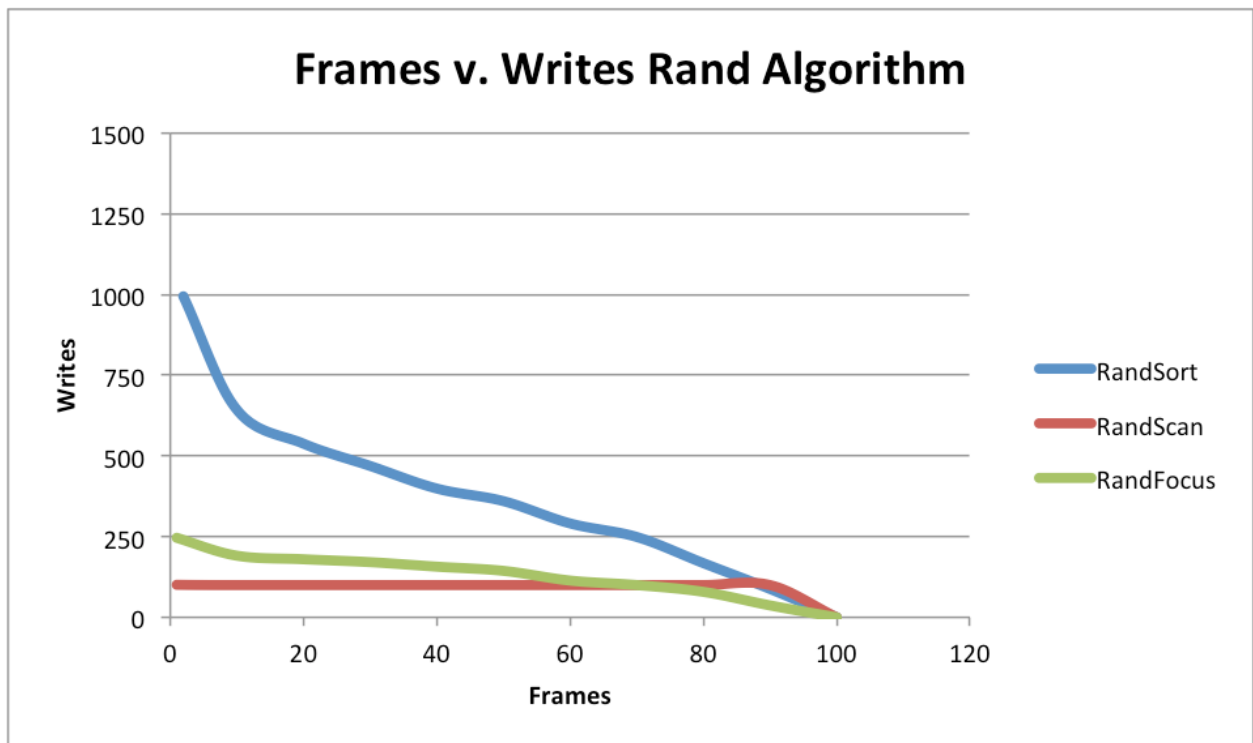


Fig. 6

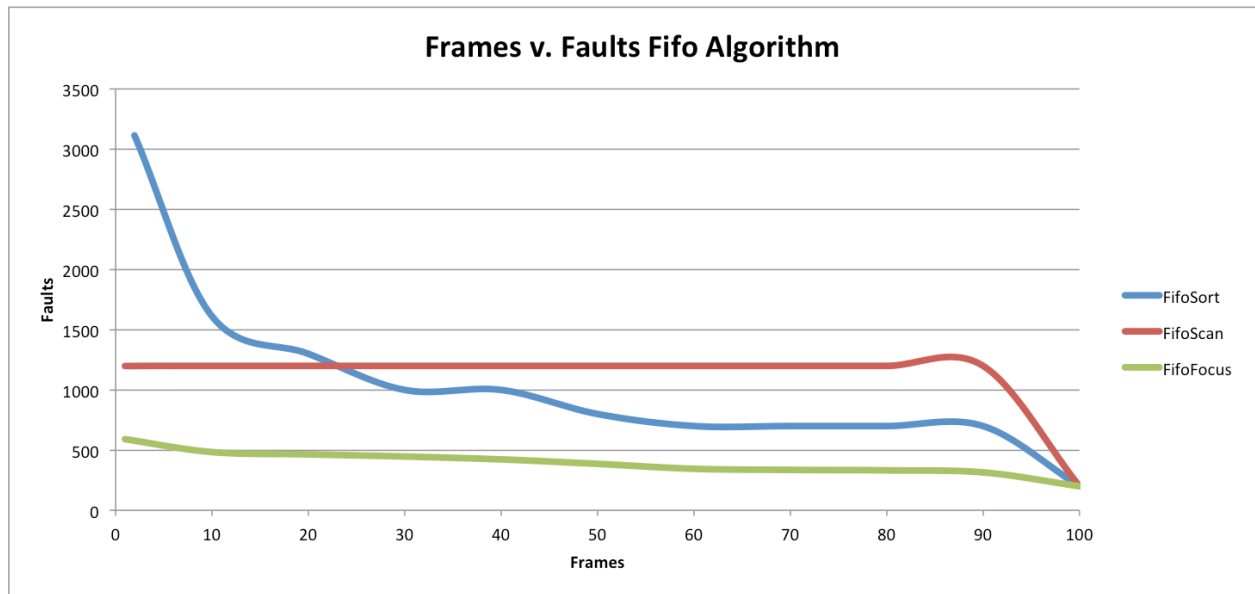


Fig. 7

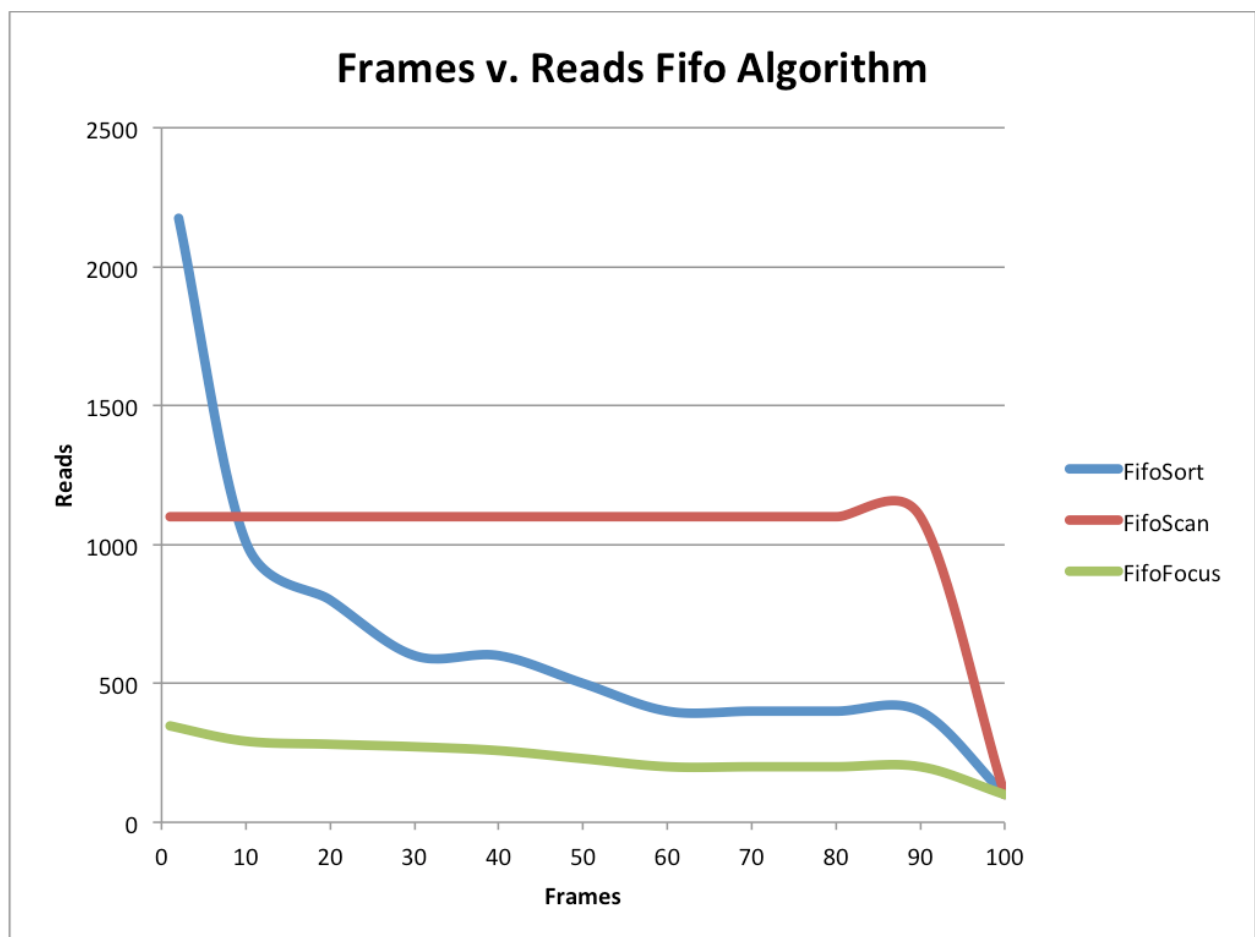


Fig. 8

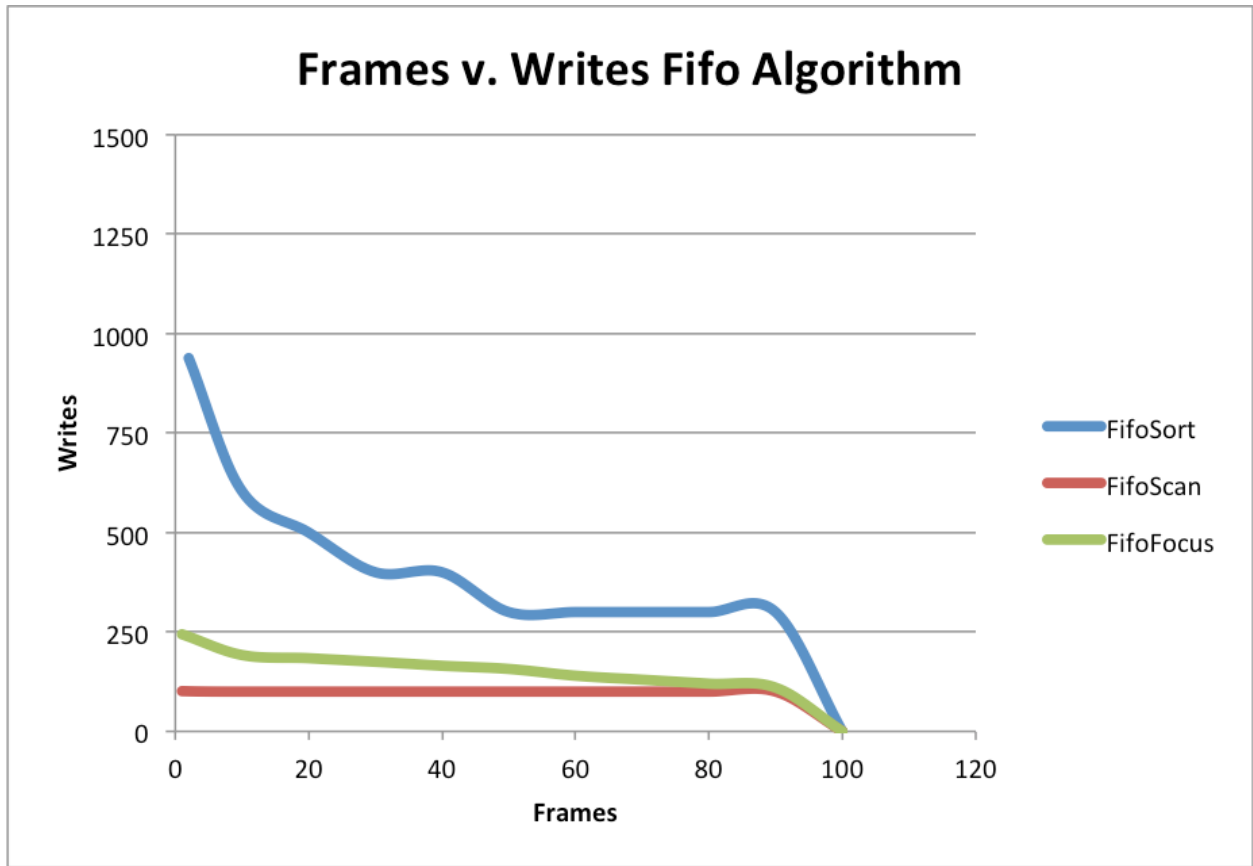


Fig. 9

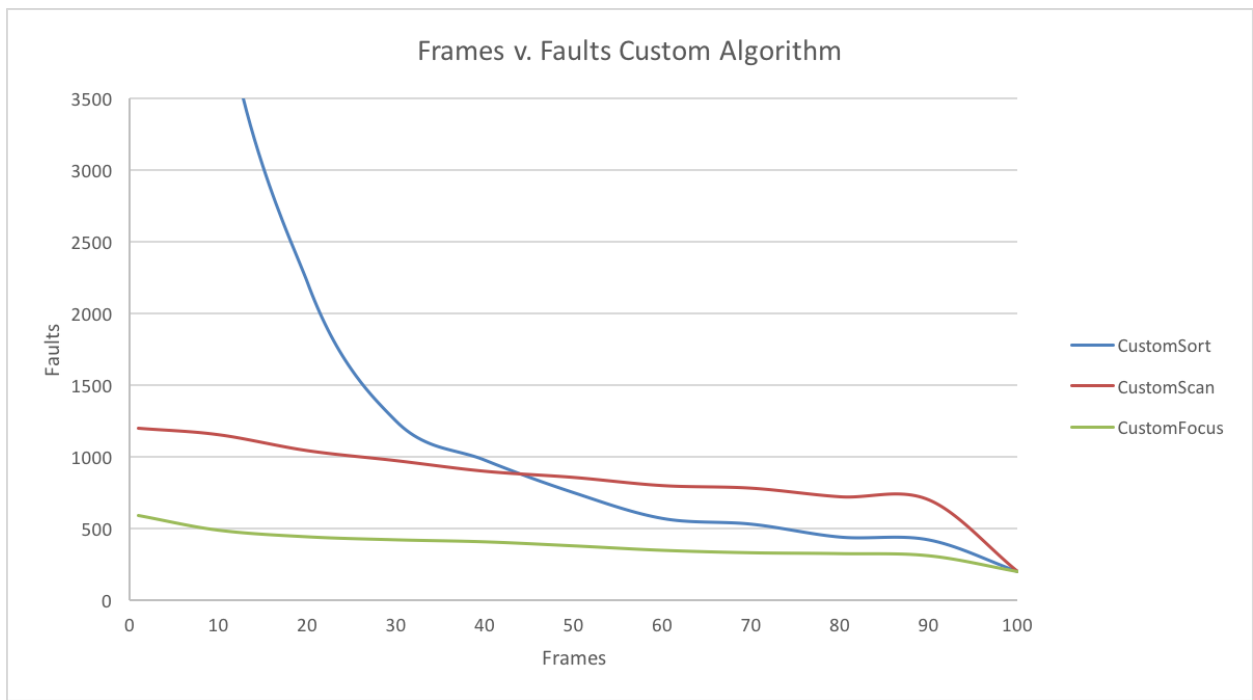


Fig. 10

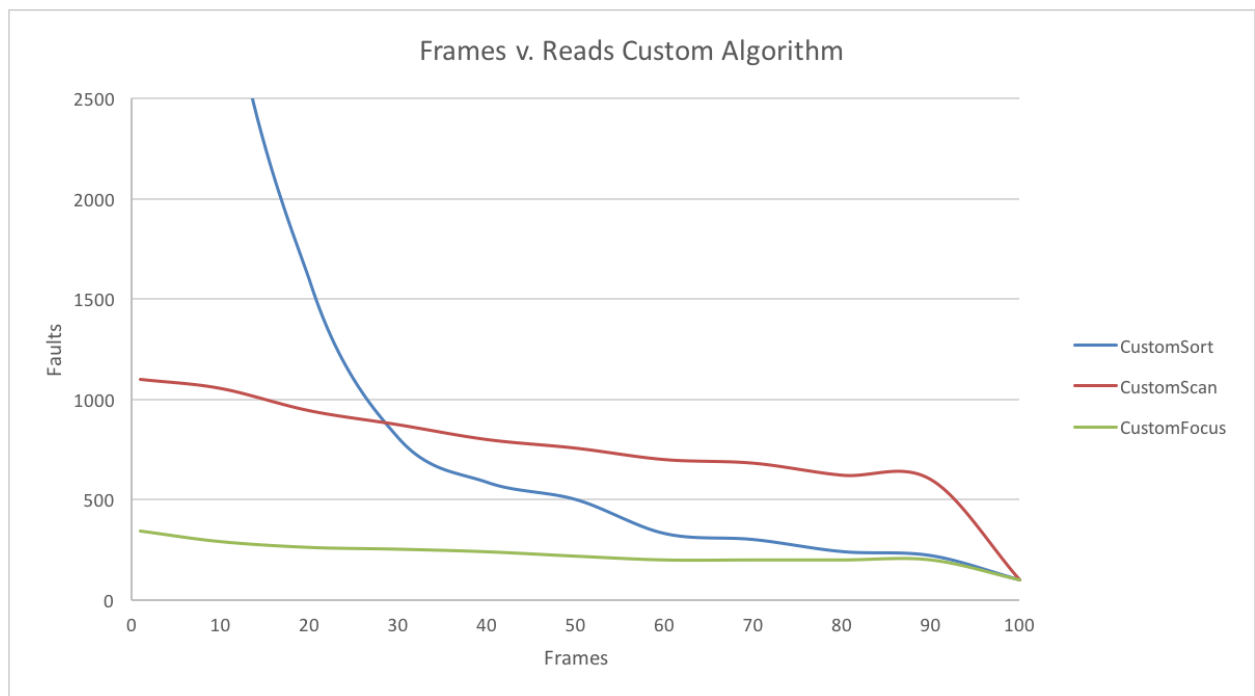


Fig. 11

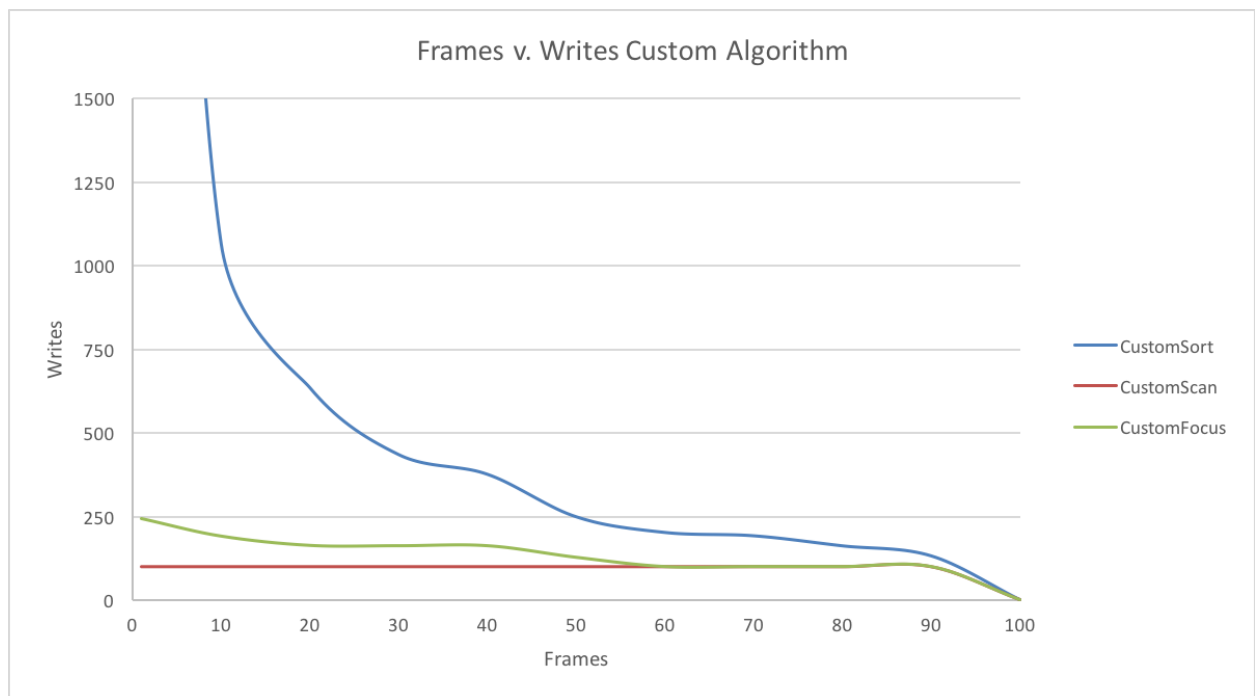


Fig. 12