

# Contents

<b>1</b>	<b>Introduction</b>	<b>3</b>
<b>2</b>	<b>Related Work</b>	<b>5</b>
2.1	ANA . . . . .	5
2.2	ReconOS . . . . .	5
2.3	Network on Chip . . . . .	5
<b>3</b>	<b>Design</b>	<b>7</b>
<b>4</b>	<b>Implementation</b>	<b>9</b>
<b>5</b>	<b>Verification and Evaluation</b>	<b>11</b>
<b>6</b>	<b>Summary</b>	<b>13</b>
<b>7</b>	<b>Conclusion</b>	<b>15</b>
<b>8</b>	<b>Future Work</b>	<b>17</b>
<b>A</b>	<b>How-To's</b>	<b>19</b>
<b>B</b>	<b>Original Task Description</b>	<b>21</b>
<b>C</b>	<b>Timeline</b>	<b>23</b>



## Chapter 1

# Introduction



## Chapter 2

# Related Work

### 2.1 ANA

### 2.2 ReconOS

### 2.3 Network on Chip



## Chapter 3

# Design





## Chapter 4

# Implementation



## Chapter 5

# Verification and Evaluation



## Chapter 6

# Summary



## Chapter 7

## Conclusion





## Chapter 8

# Future Work



Appendix A

How-To's



## Appendix B

# Original Task Description



## Appendix C

### Timeline