# Silicon Labs CP210x USB-to-UART

## **Installation Guide**

UG1033 (v1.0) February 12, 2014





## **Revision History**

The following table shows the revision history for this document.

Date	Version	Revision
02/12/2014	1.0	Initial Xilinx release.



## **Table of Contents**

Silicon Labs CP210x USB-to-UART Installation Guide	
Overview	4
Download and Install the Required Drivers	5
Appendix A: Additional Resources and Legal Notices	
Xilinx Resources	7
Solution Centers	7
References	7
Please Read: Important Legal Notices	7



## Silicon Labs CP210x USB-to-UART Installation Guide

### **Overview**

Many Xilinx evaluation boards and some characterization boards are equipped with the Silicon Labs CP2103 USB-to-UART bridge integrated circuits. This guide explains how to install the drivers for the USB-to-UART bridge.



## **Download and Install the Required Drivers**

- 1. Go to the Silicon Labs CP210x USB-to-UART Bridge VCP Drivers download page.
- 2. Download the virtual COM port (VCP) driver for your operating system to your PC. See Figure 1.

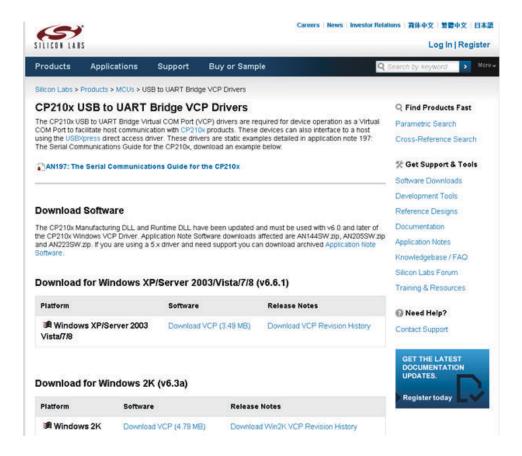


Figure 1: Silicon Labs CP210x Driver Download Page

- 3. Extract the ZIP archive file.
- 4. Open the extracted file and select the executable file that is compatible with your PC. For example, for Windows 64-bit, select CP210xVCPInstaller\_x64.exe.



5. Step through the guided installer. Accept the license agreement and install the software on your PC. When the software is successfully installed, the dialog box shown in Figure 2 is displayed.

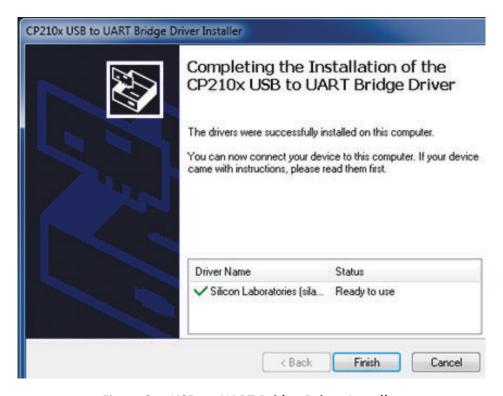


Figure 2: USB-to-UART Bridge Driver Installer

6. The next step is to set up a terminal emulator. Continue with the instructions in the appropriate quick start guide. At the appropriate time, you will be directed on the configuration of the terminal settings. If you do not have a terminal emulator, download the Tera Term emulator and follow the installation process in the *Tera Term Terminal Emulator Installation Guide* (UG1036) [Ref 2].

**Note:** Use of the Tera Term terminal emulator is not required and other terminal emulators can be used. Tera Term is recommended by Xilinx based on its long history as a reliable software product.



## Additional Resources and Legal Notices

#### **Xilinx Resources**

For support resources such as Answers, Documentation, Downloads, and Forums, see Xilinx Support.

For a glossary of technical terms used in Xilinx documentation, see the Xilinx Glossary.

#### **Solution Centers**

See the <u>Xilinx Solution Centers</u> for support on devices, software tools, and intellectual property at all stages of the design cycle. Topics include design assistance, advisories, and troubleshooting tips.

#### References

These Xilinx documents provide supplemental material useful with this guide:

- 1. See the appropriate quick start quide for your Xilinx product.
- 2. Tera Term Terminal Emulator Installation Guide (UG1036)

These external websites provide supplemental material useful with this guide:

- 3. CP210x USB-to-UART Bridge VCP Drivers
- 4. <u>Tera Term</u> download page

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