CMPE 460 Laboratory Exercise 2 UART Driver

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Performed: February 05, 2021 Submitted: February 12, 2021

Lab Section: 2

Instructor: Professor Beato

TA: Brunon Sztuba Eri Montano Connor Henley

Lecture Section: 1

Professor: Professor Beato

By submitting this report, you attest that you neither have given nor have received any assistance (including writing, collecting data, plotting figures, tables or graphs, or using previous student reports as a reference), and you further acknowledge that giving or receiving such assistance will result in a failing grade for this course.

Lab Description

The purpose of this lab is to implement methods to transmit and receive data in the form of characters through UART (Universal asynchronous receiver-transmitter). By polling flags in the UART status registers, characters are either sent or received through a serial console with the K64.

Screenshots



Figure 1: UART Driver Demo Part 2



Figure 2: UART Driver Demo Part 3

Exercise 2: UART Driver

Student's Name: Jacob	Meyerson	Section:	\supset
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Pre	-lab	Point Value	Points Earned	Comments
Pre-lab	UART Ports, Pins and Alt	10	10	BJS 2/921
	SCGC Clocks	10	10	B13 8/5/21

De	mo	Point Value	Points Earned	Date
	Part 2	15	15	305 2/5/2)
Demo	Part 3	20	20	BIS 3/5/2)

To receive any grading credit students must earn points for both the demonstration and the report.