CMPE 460 Laboratory Exercise 3 Characterization of OPB745

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Contents

Abstract	3
Design Methodology	3
Results & Analysis	3
Conclusion	3
Questions	3

Table 1: Table of Measured Voltages and Calculated Currents for the OPB745

Distance (mm)	V_{out} for $10k\Omega$ (V)	I_{RL} for $10k\Omega$ (A)	V_{out} for $20k\Omega$ (V)	I_{RL} for $20k\Omega$ (A)
X	X	X	X	X

Abstract

Design Methodology

Results & Analysis

Figure 1 shows the voltage outputs for the optoisolator based on the distance of the reflective surface from the optoisolator.

Figure 1: Voltage vs Distance Graph for Load Resistor of $10k\Omega$

After measuring the voltage outputs, the current through the optoisolator is calculated using Ohm's Law (V = IR). The current at each distance is calculated by dividing the voltage at each distance by the $10k\Omega$ load resistor. The current at each distance measurement was calculated and graphed, shown in Figure 2.

Figure 2: Current vs Distance Graph for Load Resistor of $10k\Omega$

Figure 3: Voltage vs Distance Graph for Load Resistor of $20k\Omega$

The current at each distance measurement is calculated for the $20k\Omega$ load resistor the same was it is calculated for the $10k\Omega$ load resistor. The current vs distance plot is shown below in Figure 4.

Figure 4: Current vs Distance Graph for Load Resistor of $20k\Omega$

Conclusion

Questions

Question 1

The 74LS14 with Schmitt trigger differs from the 7406 inverter because it uses 2 edge trigger values when determining outputs, whereas the 7406 uses 1 trigger edge to determine whether to drive the output high or low. The advantage of the 74LS14 with Schmitt trigger is that it

is very effective in filtering out noise. There are thresholds set for high and low values, and when those values are reached, the output is driven either high or low. However, any voltage value falling between the 2 triggers (for example, 2.5 V on a scale from 0 - 5 V) could be interpreted as either high or low, since it is right on the border. The Schmitt trigger will read this value as noise, and will maintain the most recent output until the opposite threshold gets crossed. The 7406 will flip back and forth as noise interferes with the input, so there would be short, unexpected pulses in ranges that should be held at a constant output.

Question 2

The voltage from the optoisolator starts at close to the source voltage (5 V), because there is no way for any light to get reflected when the output diode is completely covered. Then, as soon as any light escapes that hole and gets reflected at a very close distance, the voltage drops down to the lower rail value (close to 0 V). As the distance increases, less light gets reflected, the voltage begins to increase from the lower rail up to the voltage source (upper rail).

Question 3

When doubling the load resistor from $10k\Omega$ to $20k\Omega$, the maximum frequency that the circuit can operate at is halved. In this exercise, the maximum working frequency decreased from 700Hz to 400Hz, which is roughly half. This matches the expected behavior, because it follows that a larger resistance/ impedance would limit the amount of current flow through the circuit, which will cap the maximum frequency the circuit can handle.

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