Jacob Meyerson

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Currently hold TS Government Security Clearance

Education

Rochester Institute of Technology (RIT) Rochester, NY
Bachelor of Science in Computer Engineering *May 2022*
Minor in Mathematics *May 2022*
GPA: 3.98/4.00

Experience

STR June 2021 - August 2021, August 2022 - Current
Computer Engineer *August 2023 - Current*

- Routinely making significant contributions to 2-3 programs every month
- Member of the Sensors Division, while supporting various programs across all divisions
- Managed an intern during the summer of 2024, tasking him to maximize contributions to the program, and served as his technical mentor
- Briefing several customers on technical work and its impact to program's goals
- Creating the backend of an RF transmitting system in Python, including:
  - Several concurrent Python processes, communicating through gRPC and ZMQ interfaces
  - Integrated software to integrate with several devices communicating through serial connection
  - Designing robust software capable of recovering from unintended use cases or other system errors
- Update customized firmware on ZYNQ UltraScale+ RFSoC

Associate Computer Engineer *August 2022 - August 2023*

- Developed optimized C++ software from advanced MATLAB algorithms
- Worked briefly with a classification Neural Network (Python)
- Worked closely with Docker and Singularity containers to deploy software
- Set up Gitlab CI/CD pipelines to compile and test committed code. Also integrated git hooks to enforce stylistic constraints on commits
- Created an automated build script using Ansible for imaging computers and configuring runtime environmments
- Implemented several Bash scripts to automate setup and testing

Intern - Department: Sensors Signal Processing & Algorithms *June 2021 - August 2021*

- Part of the Signal Processing & Algorithms department
- Worked on porting the PYNQ environment for ZYNQ architectures from a supported board to high powered signal processing ZYNQ board

Lockheed Martin June 2019 - Nov 2019, June 2020 - April 2021
Software Engineering Co-Op *Owego, NY*

- Part of the Diagnostic Software unit
- Worked on B2 Bomber Avionics and Graphics Processor (B2-AGP) automating builds and card setups, along with verifying card functionality
- Worked with Northern Arizona University (NAU) using Ternary Physically Unclonable Functions (PUF) for encryption and key authentication

Technical Skills

Languages: C/C++, Java, Python, Bash, ARM Assembly, MIPS Assembly, VHDL, System Verilog, LaTeX, Markdown
Developer Tools: Git, Gitlab CI/CD, Xilinx Vivado & Petalinux, Anaconda, Docker & Singularity, Ansible
Hardware: Raspberry Pi, Arduino, Nexys 4 DDR, ZYNQ, AMD Zynq UltraScale+ RFSoCs, STM32
Operating Systems: FreeRTOS, Ubuntu, RHEL, CentOS
Software Development Processes: Agile, Waterfall