3 Pinouts and pin description

PE2 □ PE3 □ PE4 □ PE5 □ PE6 □ VBAT 🗖 PC13-ANTI_TAMP ☐ PC14-OSC32_IN□ PC15-OSC32_OUT□ vss 🗖 VDD ☐ 11 PH0-OSC_IN ☐ 12 PH1-OSC_OUT☐ 13 LQFP100 NRS | 15
PC0 | 15
PC1 | 16
PC2 | 17
PC3 | 18
VSSA | 19
VREF+ | 20
VDDA | 2
PA0-WKUP | 2
PA1 | 7
PA2 | 7
PA3 | 7 NRST | 14 PC0 | 15 61 | PD14 60 | PD13 59 | PD12 58 | PD11 57 | PD10 56 | PD9 55 | PD8 54 | PB15 53 | PB14 52 | PB13 51 | PB12 MSv34171V2

Figure 11. STM32F74xVx LQFP100 pinout

2. The above figure shows the package top view.