

Schedulability analysis of limited-preemptive moldable gang tasks

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Real-time systems

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- Systems of which correctness does depends not only on **logical** results but also on **timing constraints**

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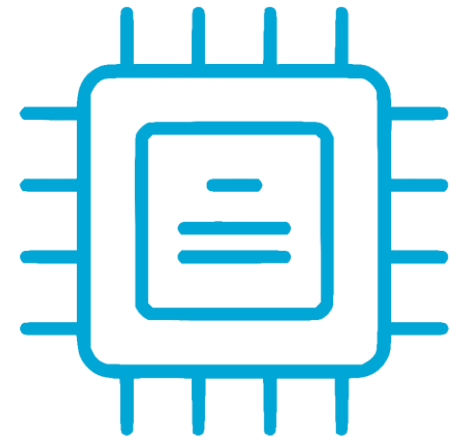
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Real-time systems

- Systems of which correctness does depends not only on **logical** results but also on **timing constraints**
- Multicore systems



Definitions

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- Task
 - A functionality of the system

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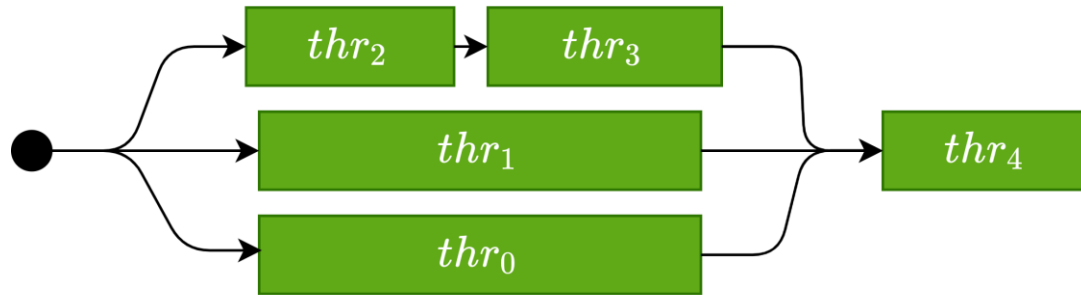
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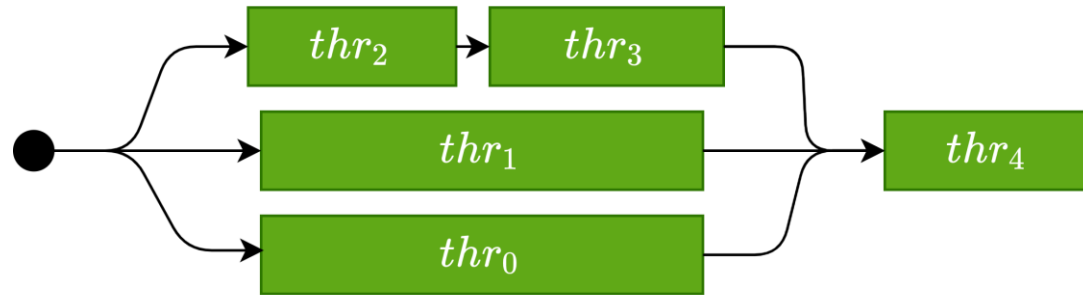
- Task
 - A functionality of the system
- Job
 - Instance of a task
- Schedule
 - A particular assignment of jobs to the processors and time intervals
- Scheduling policy
 - Algorithm that produces a schedule
 - FIFO, Round-Robin, JLFP, EDF

What is gang?

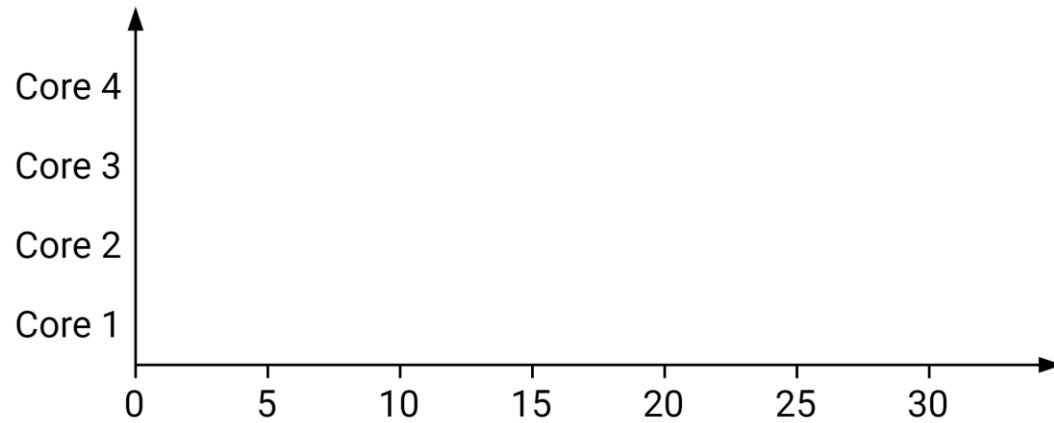
What is gang?



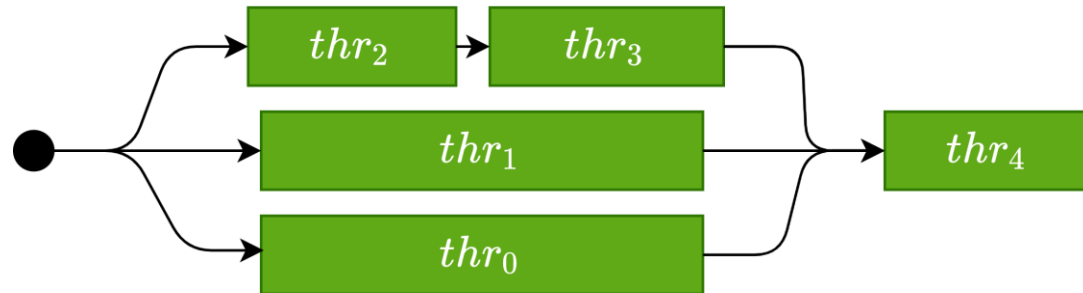
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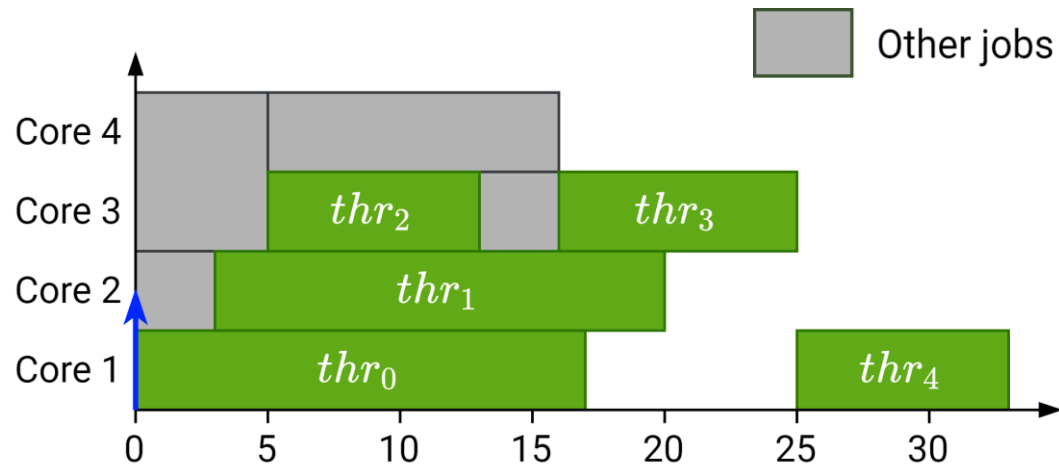
Global scheduling



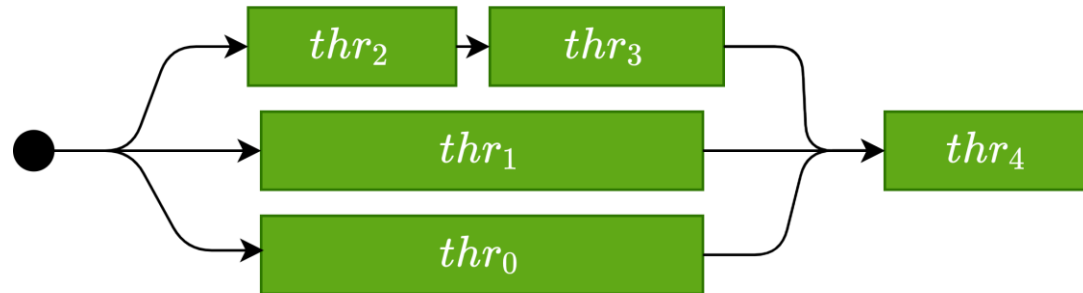
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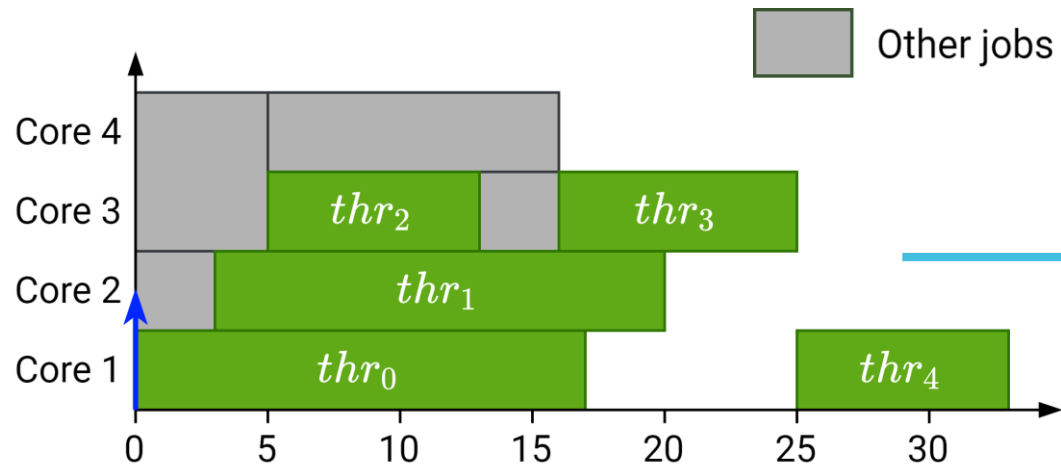


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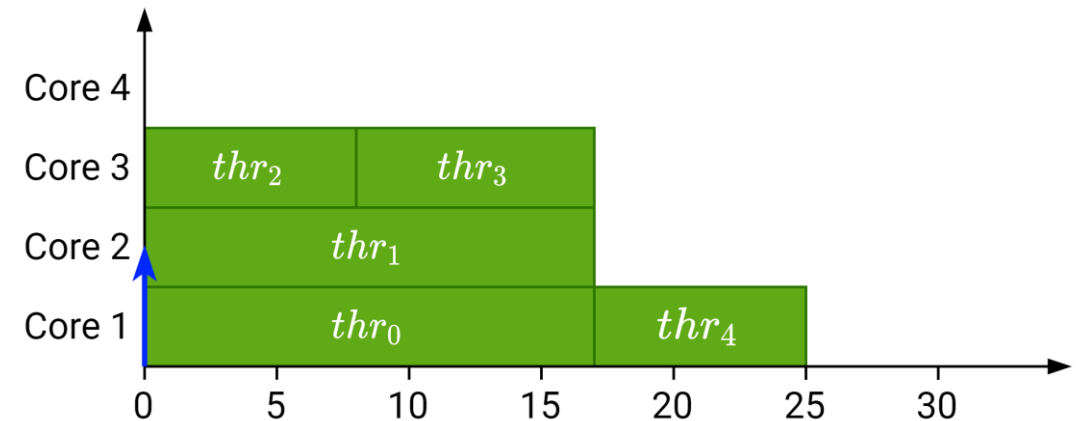


Parallel threads together as a “gang”

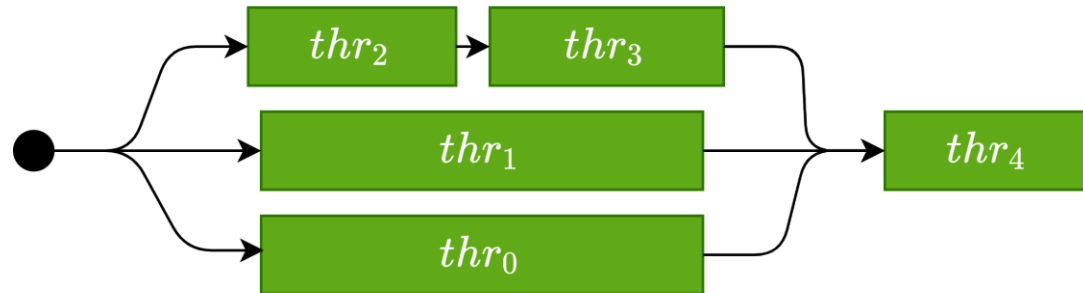
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Gang Scheduling

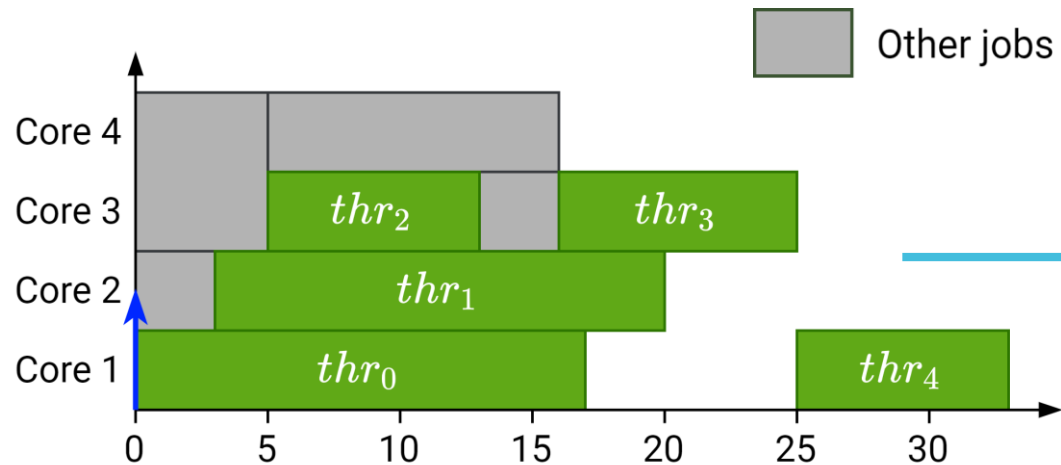


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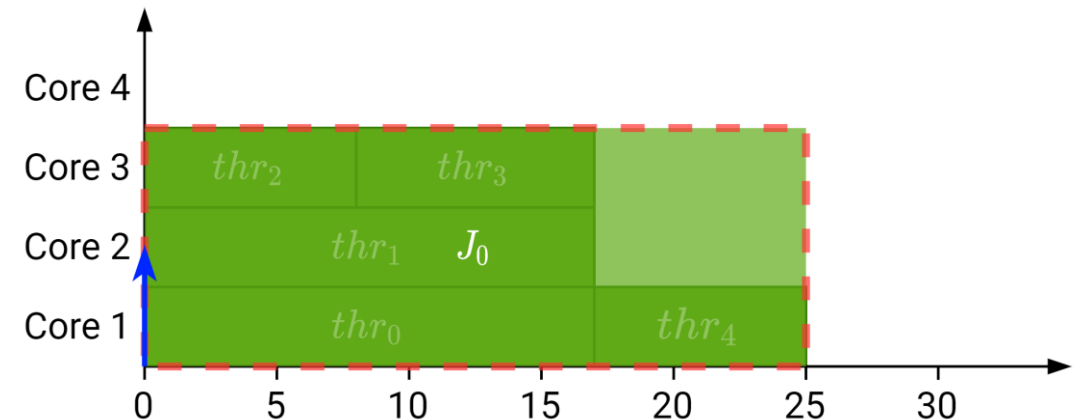


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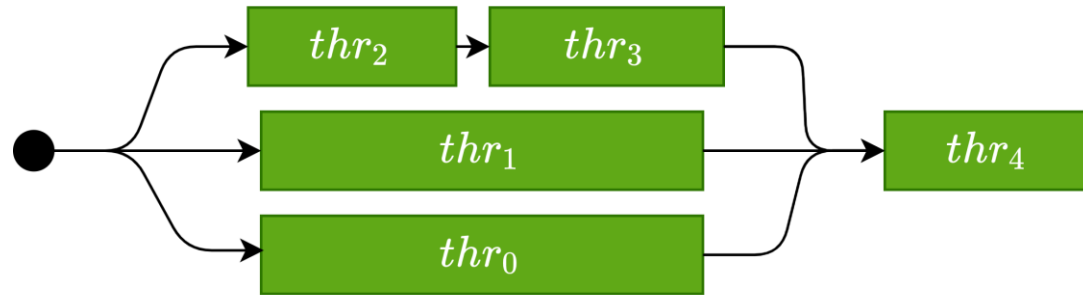
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Gang Scheduling



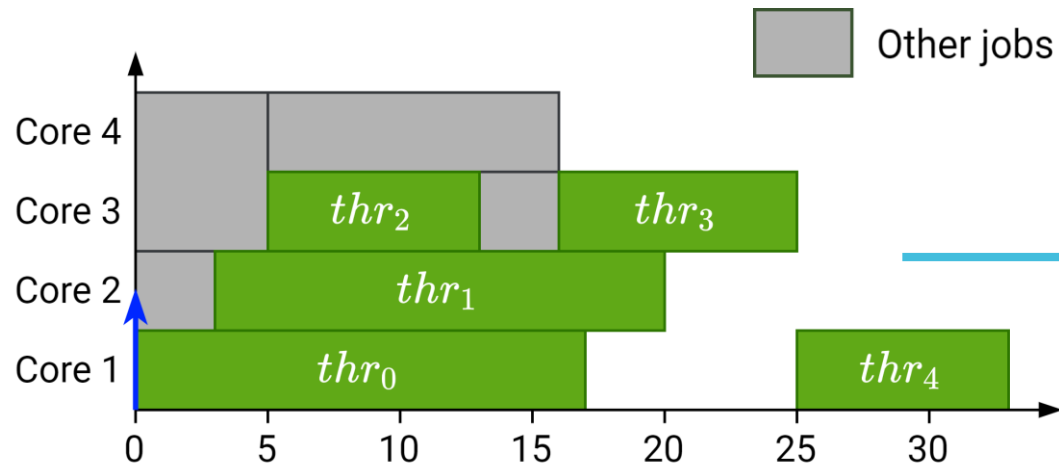
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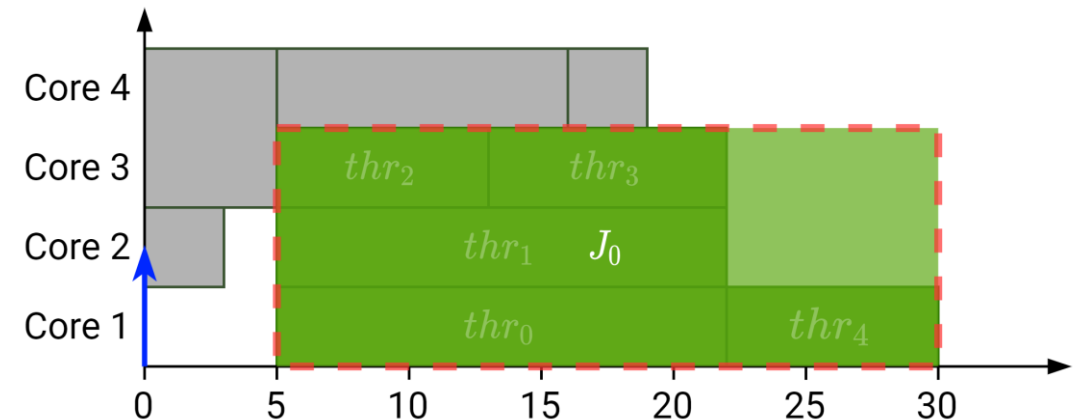
Parallel threads together as a “gang”

Execution does not start until there are enough cores

Global scheduling



Gang Scheduling



Why gang?

Why gang?

- More efficient synchronization

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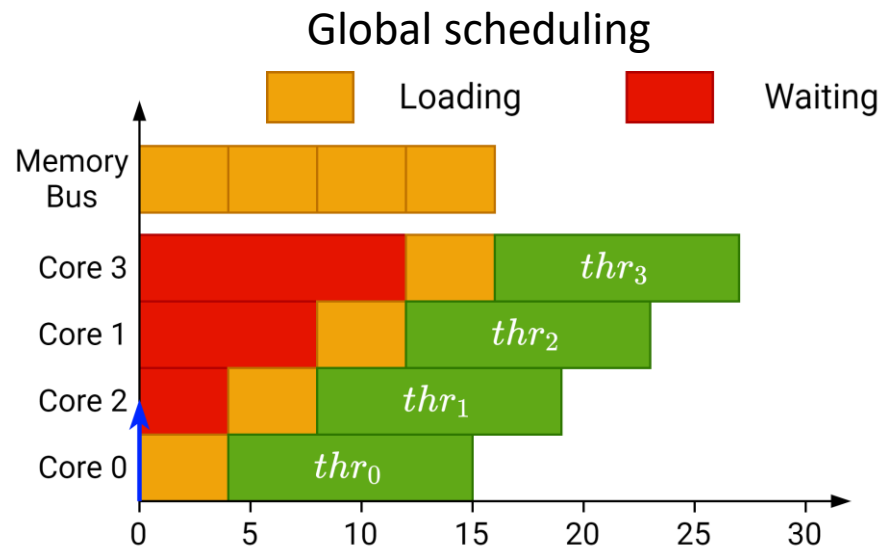
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Why gang?

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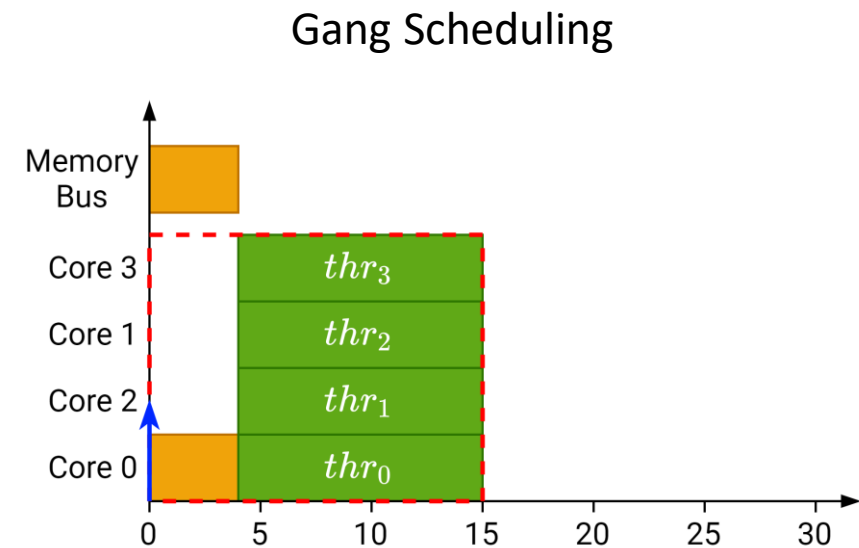
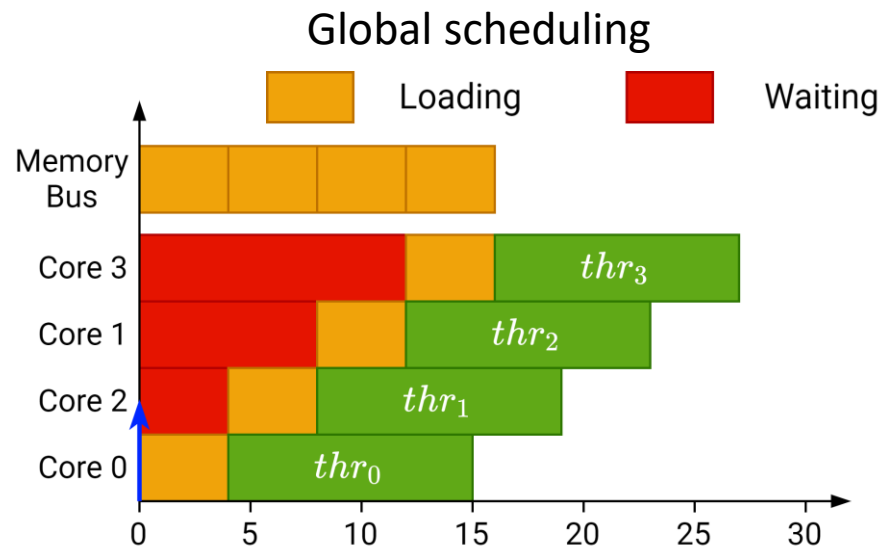
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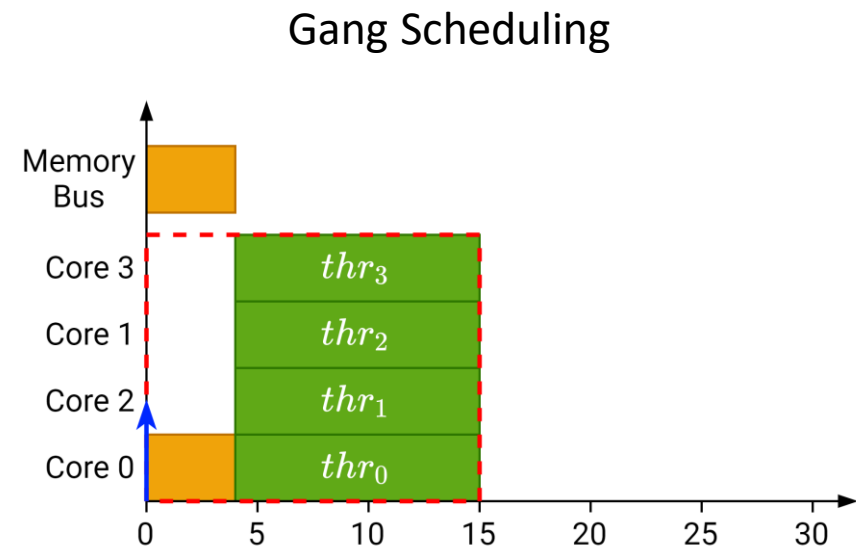
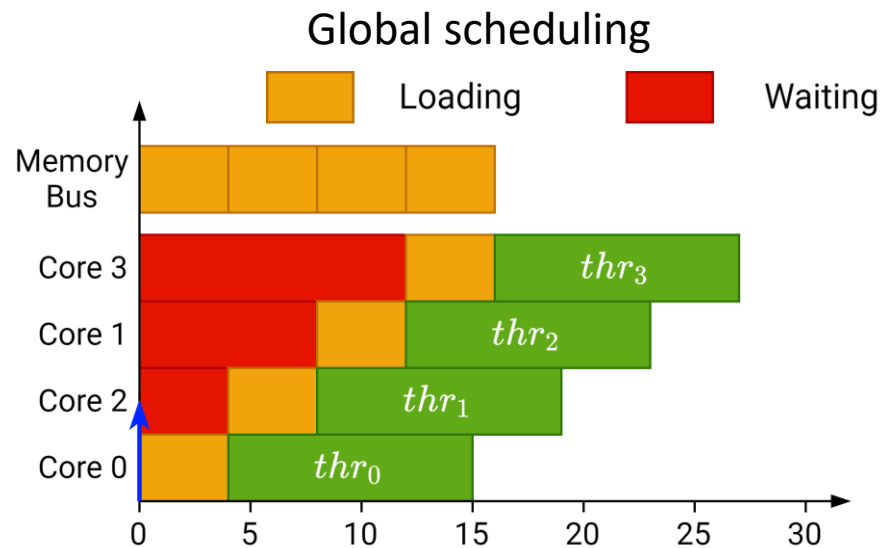
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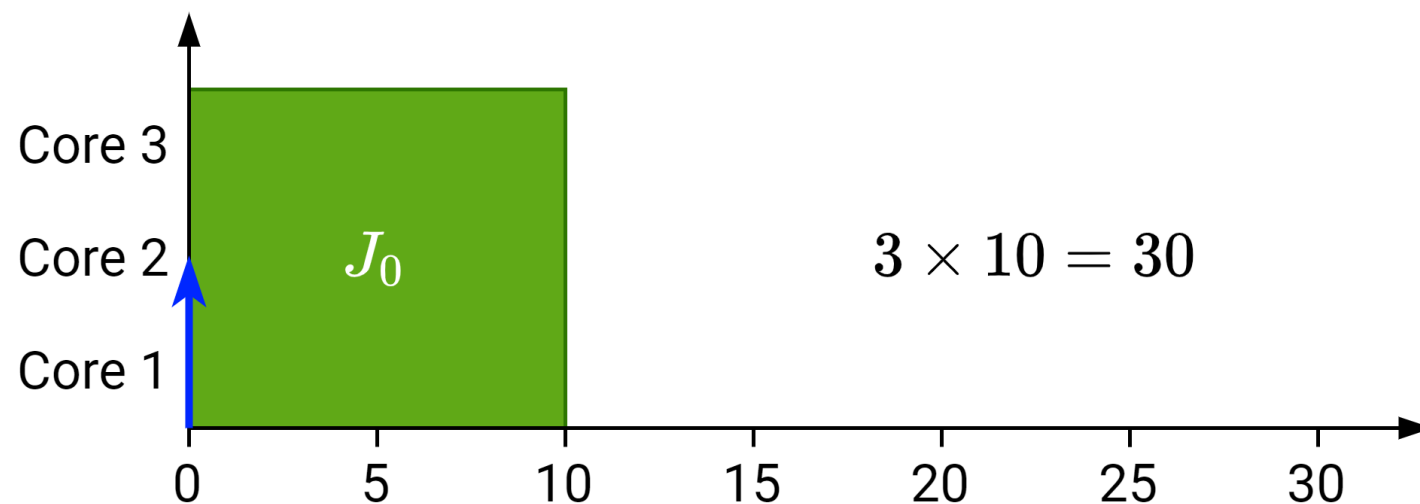
- More efficient synchronization
- Reduces variability in the execution
- Avoids overhead when loading initial data
- Shows its full potential when executed non-preemptively



Types of gang

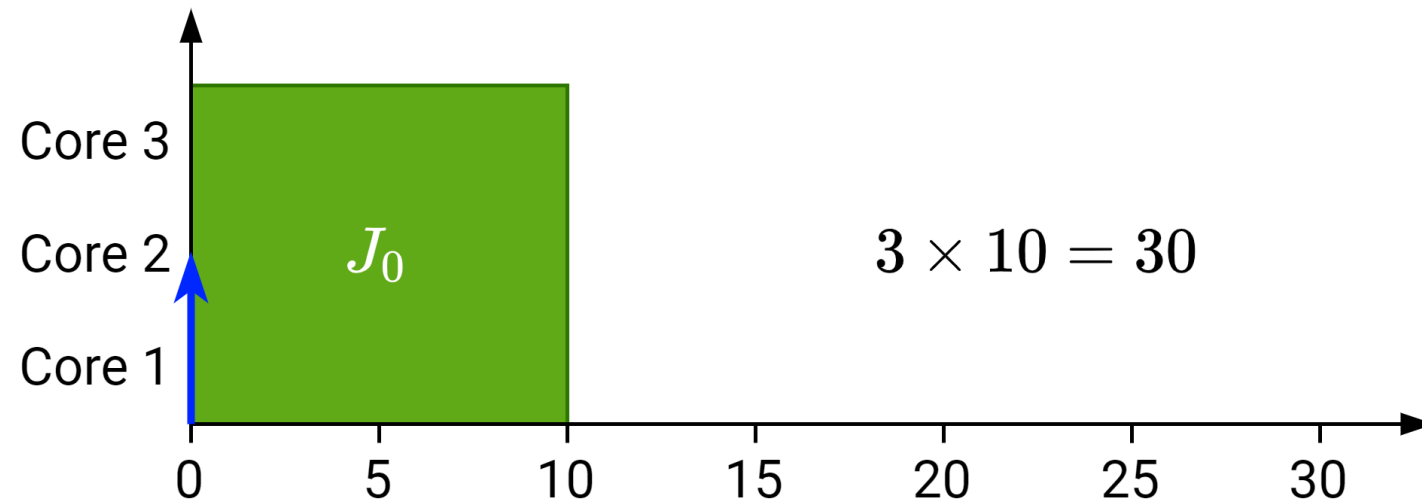
Types of gang

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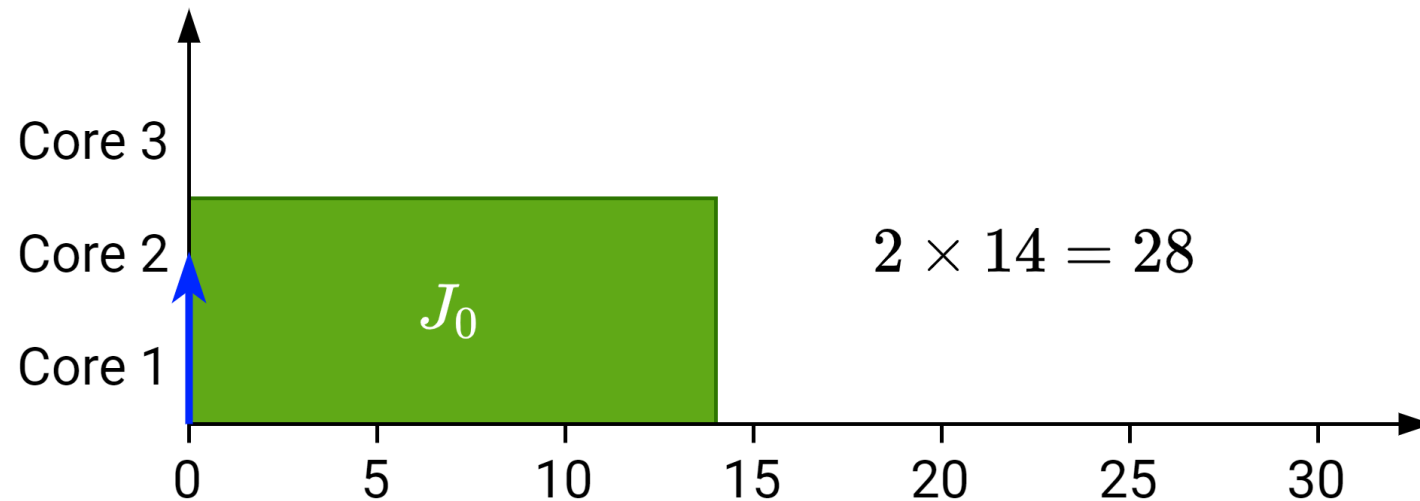
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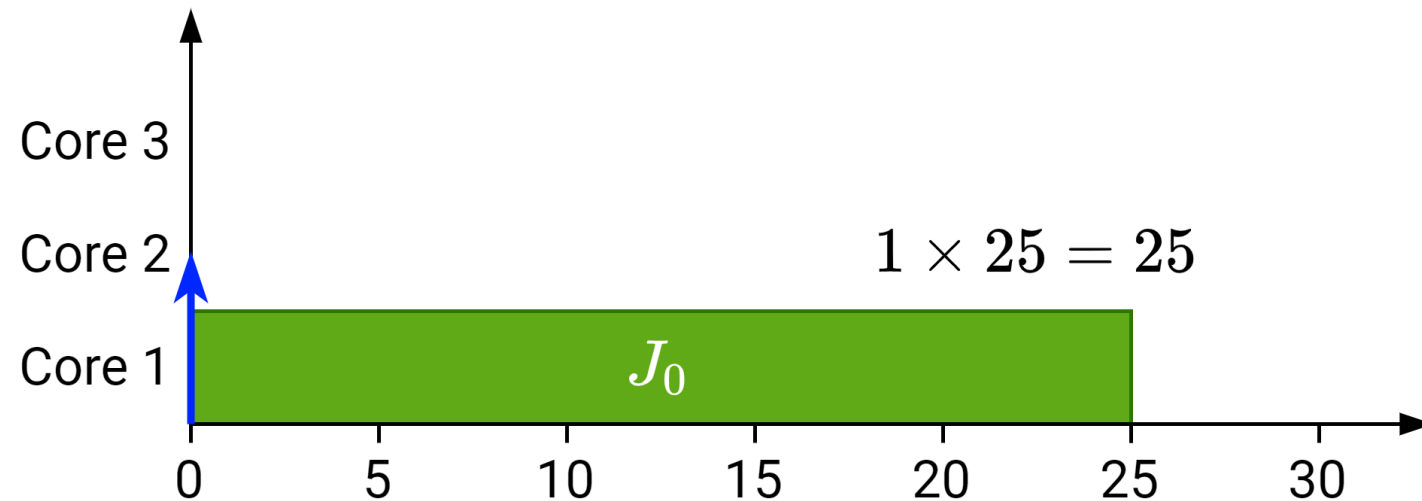
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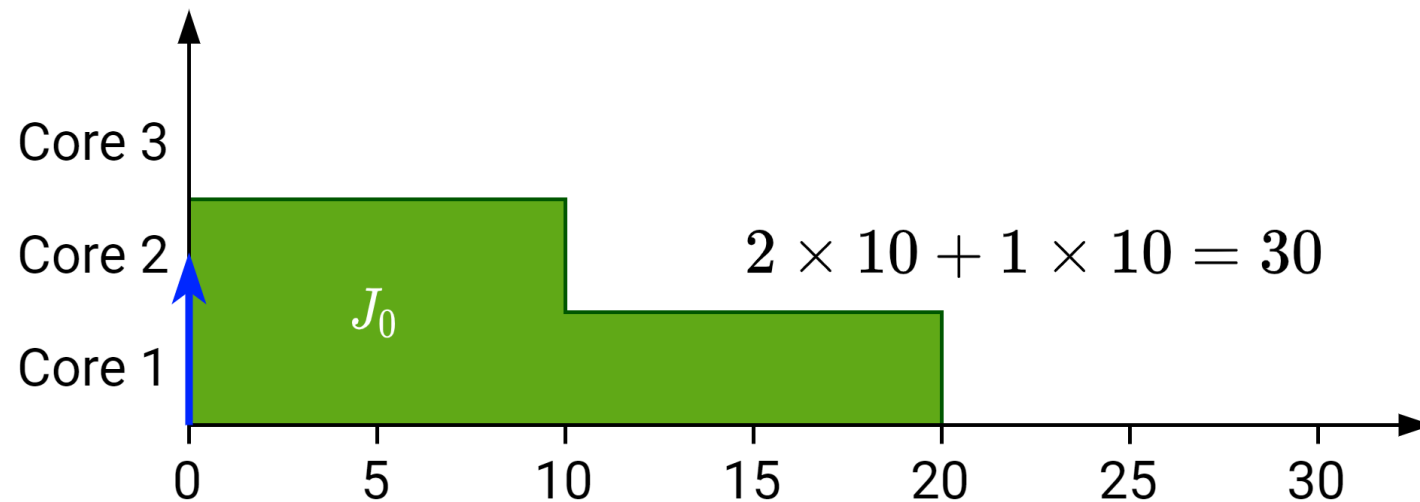
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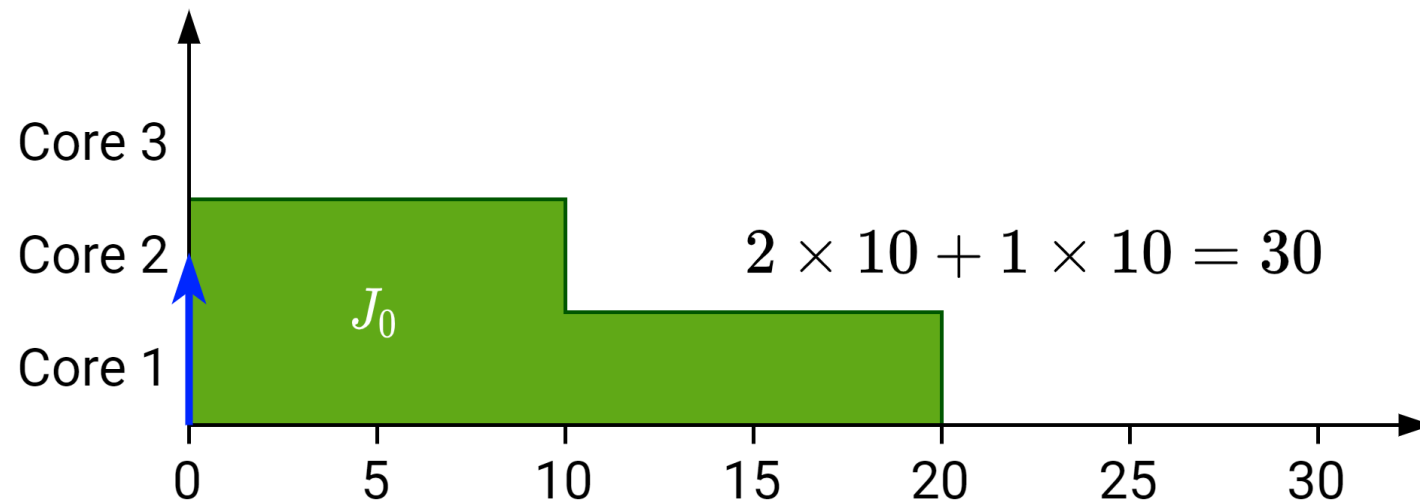
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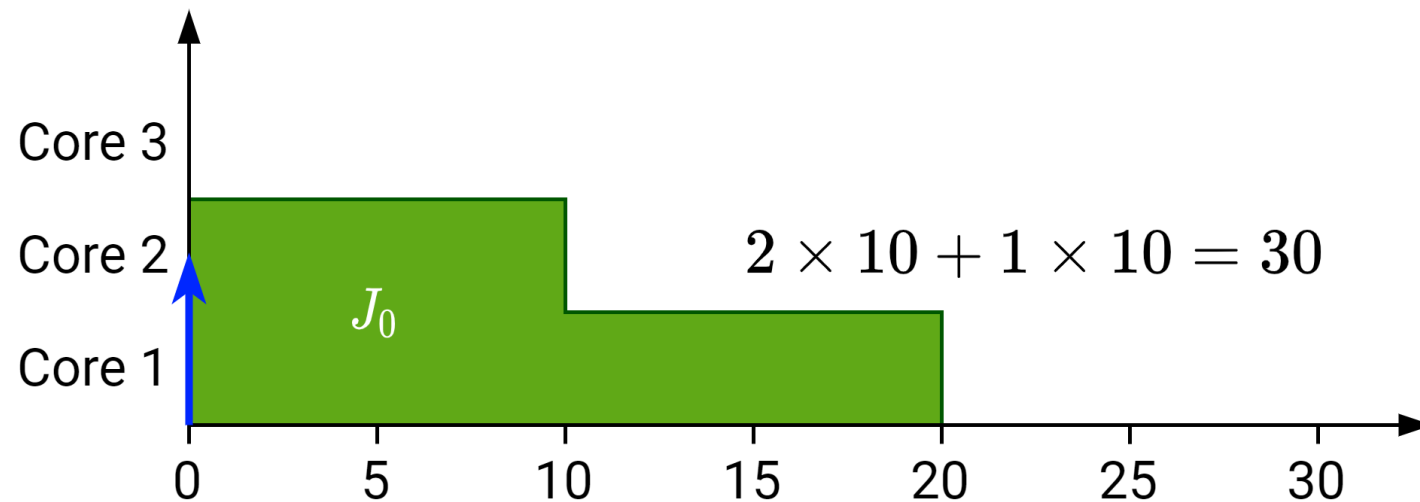
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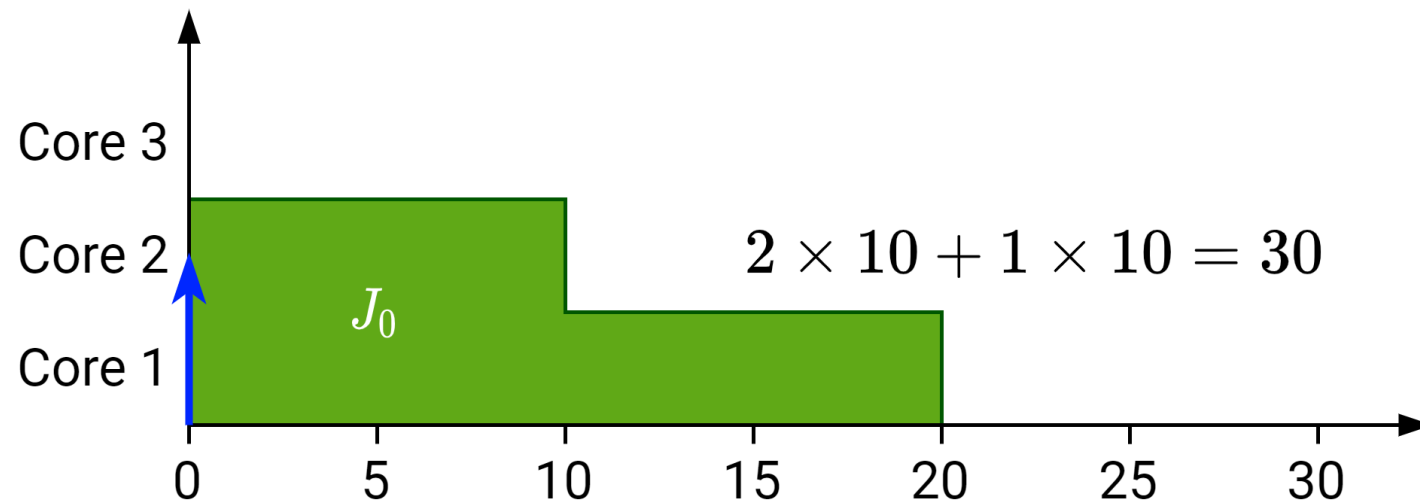
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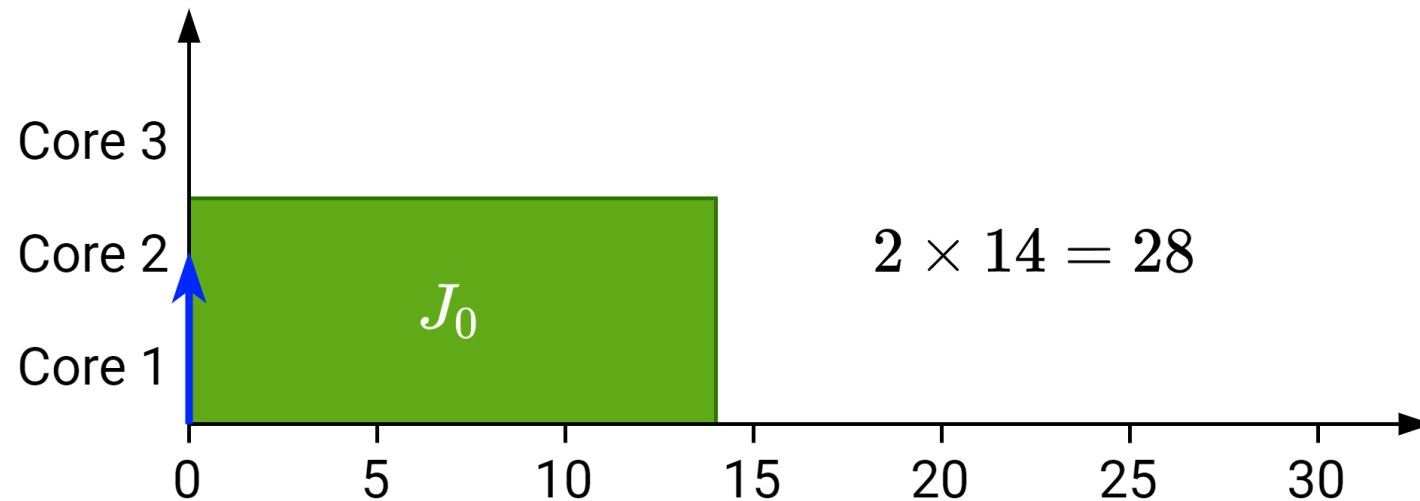
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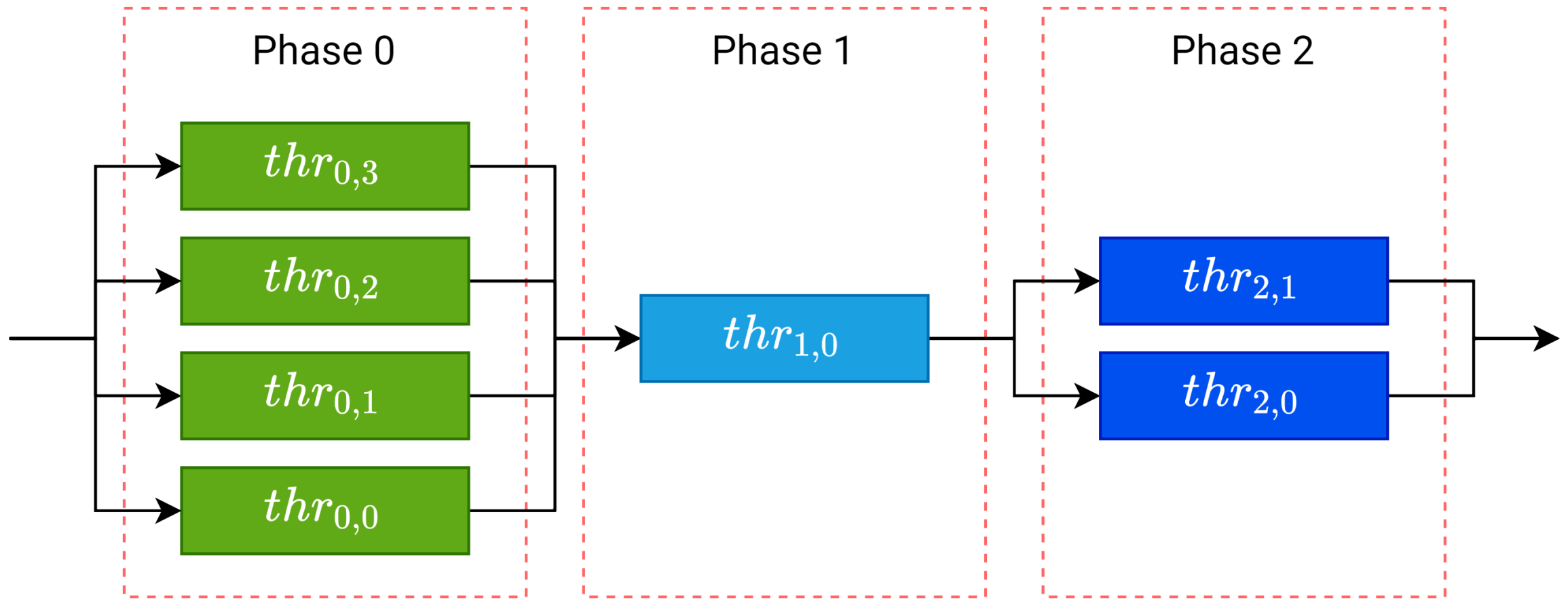


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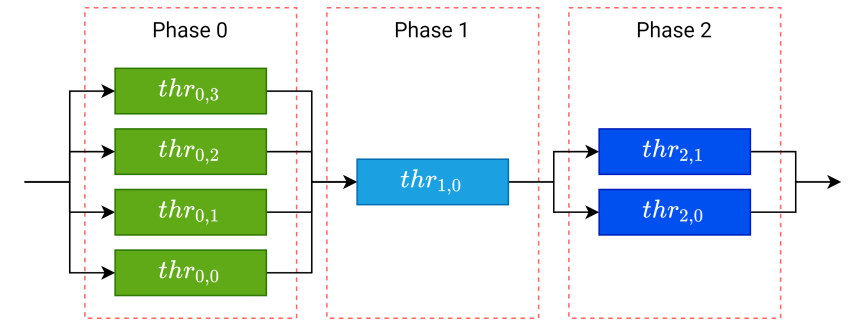
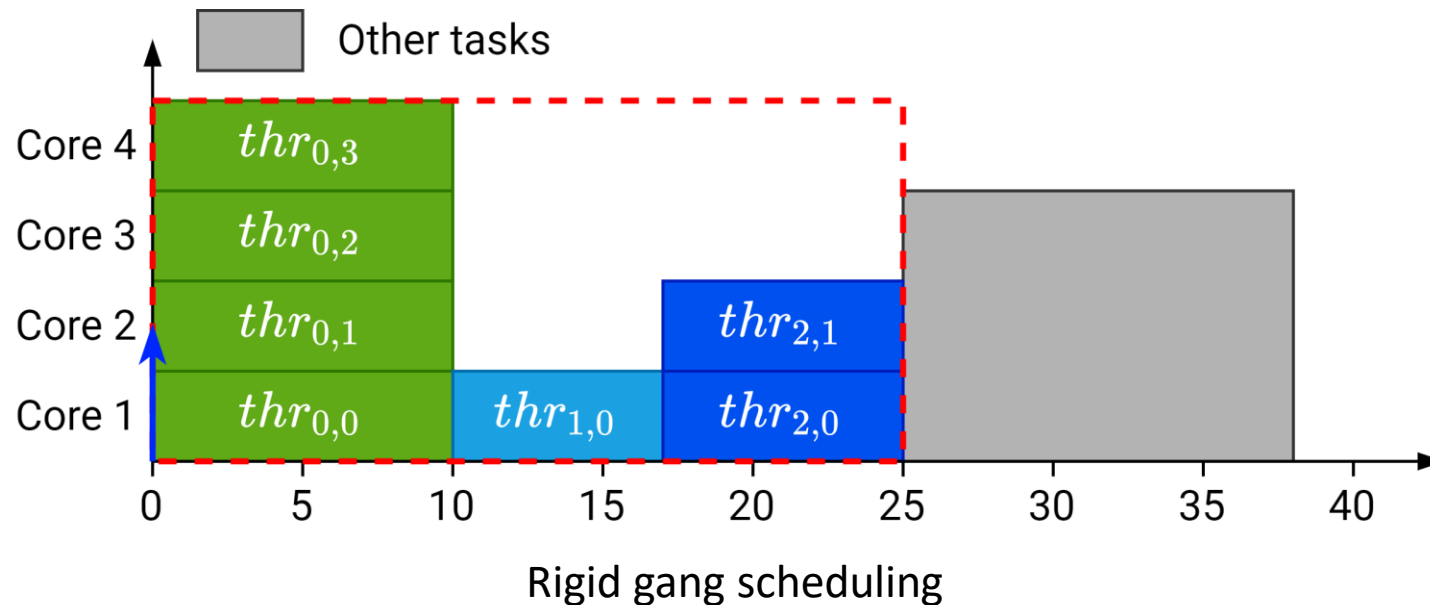


Bundled scheduling^[1] vs limited-preemptive



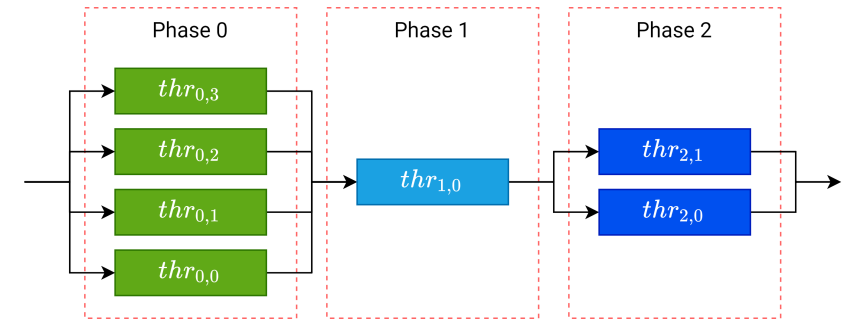
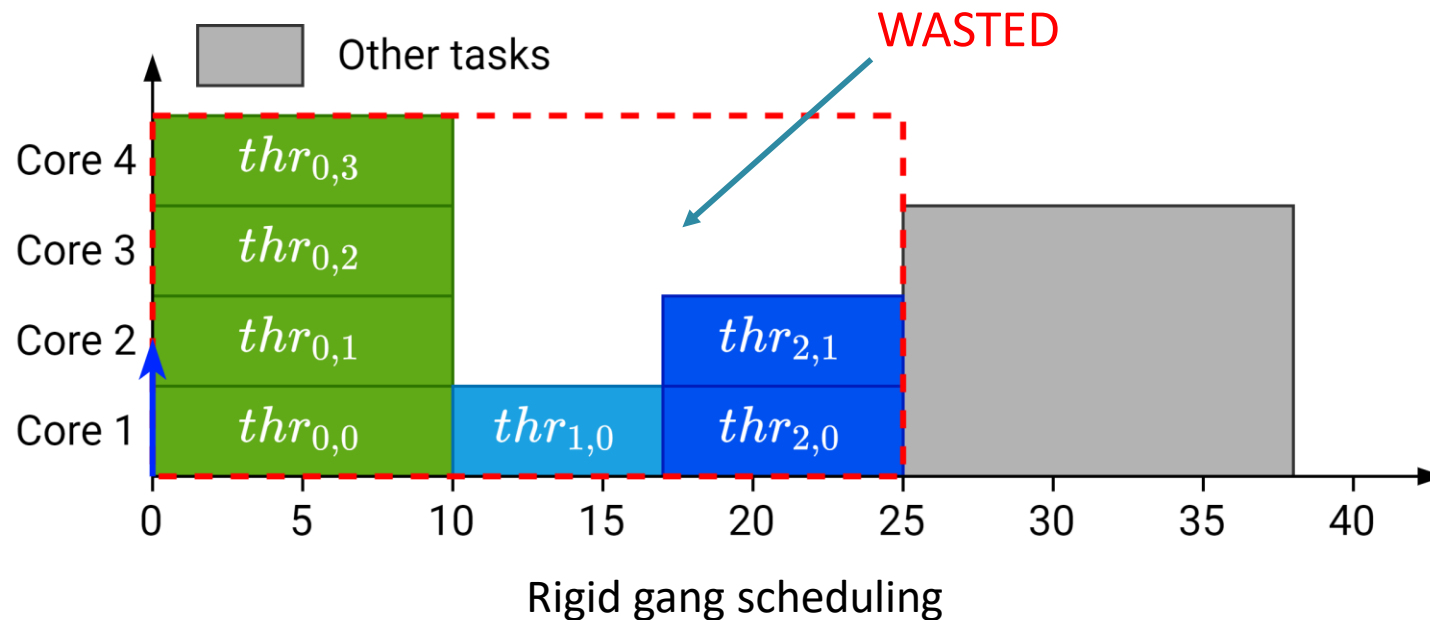
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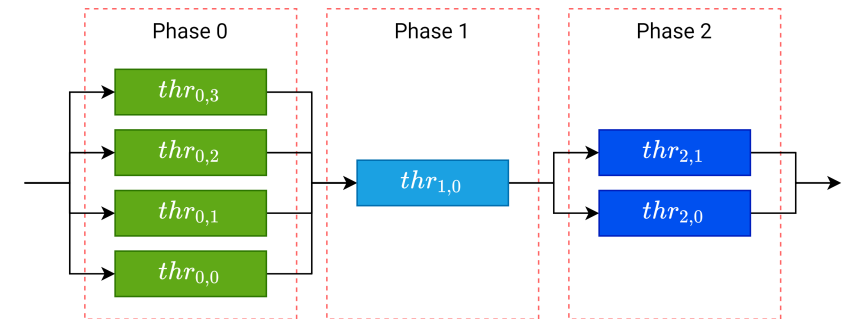
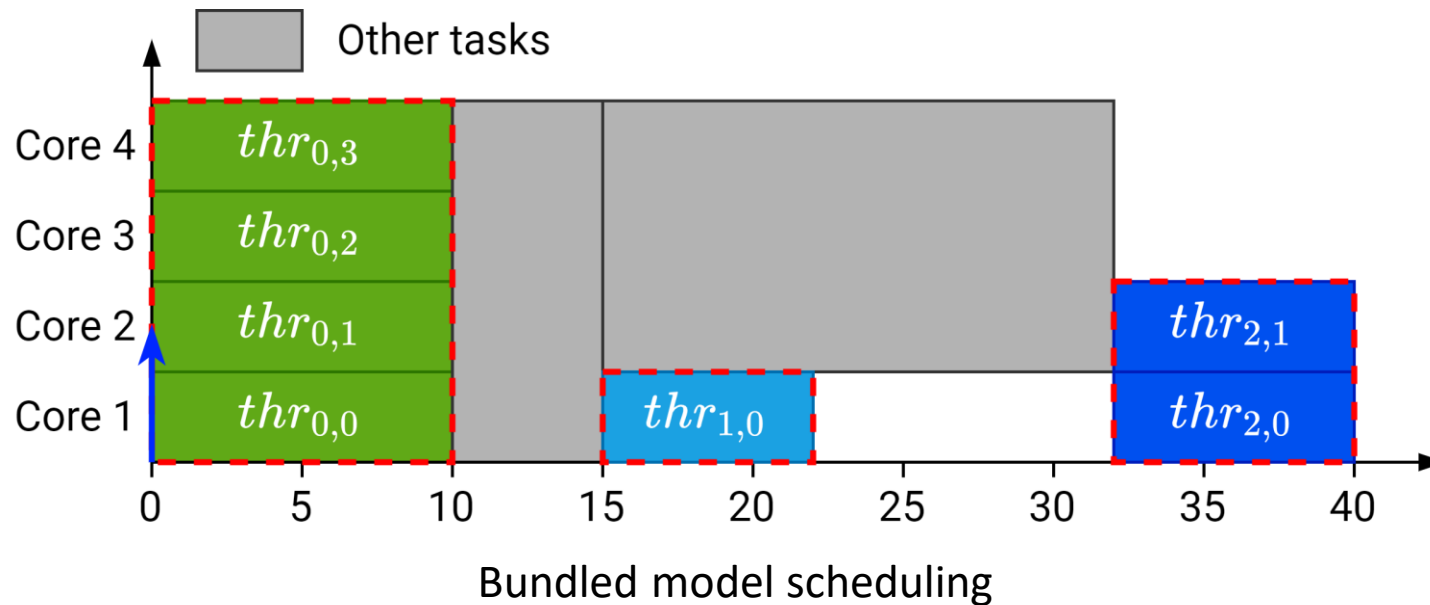
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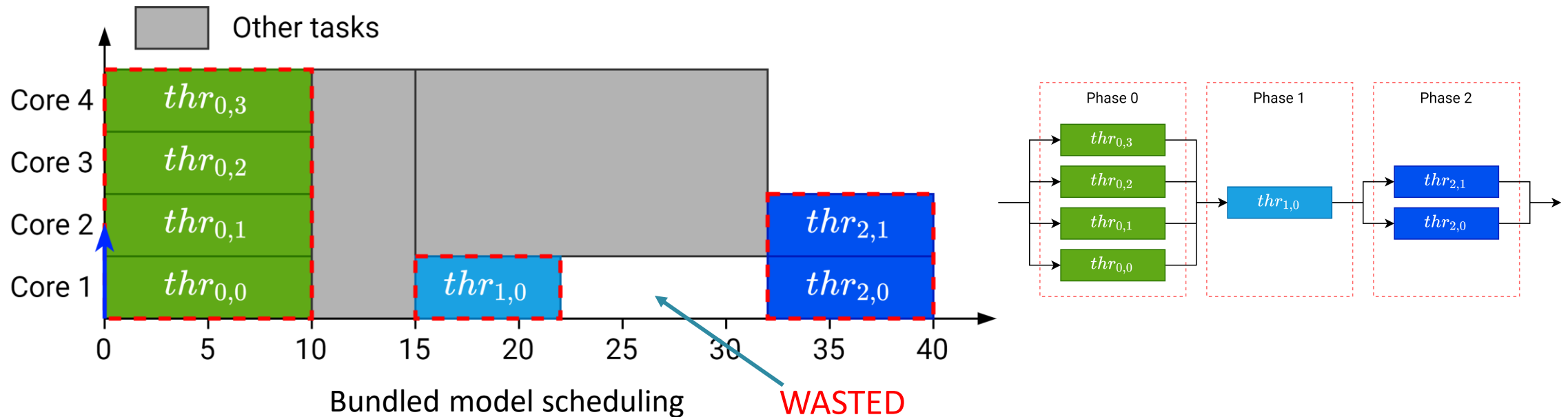
Bundled scheduling^[1] vs limited-preemptive

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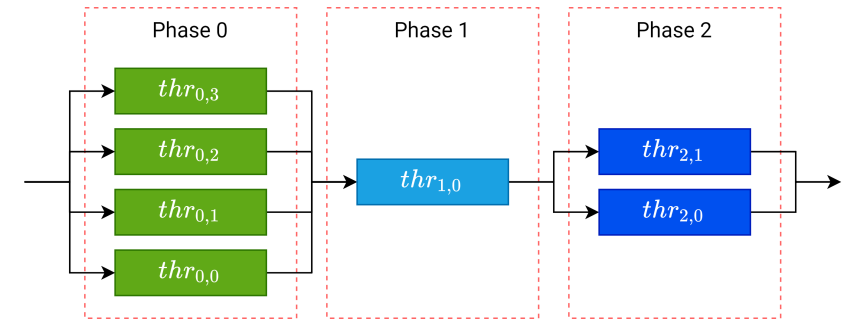
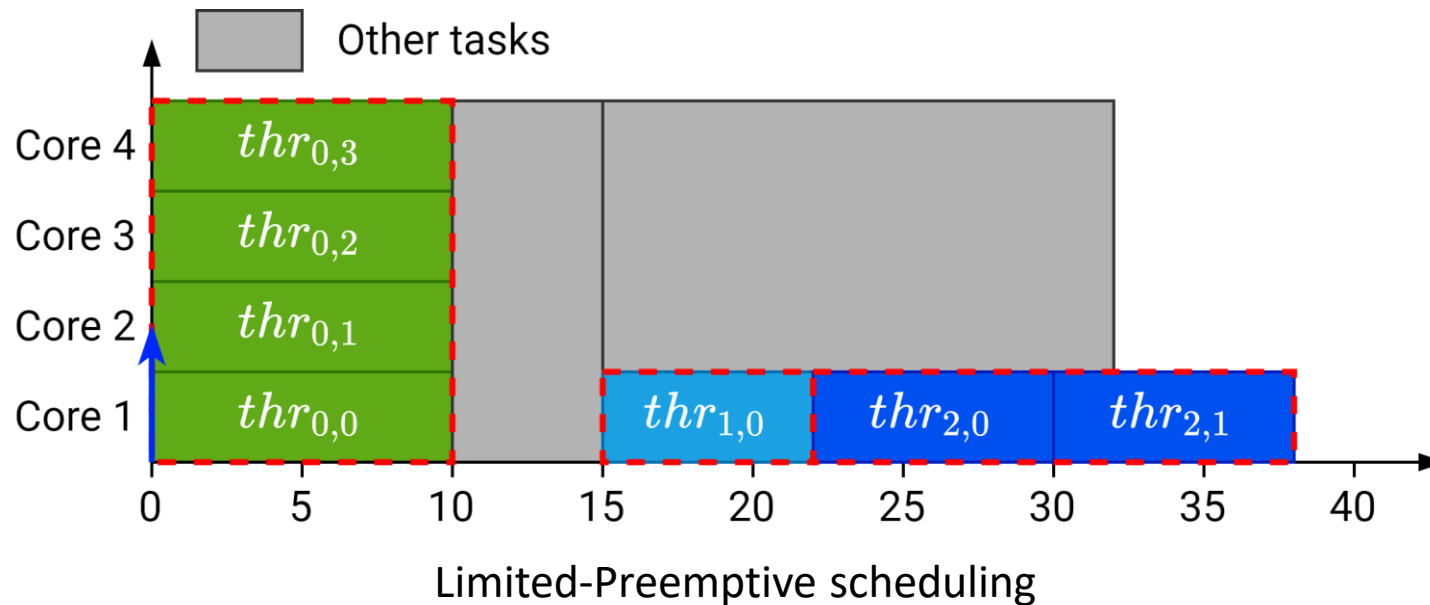
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Bundled scheduling^[1] vs limited-preemptive

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- Limited-Preemptive creates **moldable blocks** with dependencies



Job-level fixed-priority scheduling (JLFP) for gang

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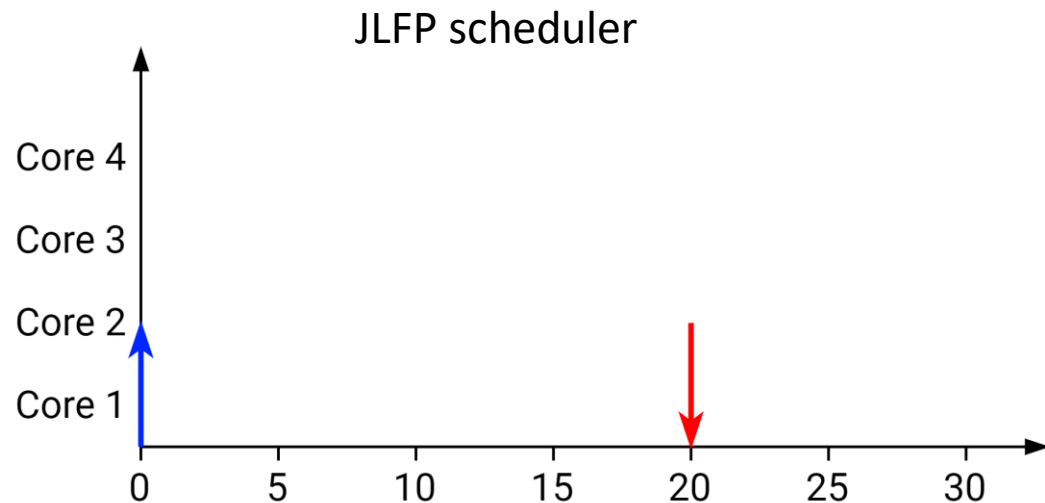
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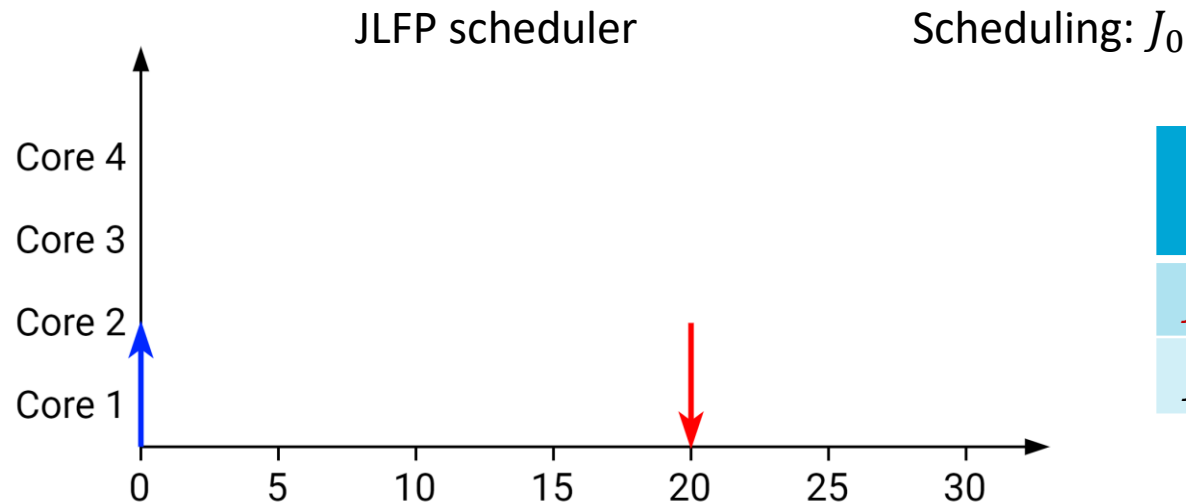
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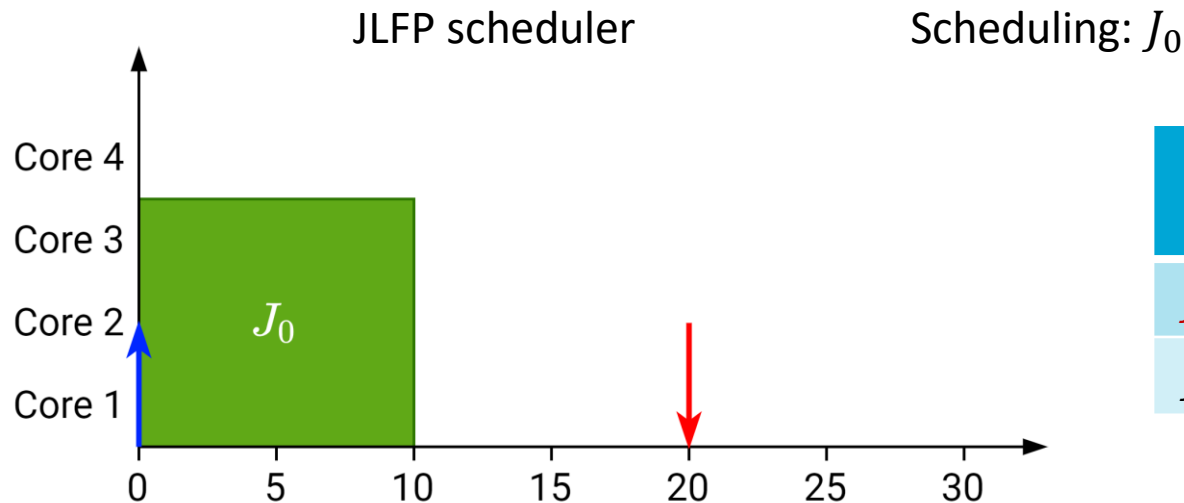
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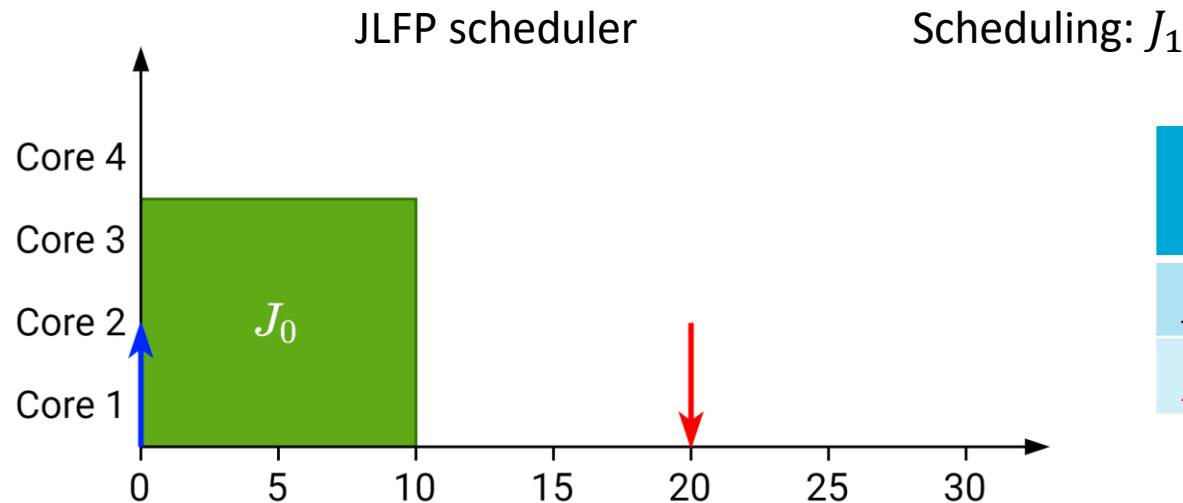
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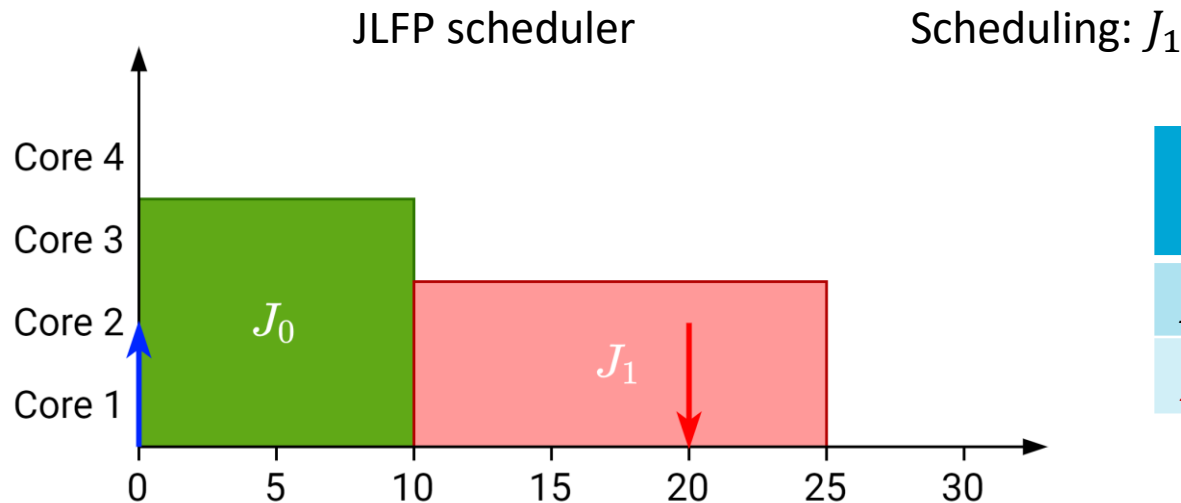
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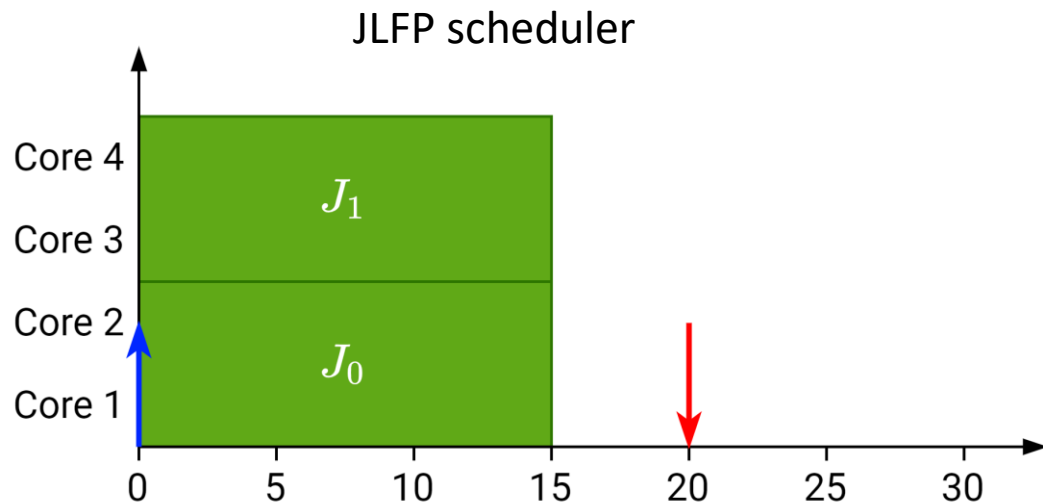
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Previous work

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Preemptive solutions

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Schedulability tests

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- Optimal for rigid gang (DP-Fair)^[4]

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Non-preemptive solutions

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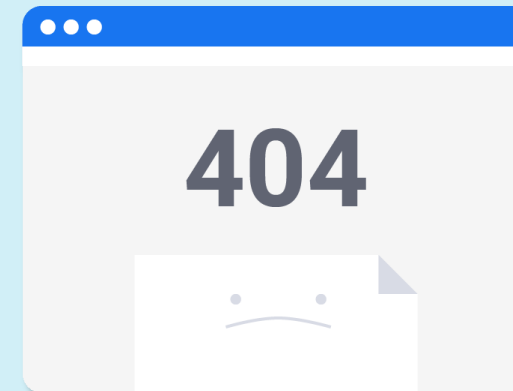
Schedulability tests

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Non-preemptive solutions



Our work

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1. Design an accurate schedulability **analysis** for limited-preemptive **moldable gang tasks**

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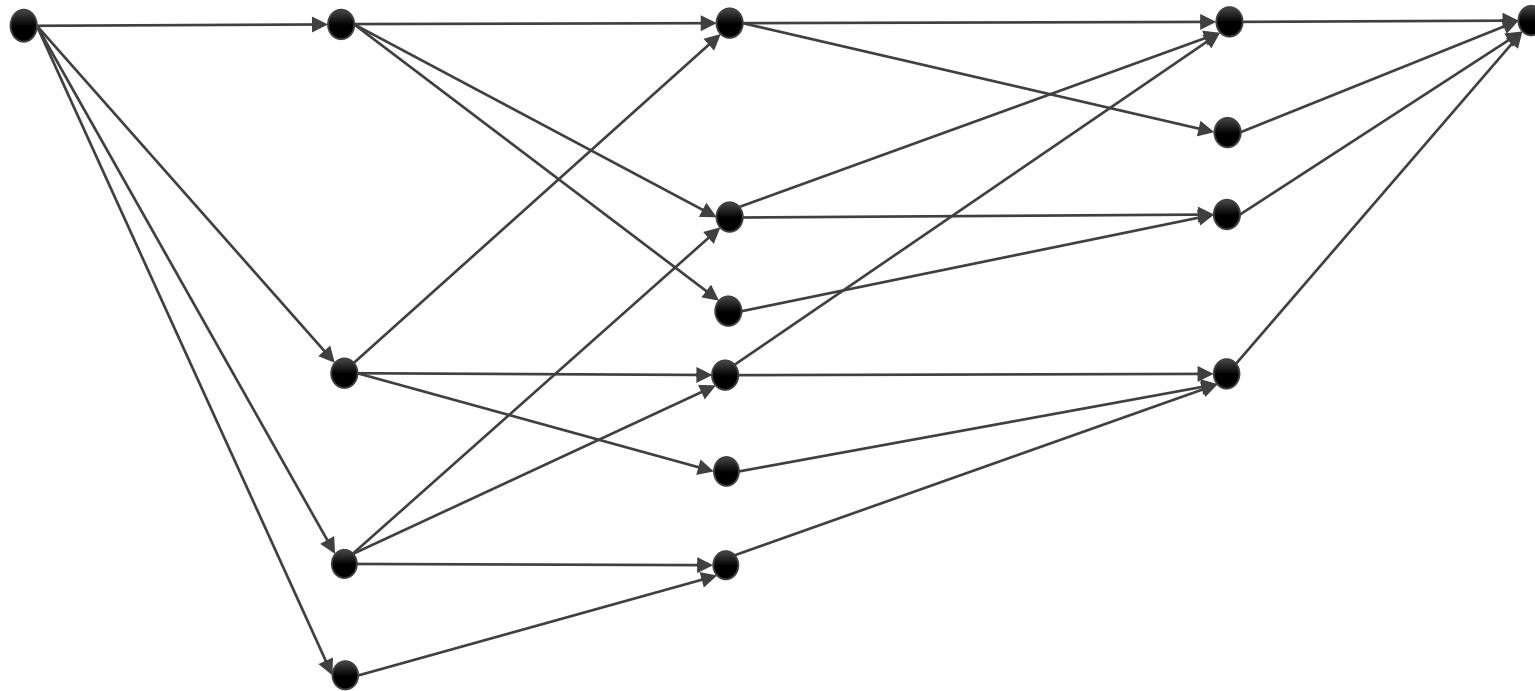
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 - Extend analysis to support this new algorithm

Agenda

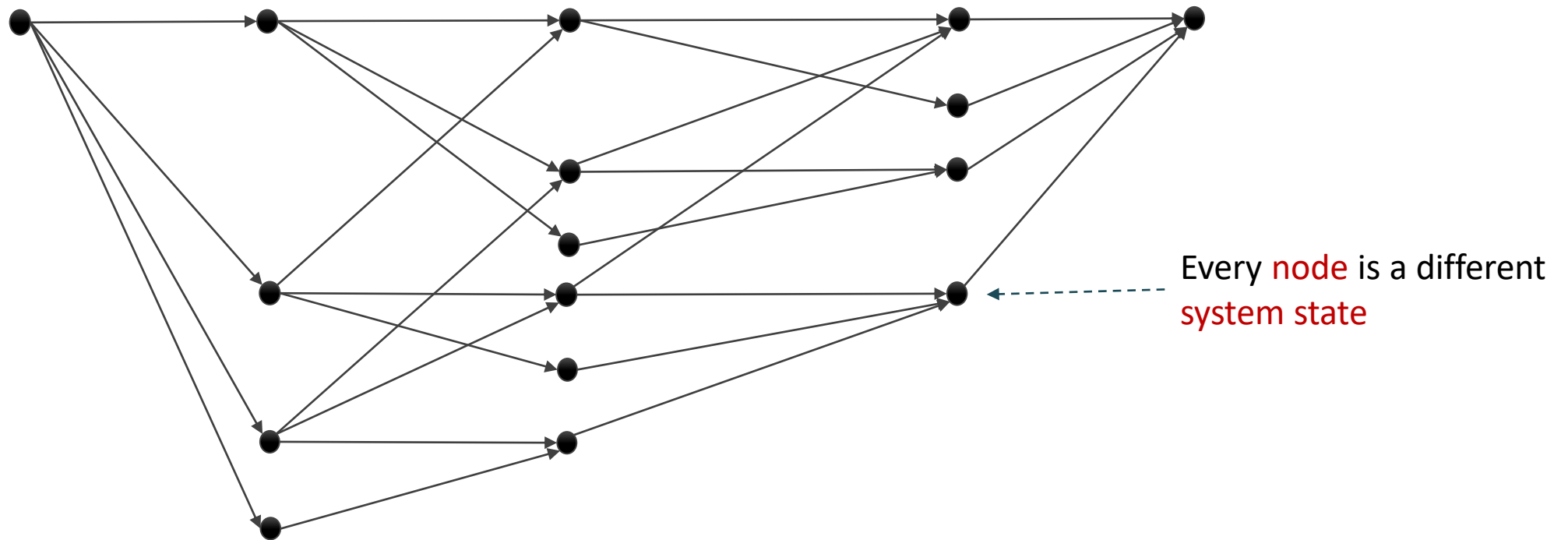
- Gang schedulability analysis
- New scheduling policy

Schedule abstraction graph

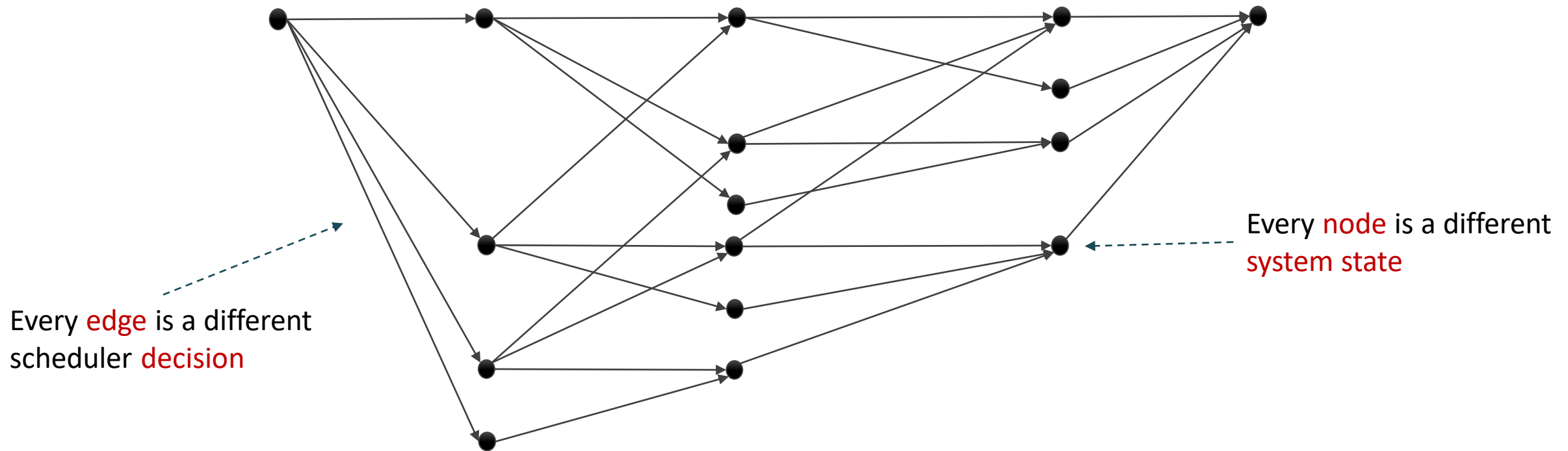
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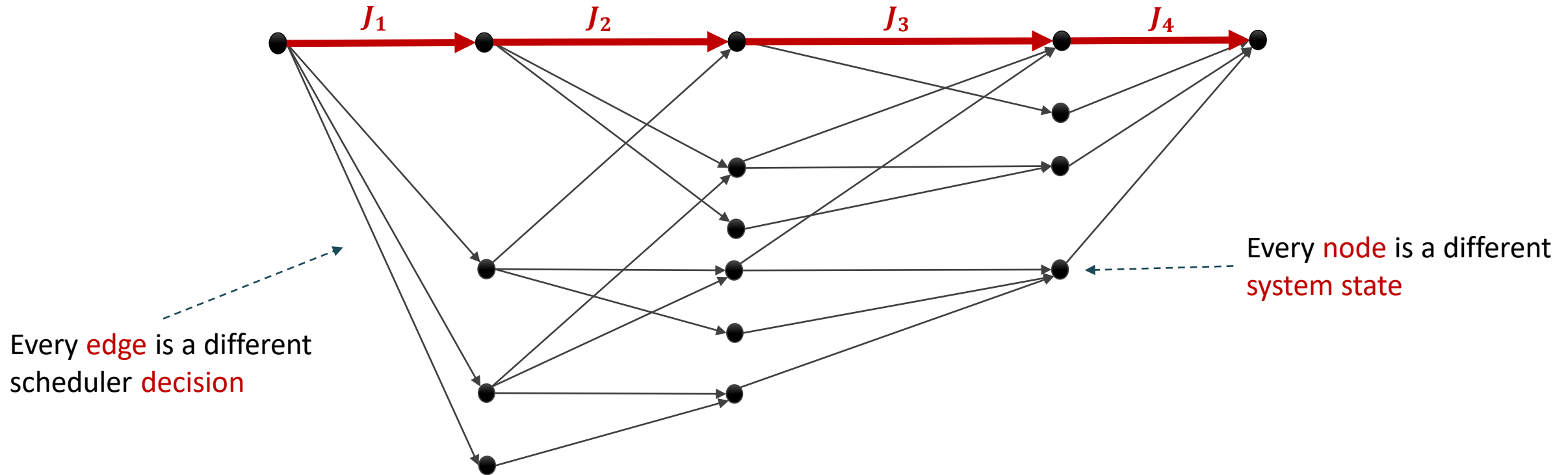
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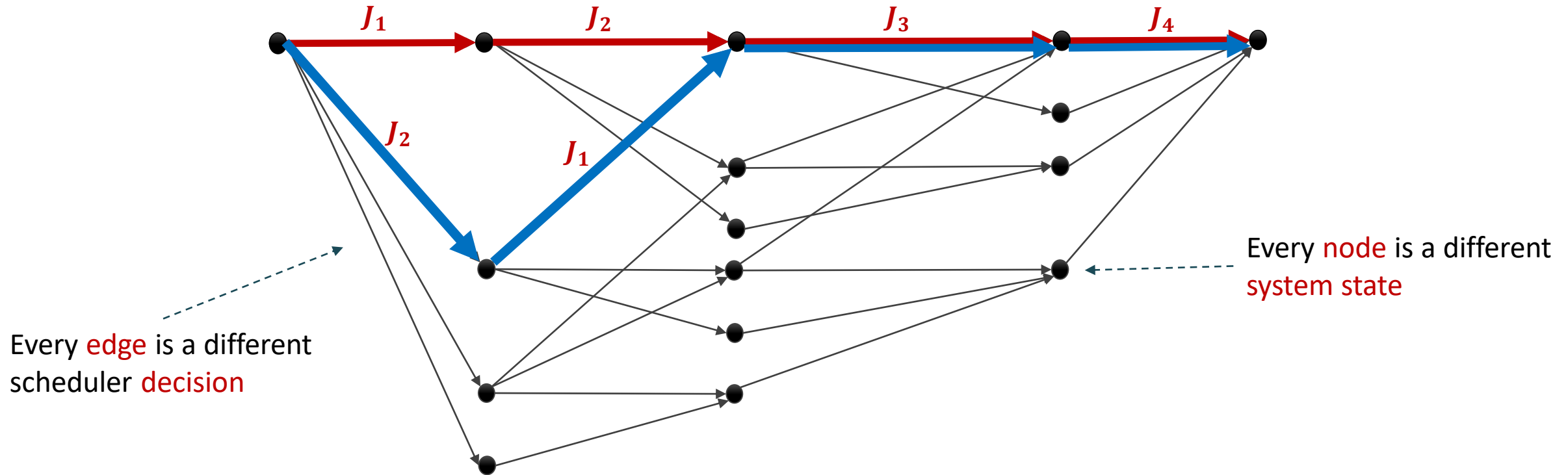
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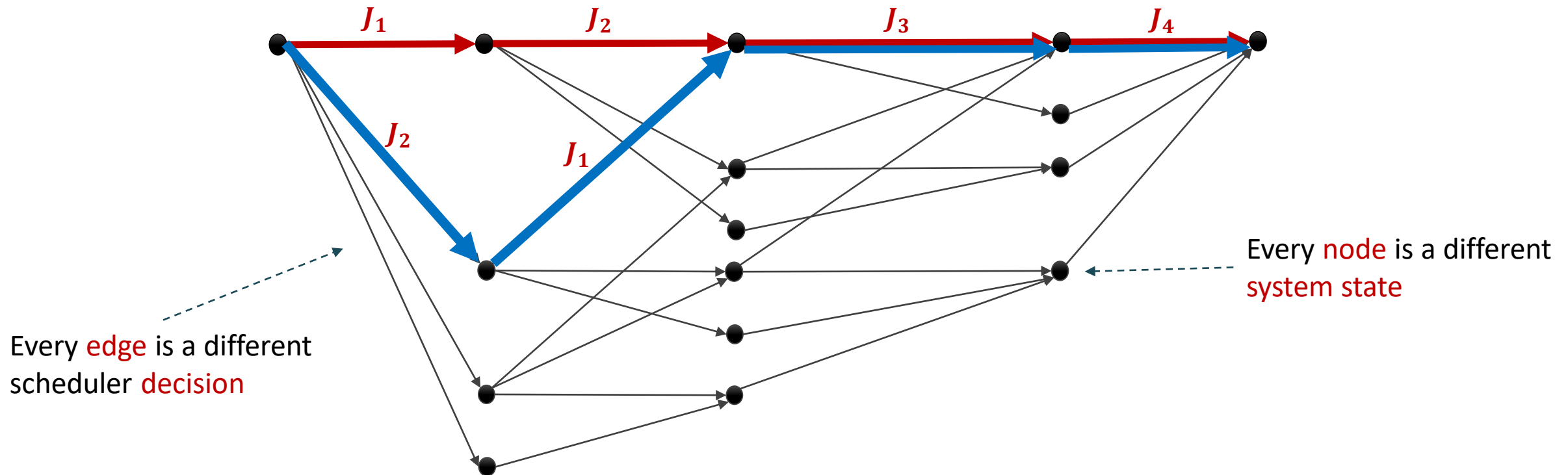


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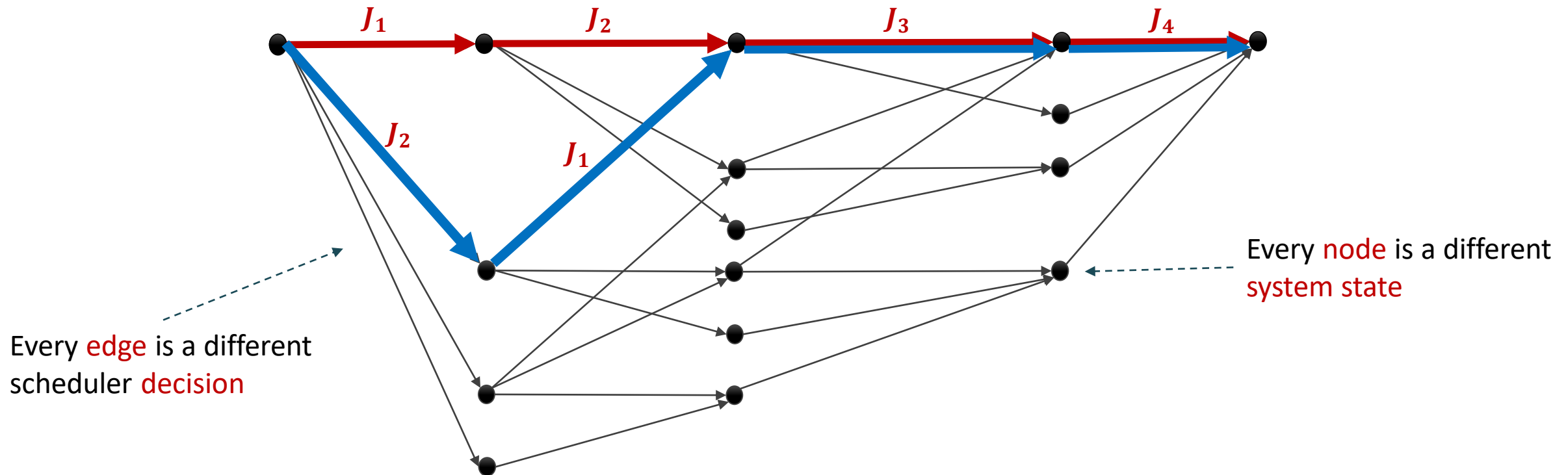
Schedule abstraction graph

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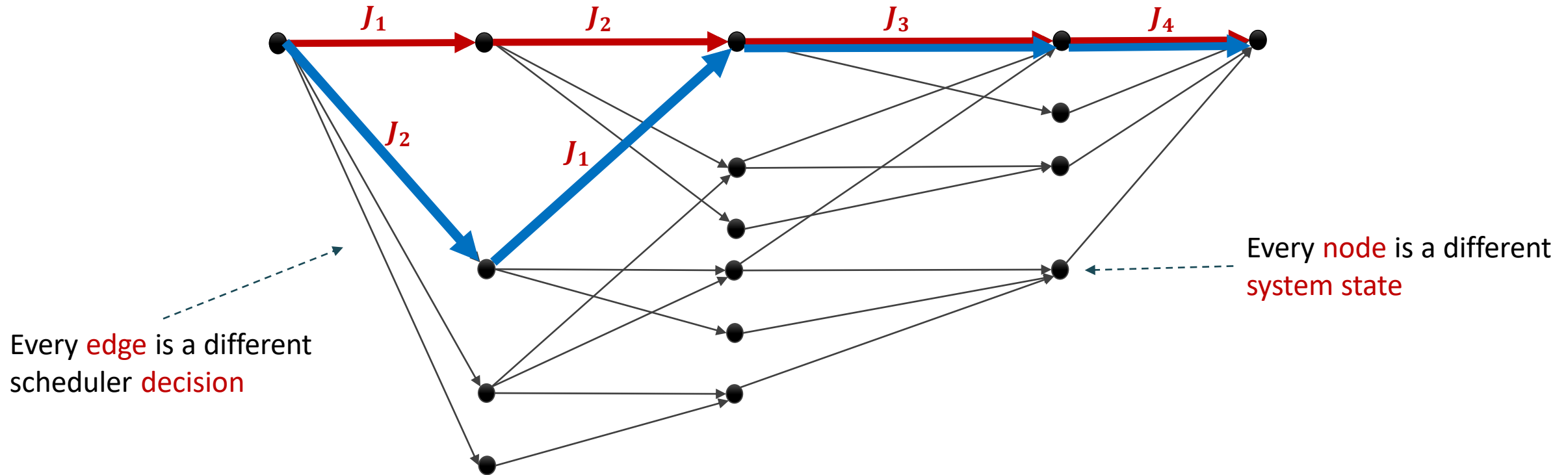
Schedule abstraction graph

- It is a technique that allows:
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Schedule abstraction graph

- It is a technique that allows:
 - Search for all possible schedules
 - Aggregate “similar” schedules



SAG analysis changes for gang

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- Previously a state was created for every schedulable job

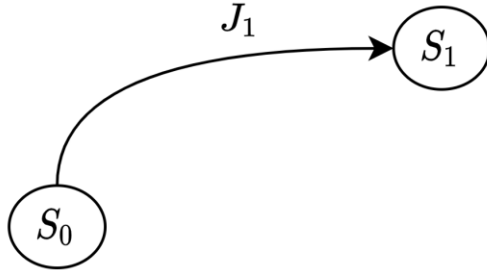
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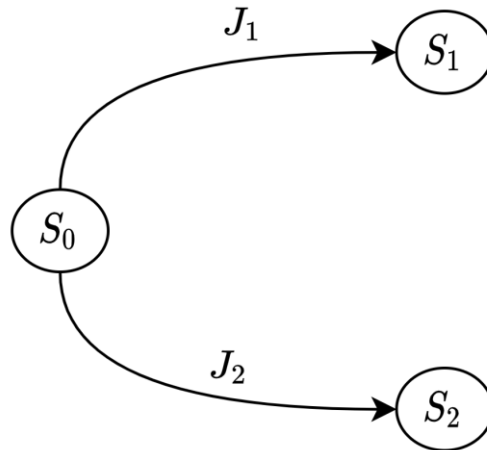
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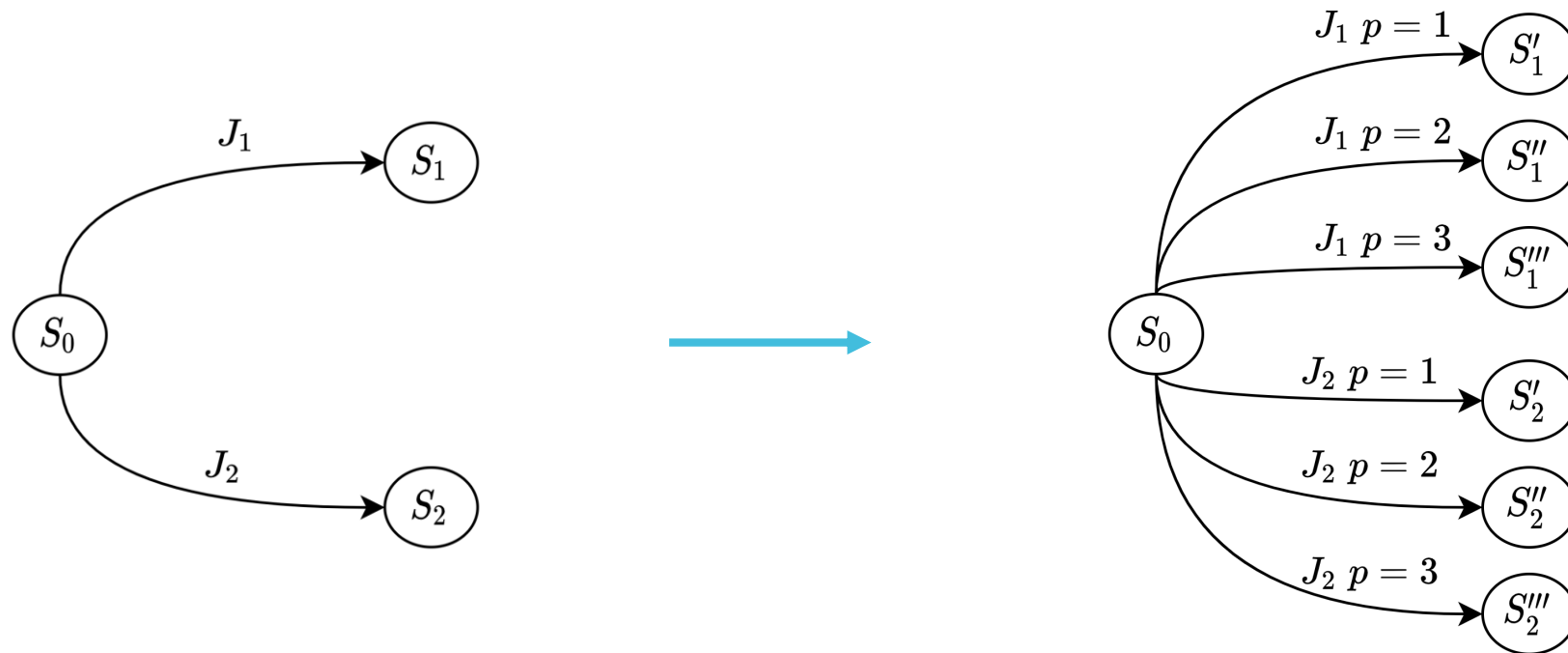
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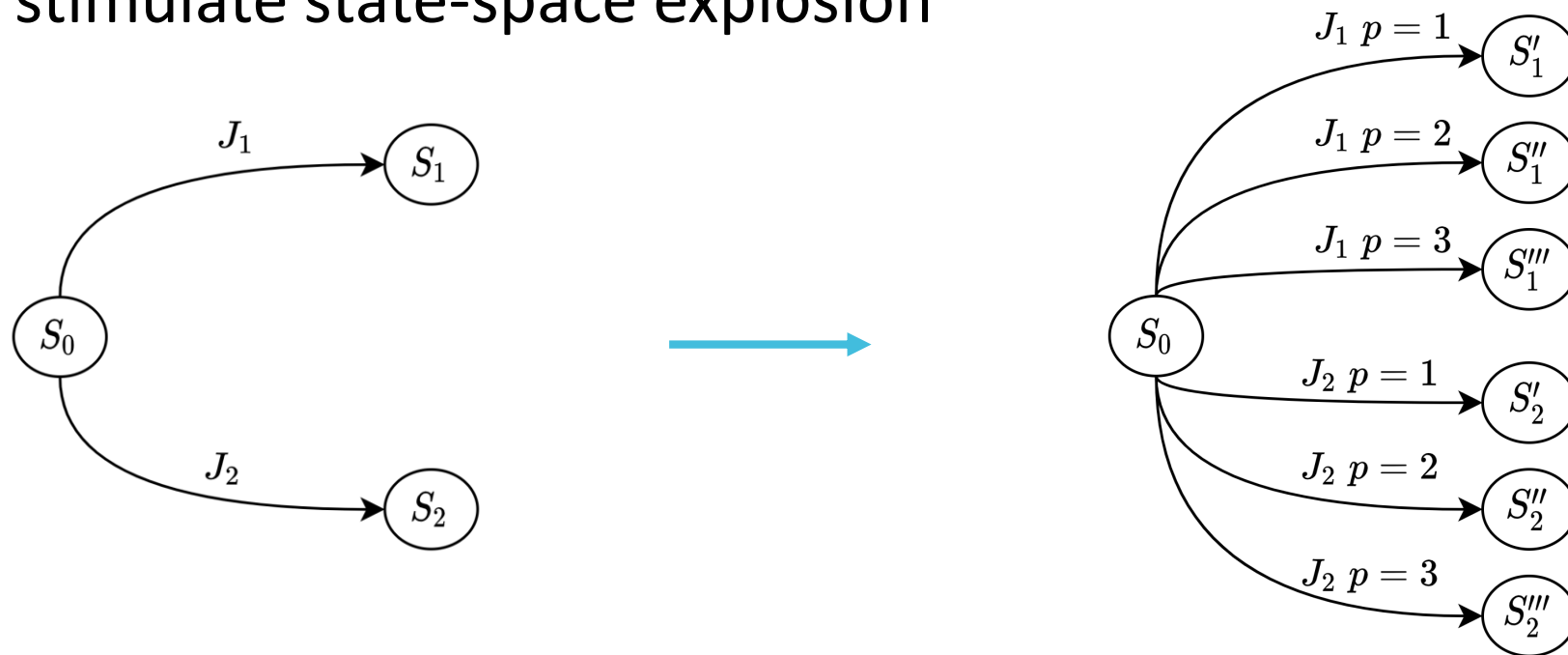
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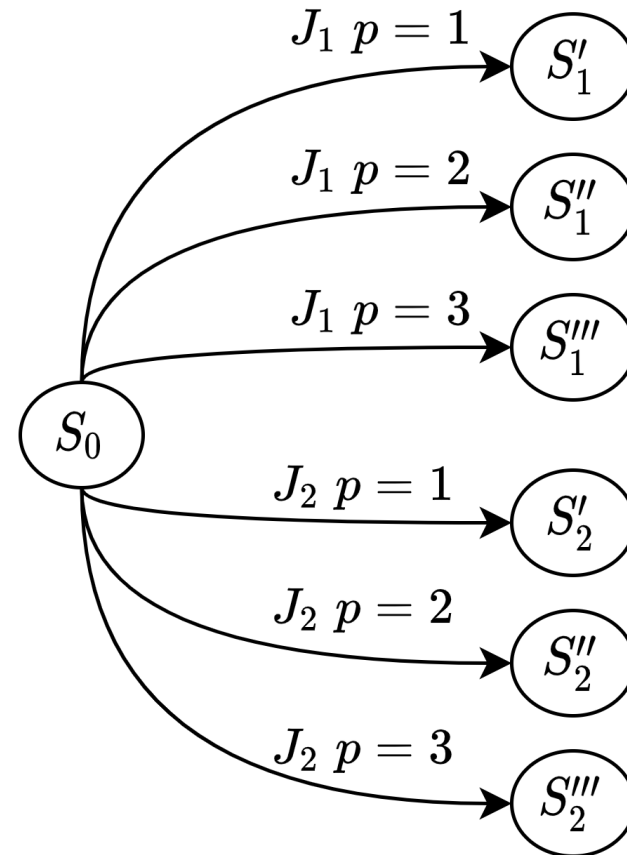
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- Previously a state was created for every schedulable job
- Now a state is created for every job and possible number of cores
- Can stimulate state-space explosion



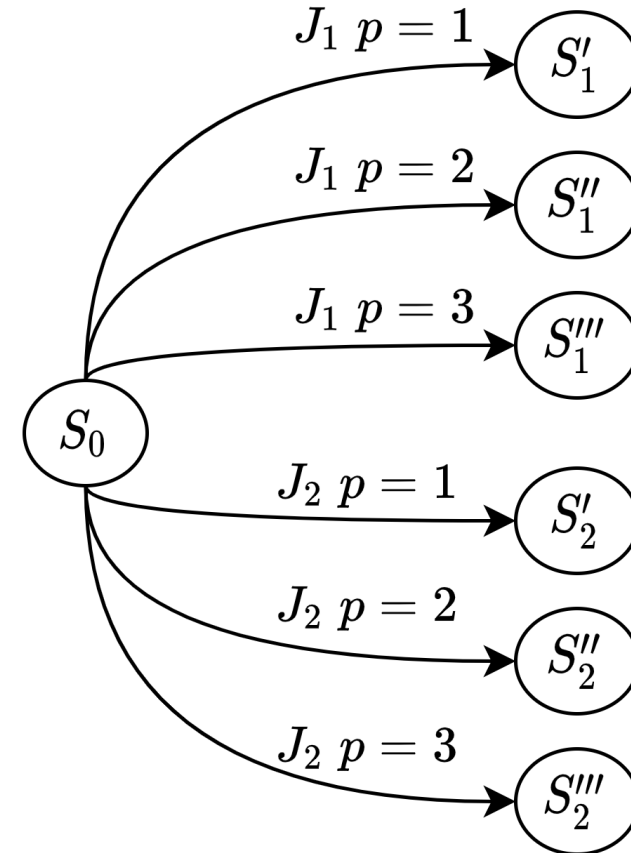
SAG analysis changes for gang

- cores available



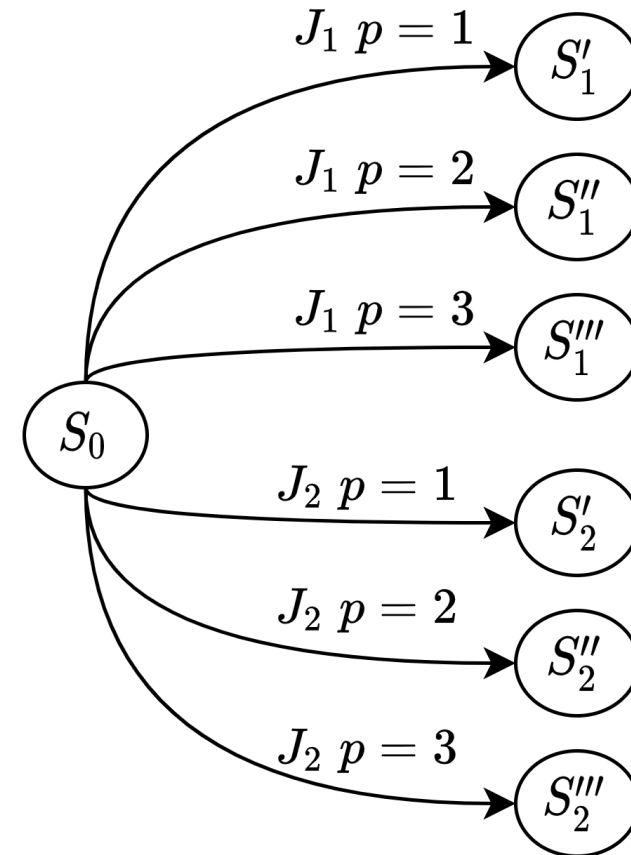
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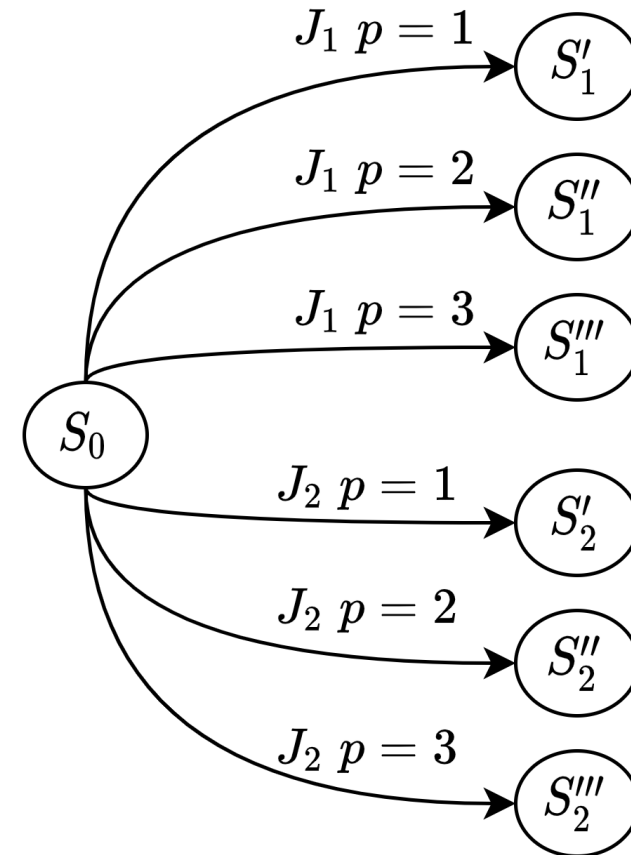
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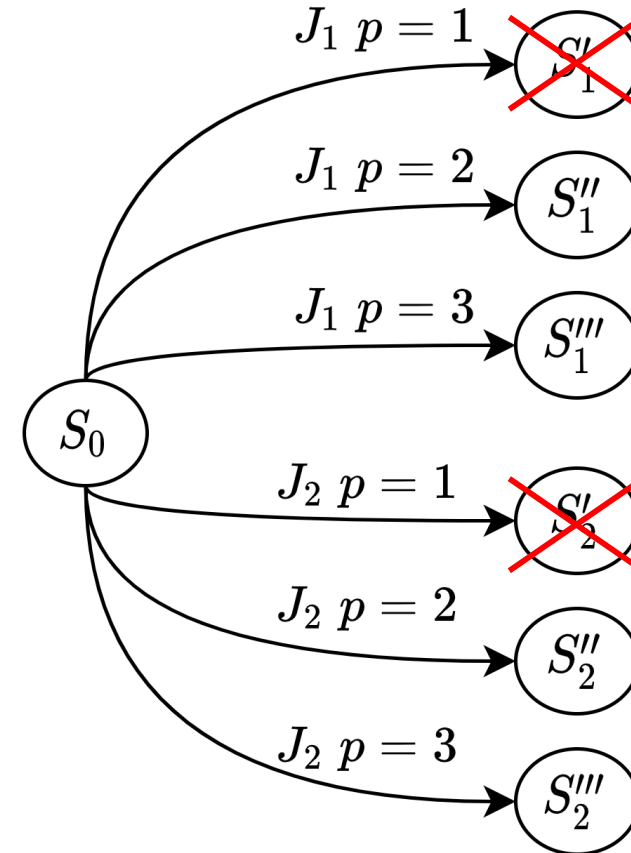
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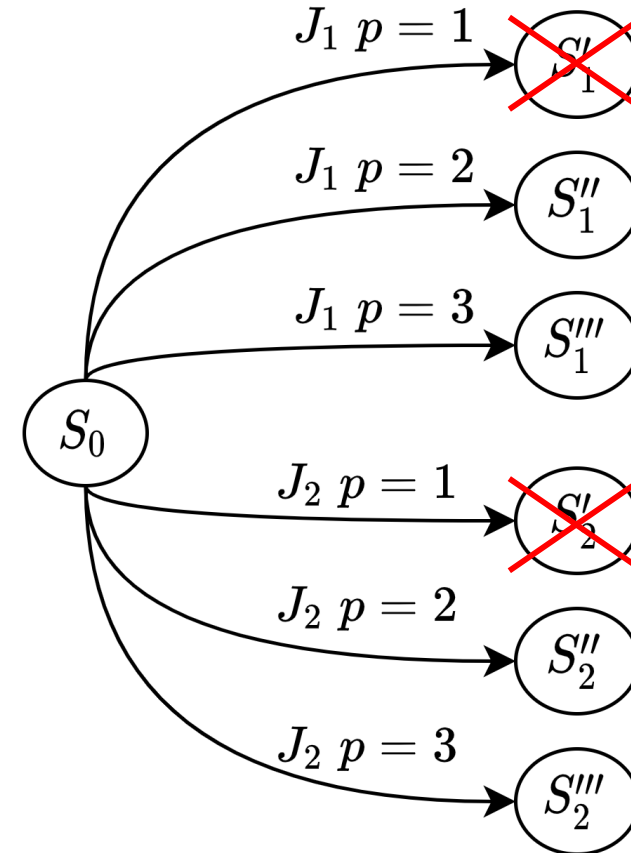
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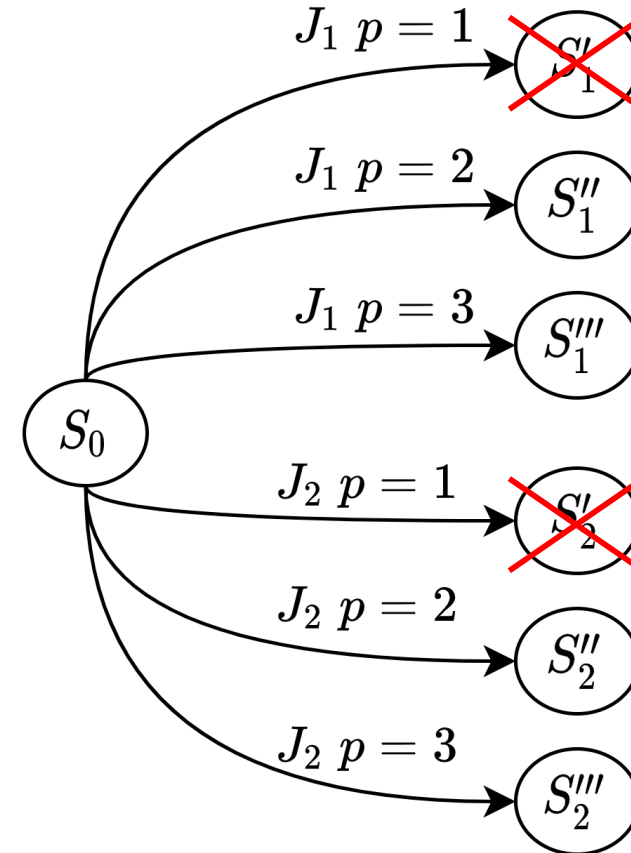
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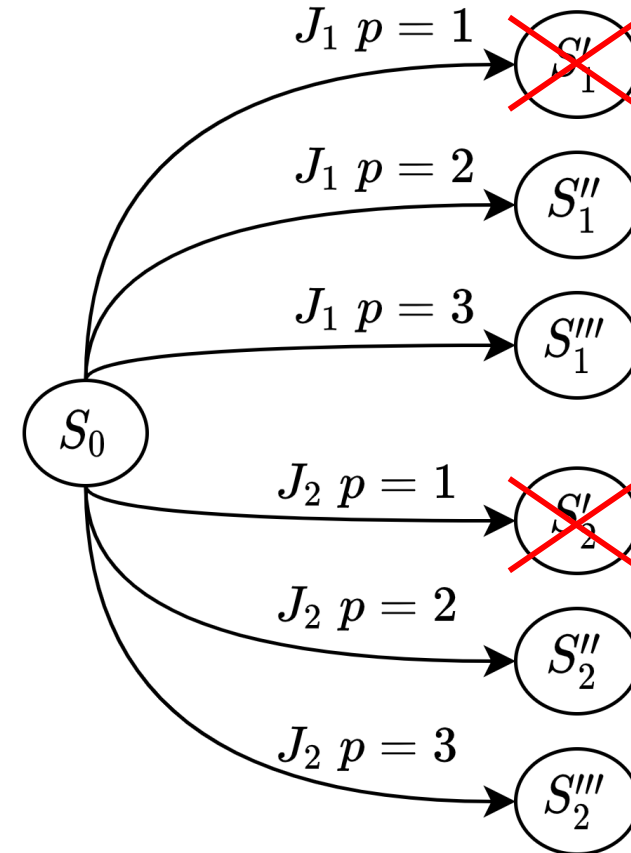
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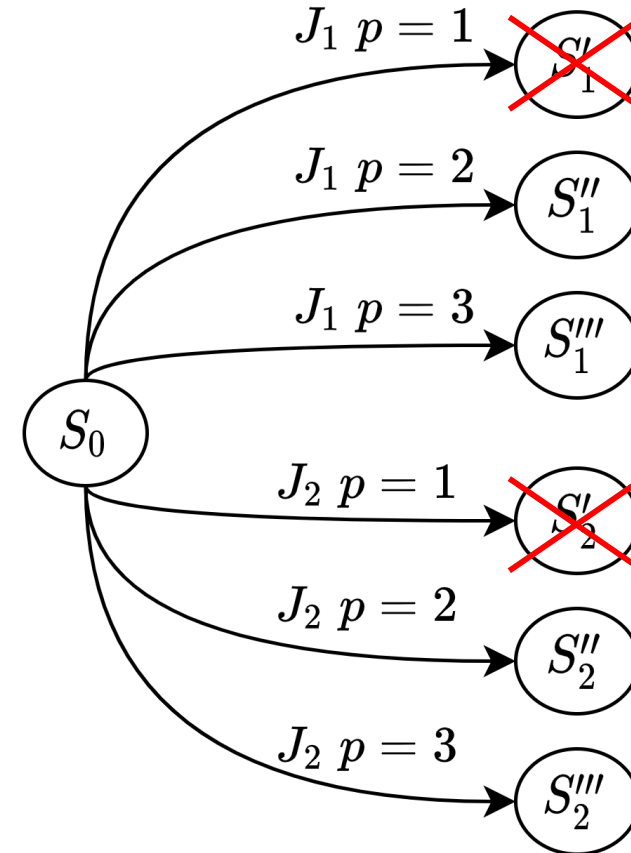
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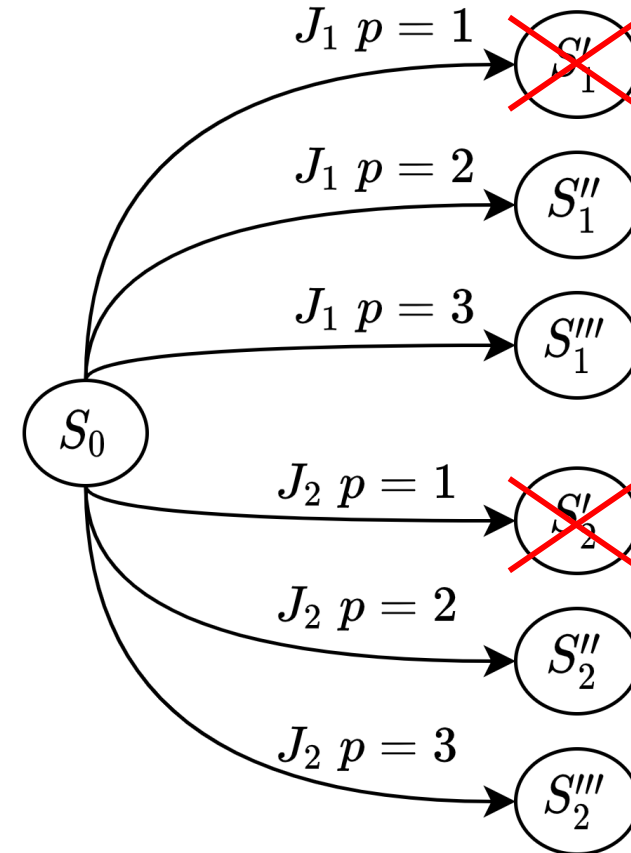
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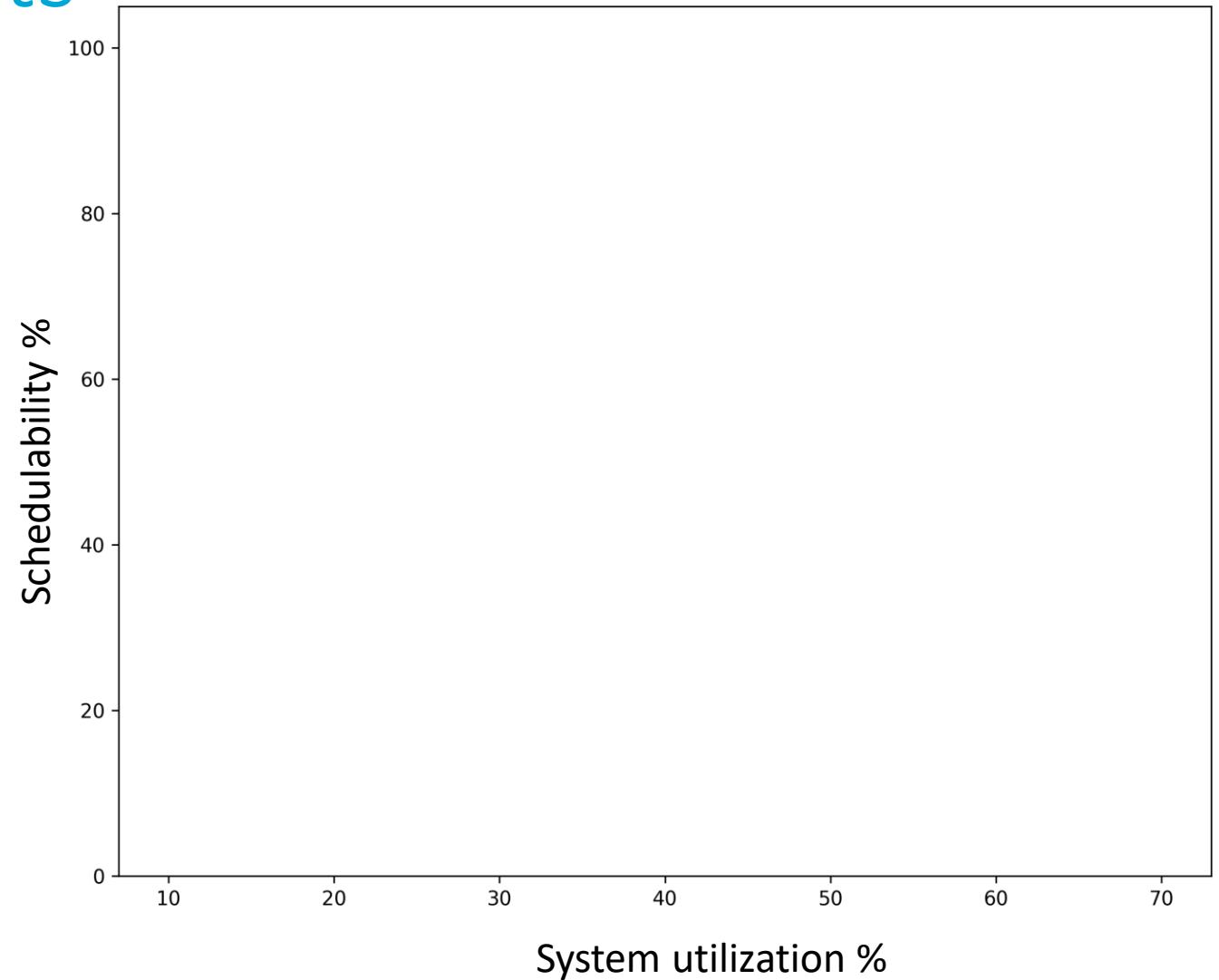
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- Proofs



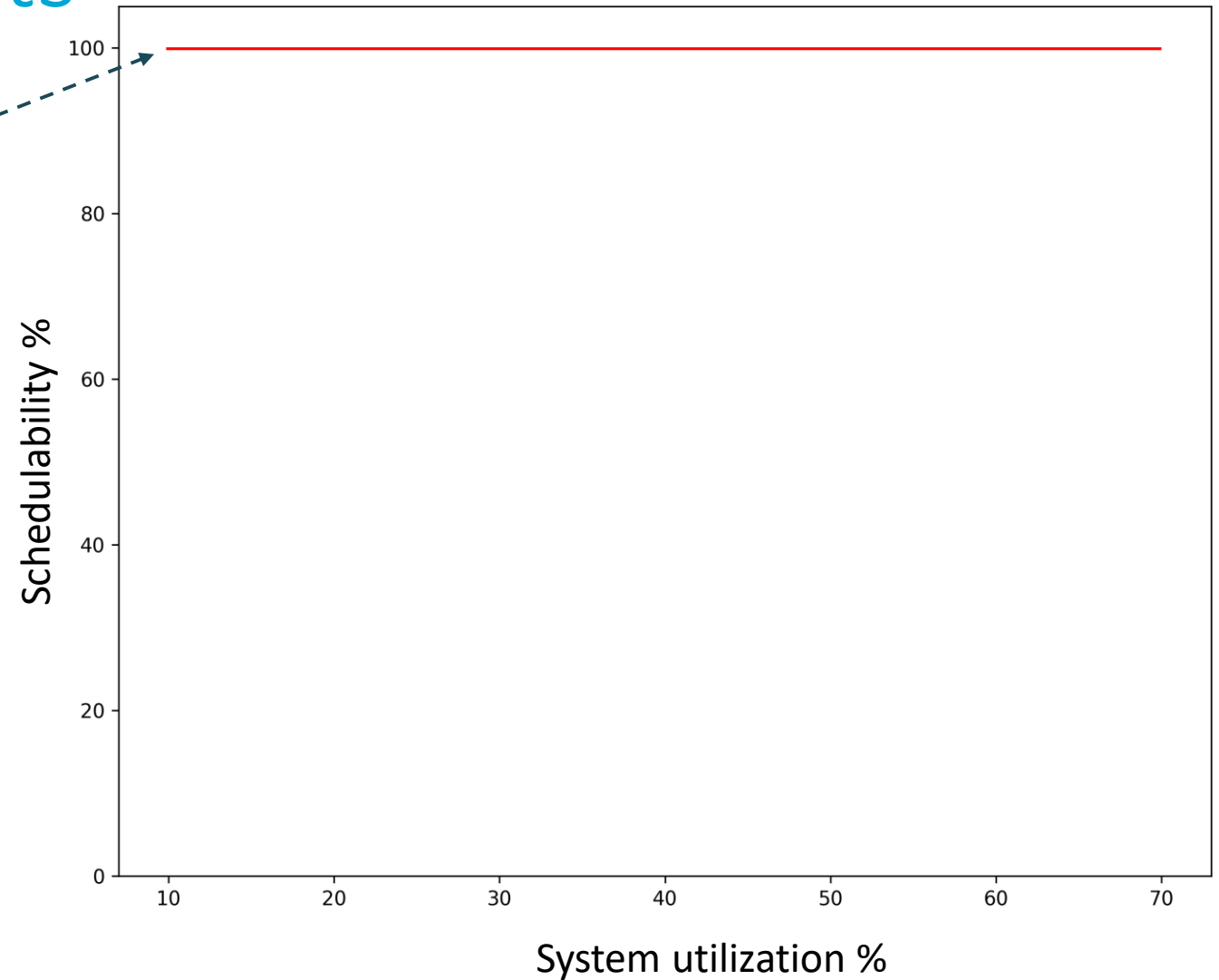
Our analysis' results

Our analysis' results



Our analysis' results

All task sets pass necessary test

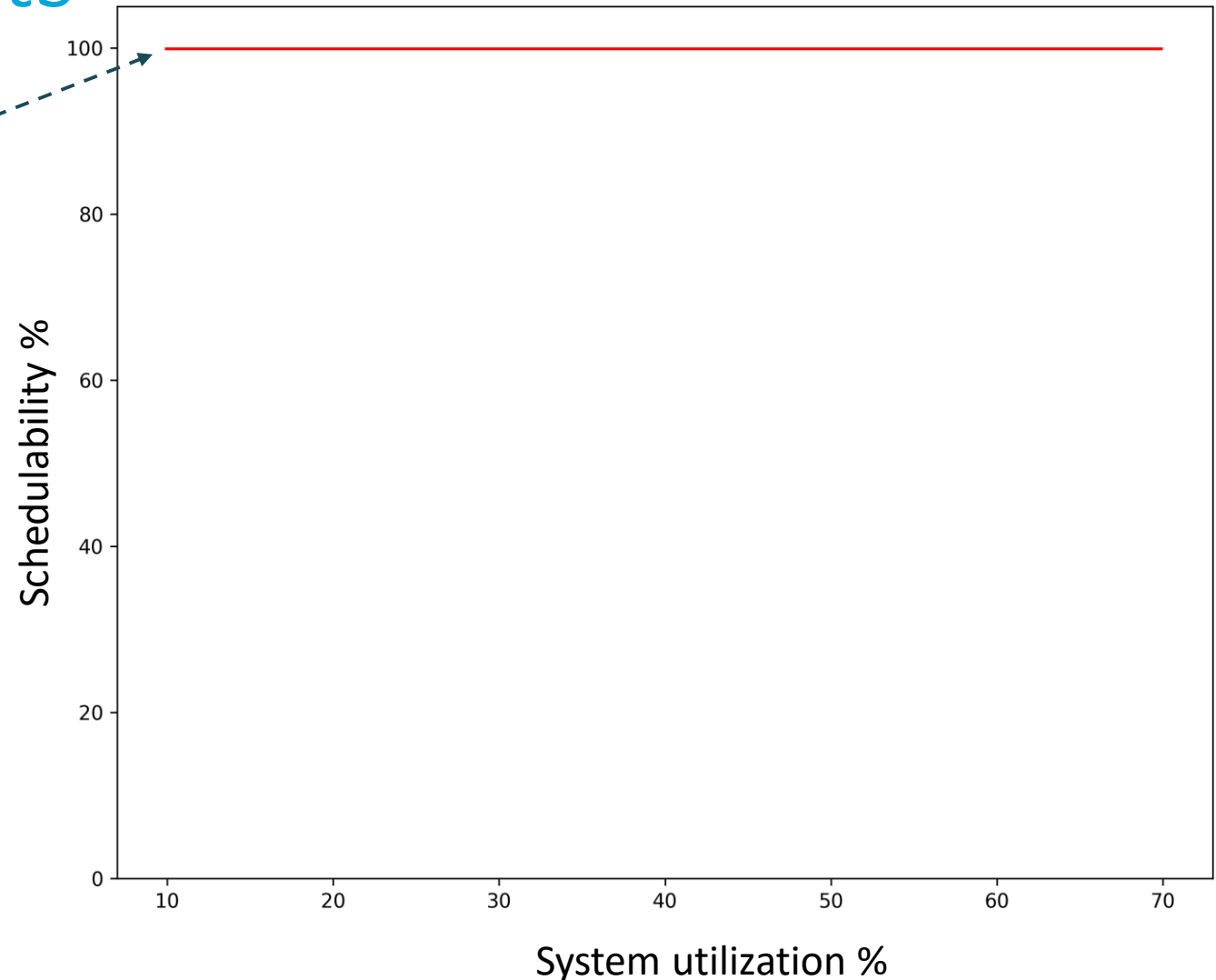


Our analysis' results

All task sets pass necessary test

- System processors: 8
- System tasks: 4
- Execution-time variation: 25%
- Segments per task: 1

Randomly generated task sets

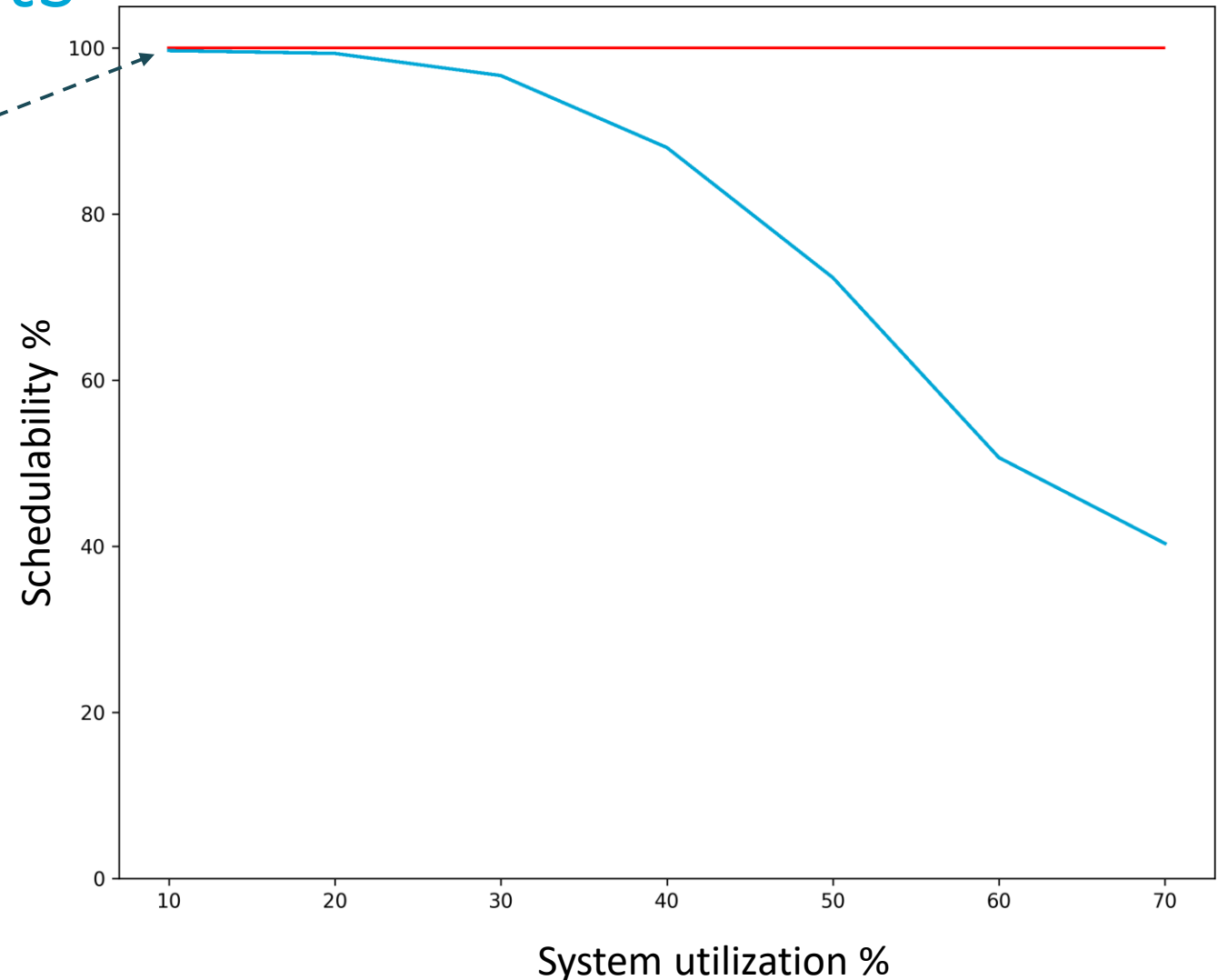


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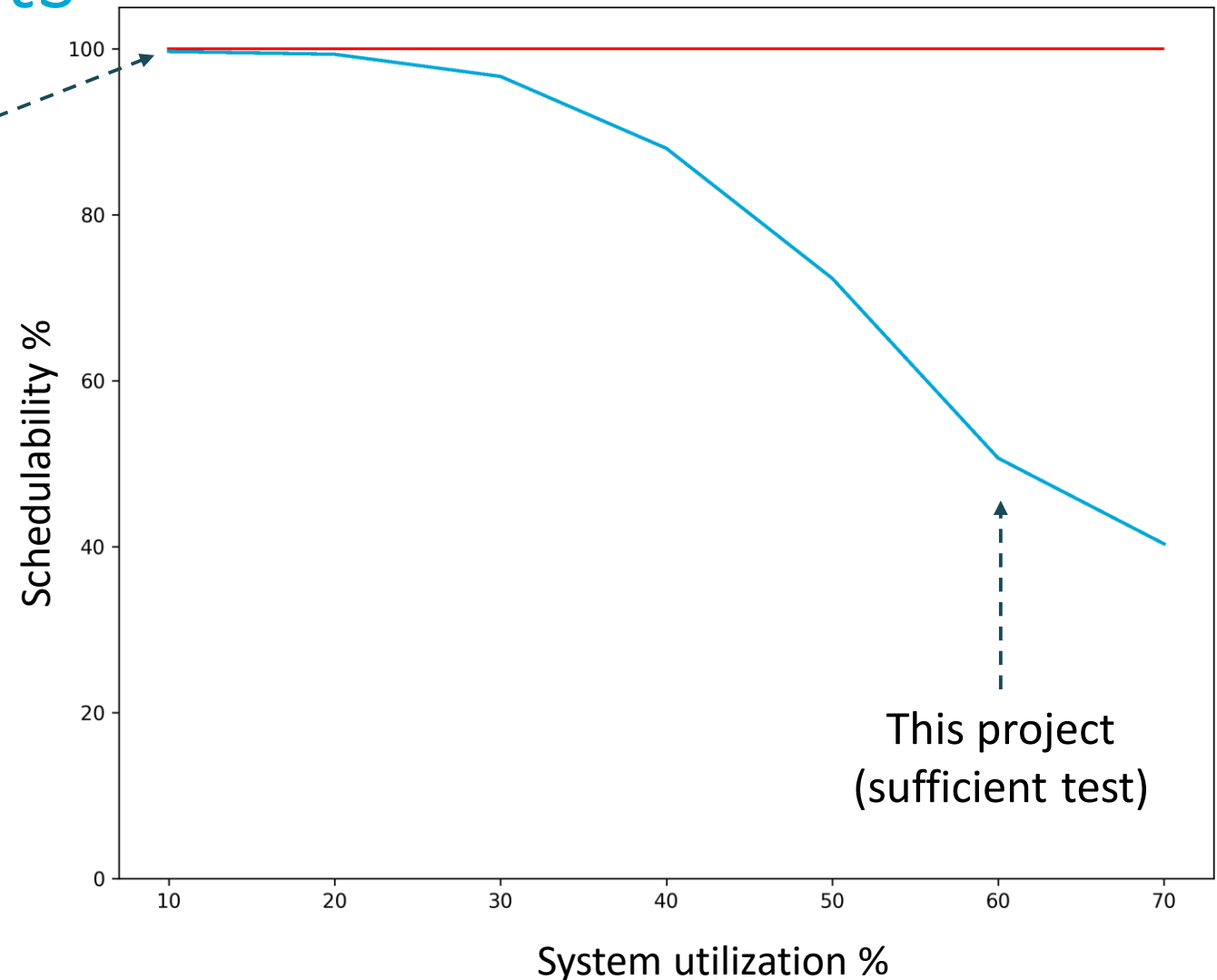


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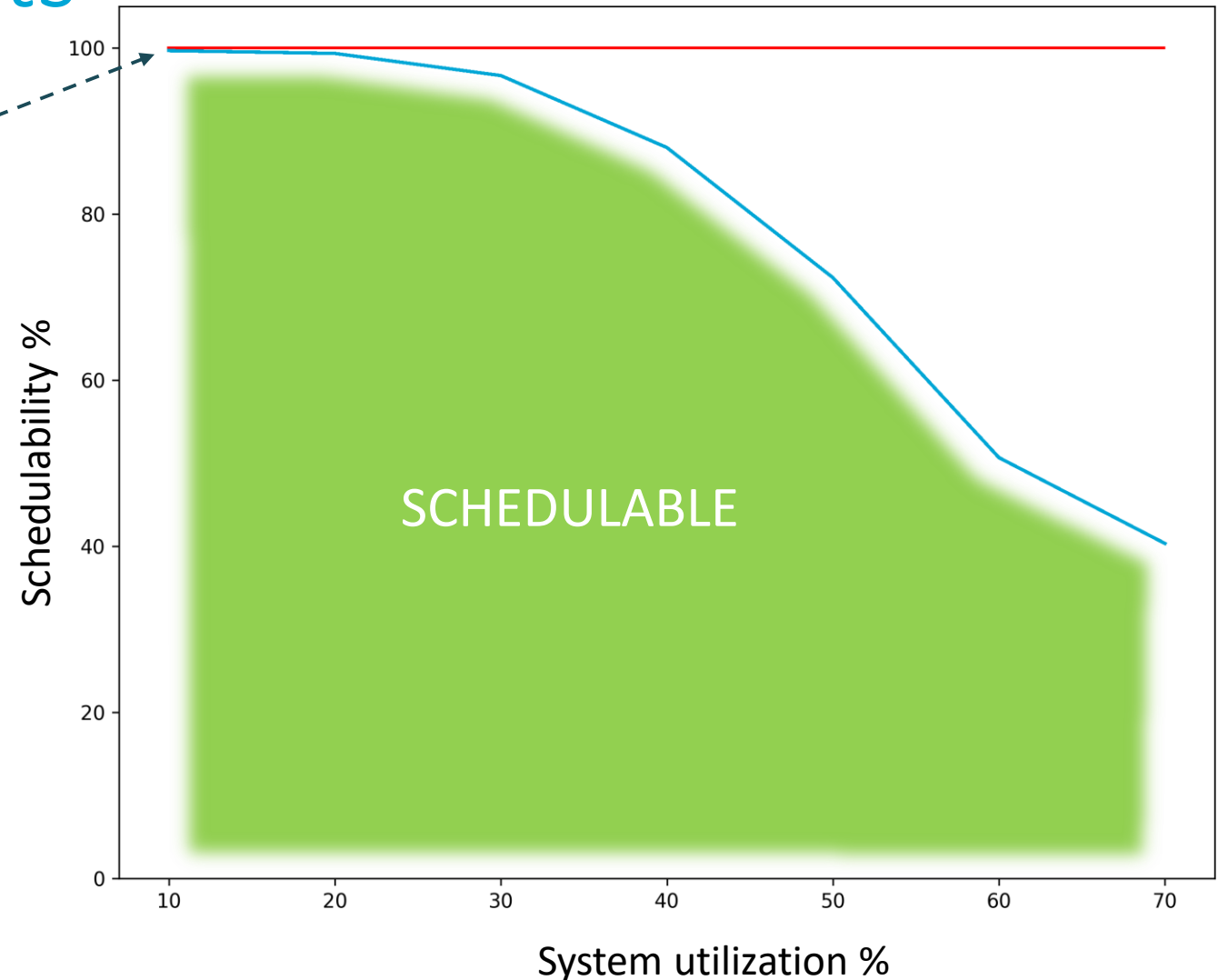


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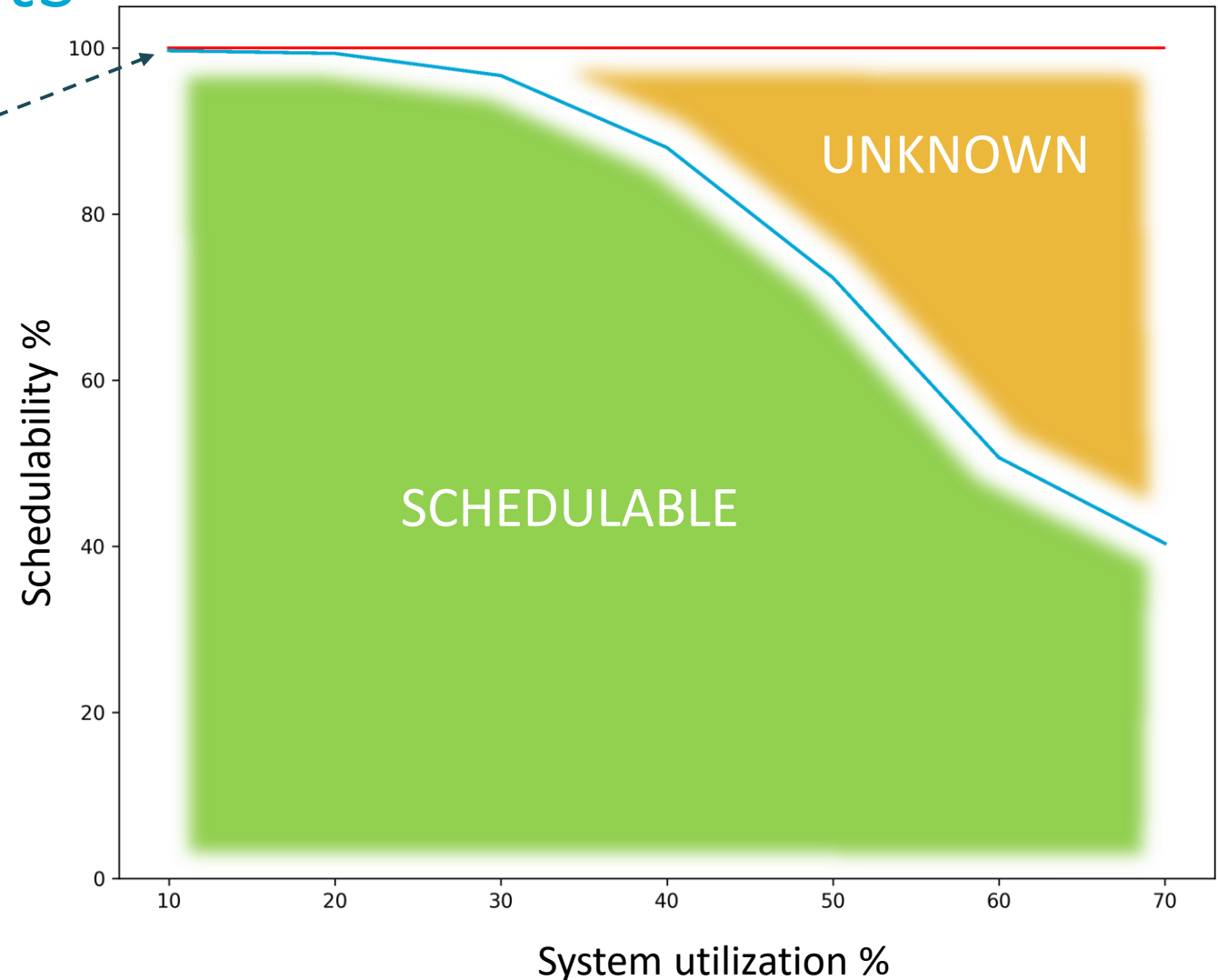


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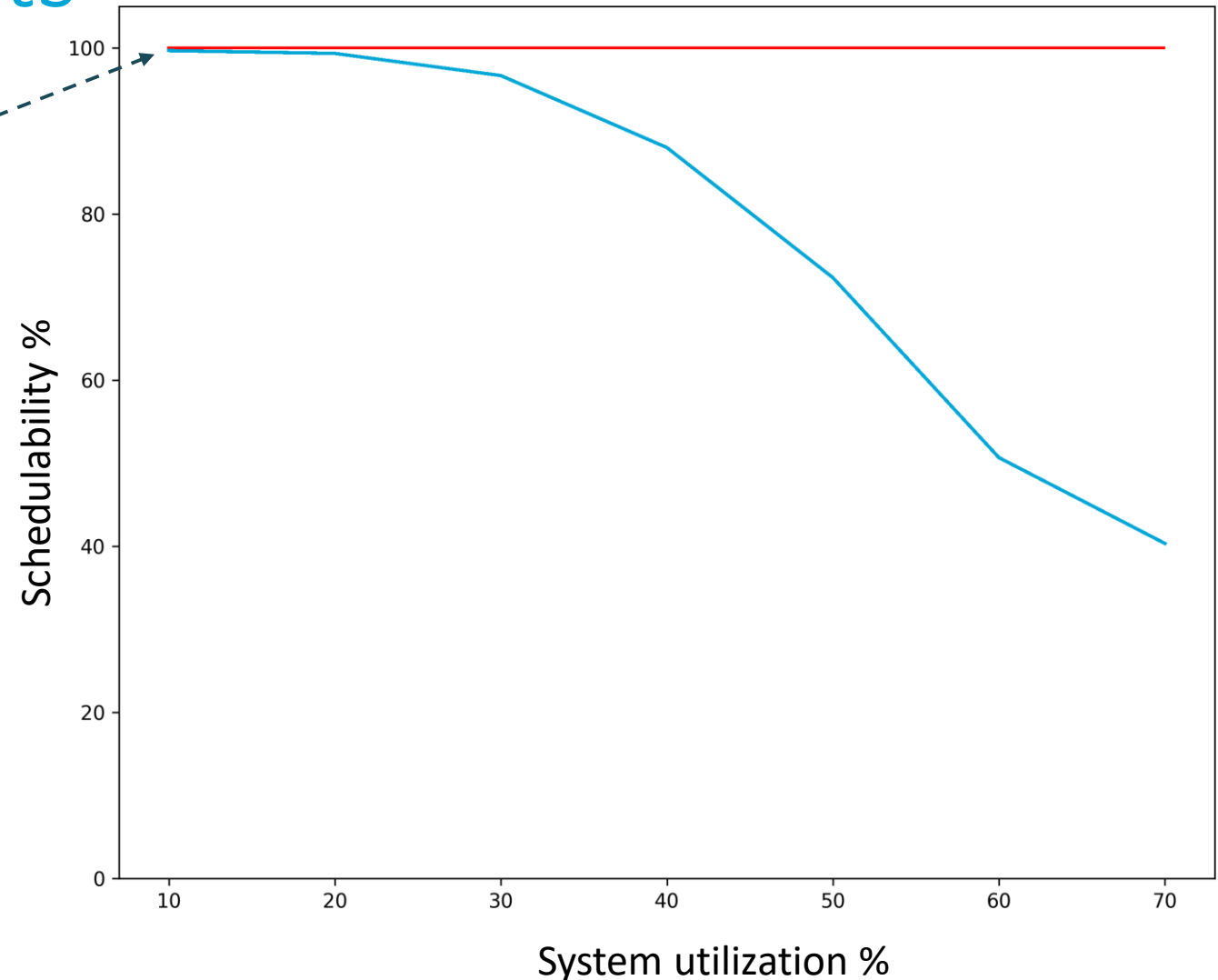


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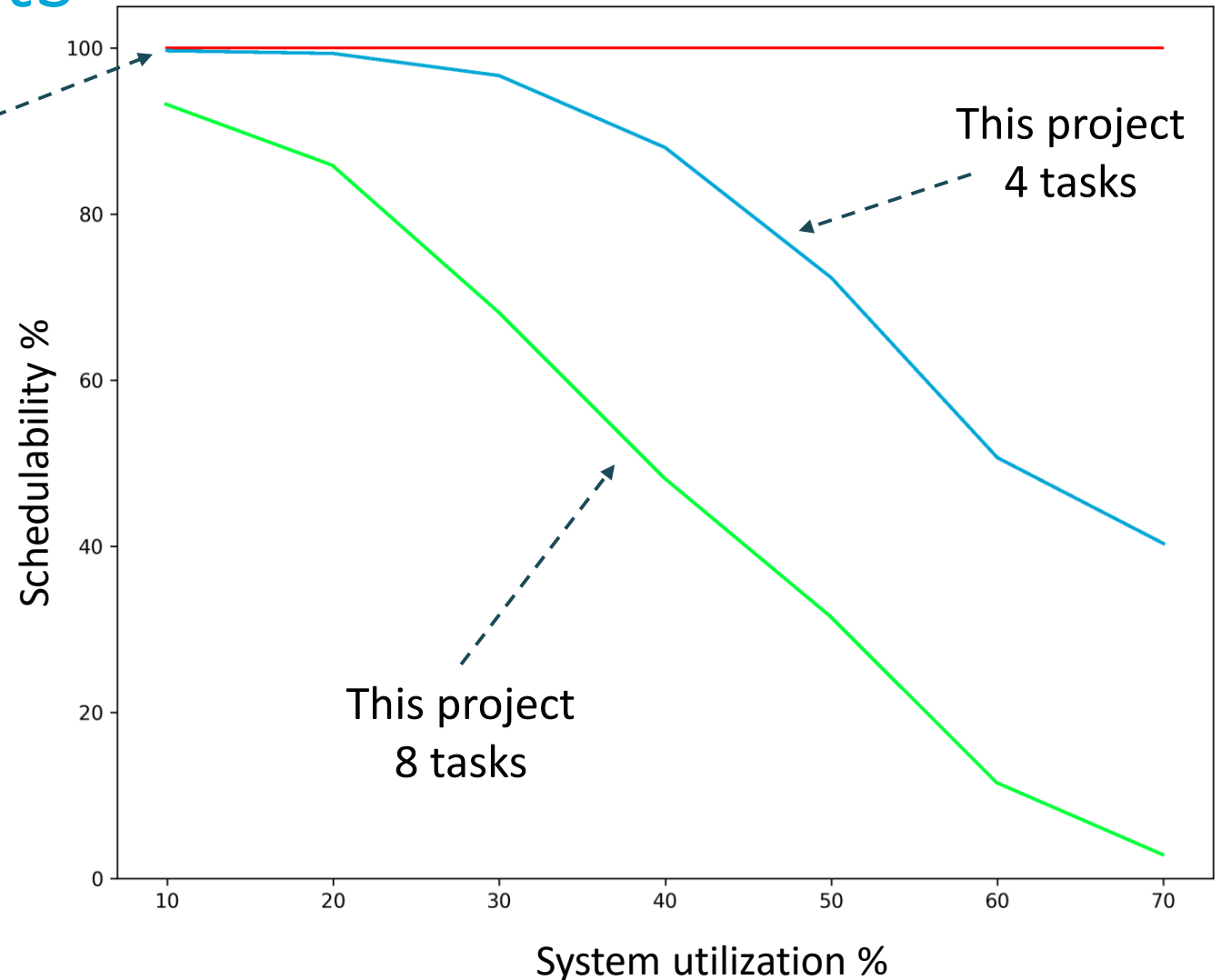


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Randomly generated task sets

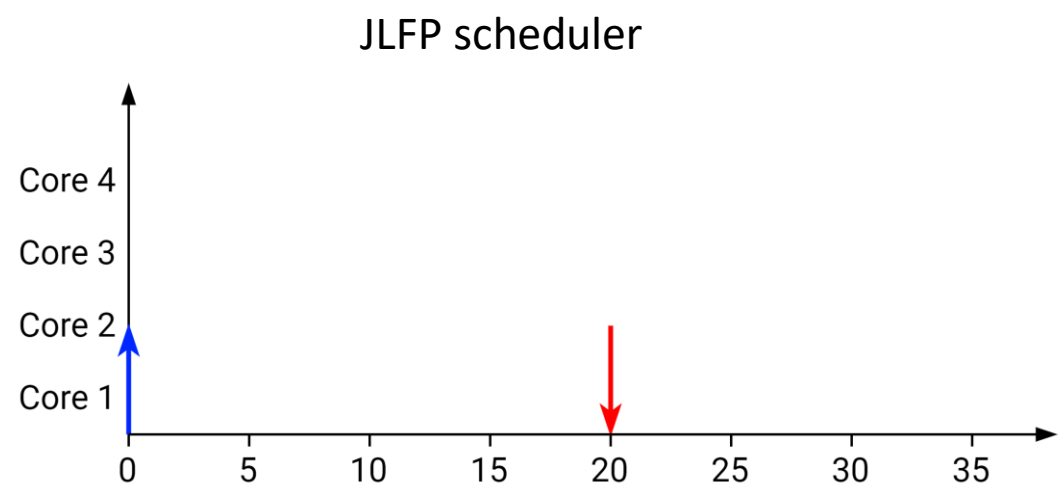


Agenda

- ~~Gang schedulability analysis~~
- New scheduling policy

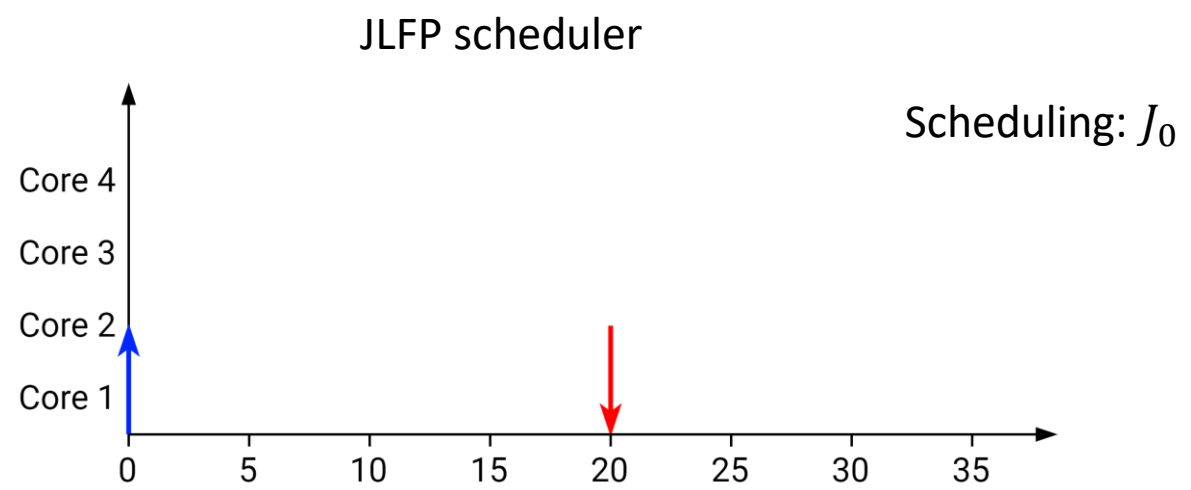
JLFP limitations with moldable gang

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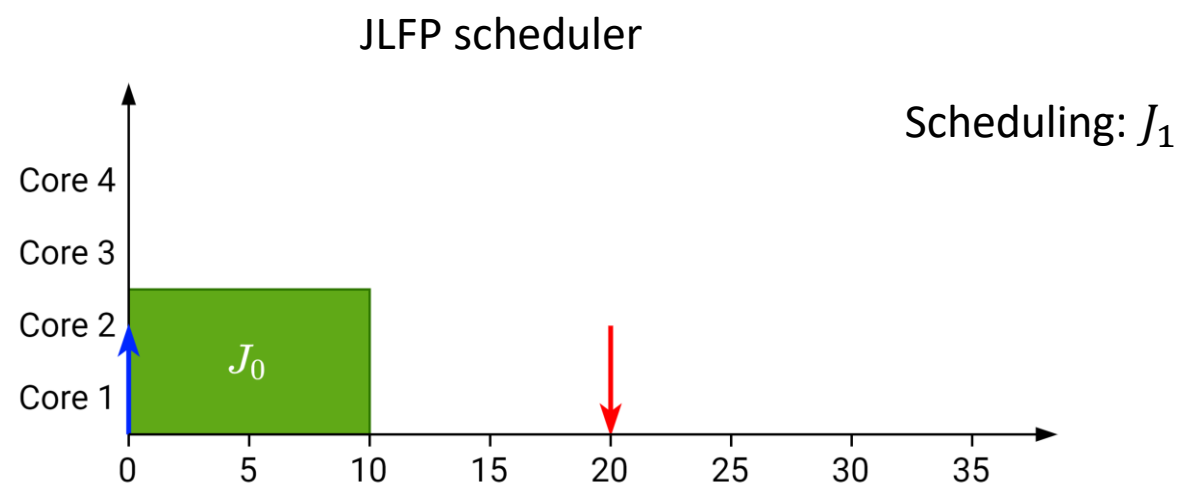
	Priority	Cores	Deadline	Execution time
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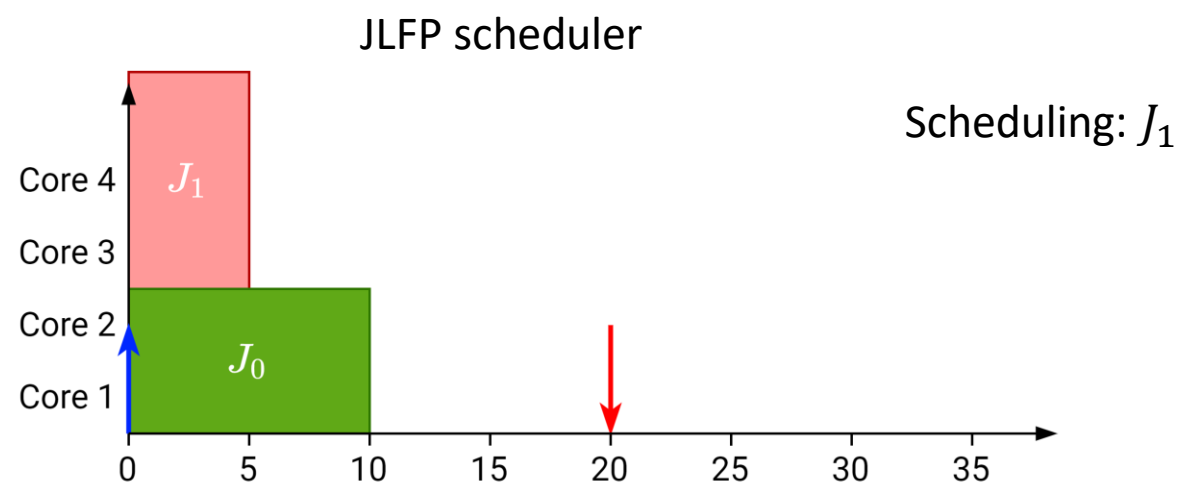
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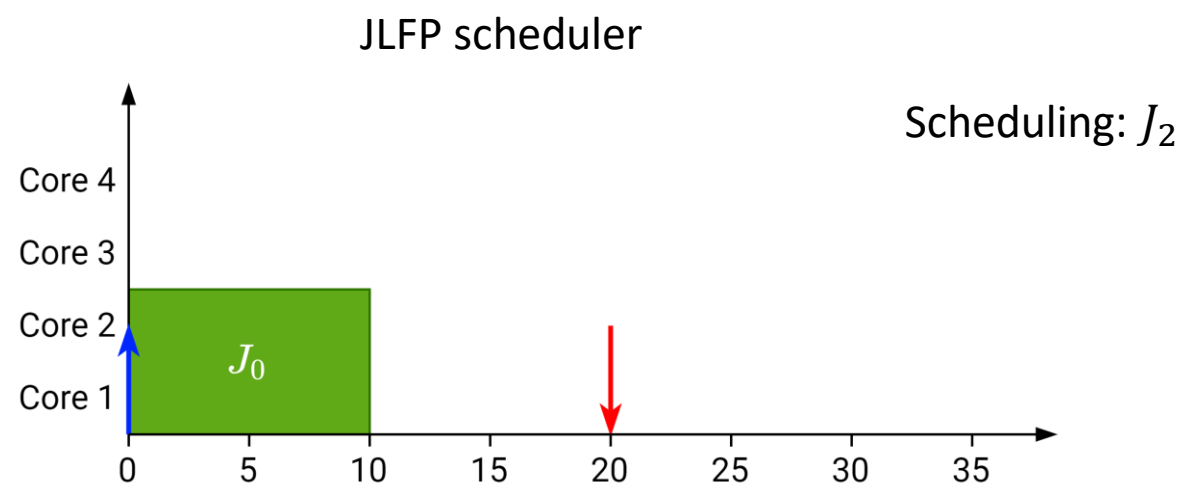
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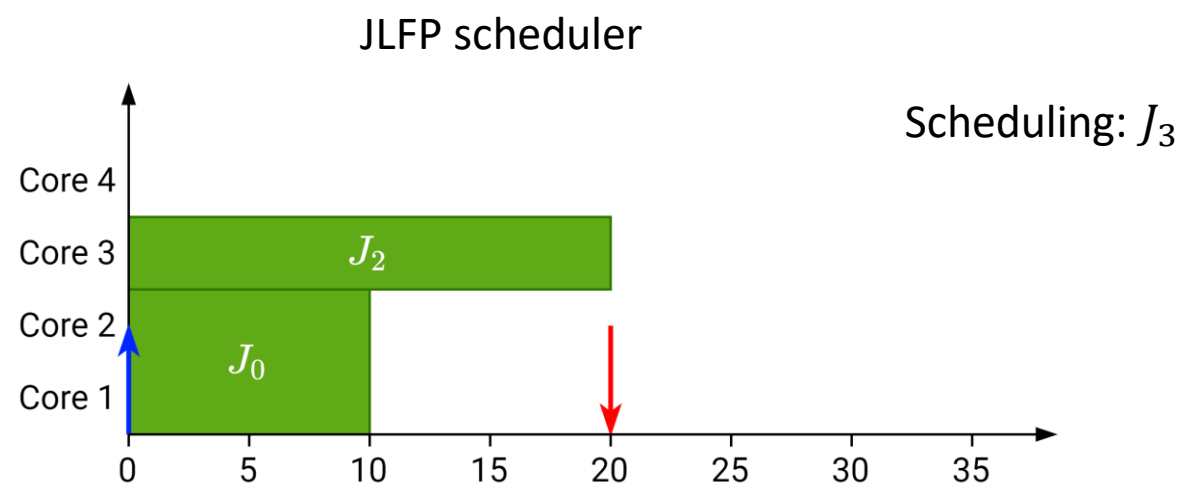
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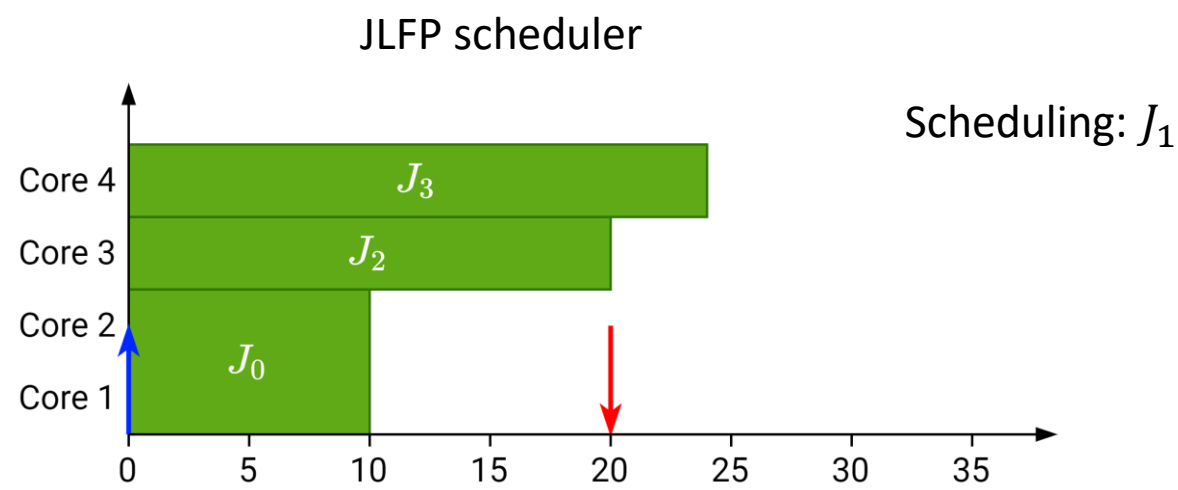
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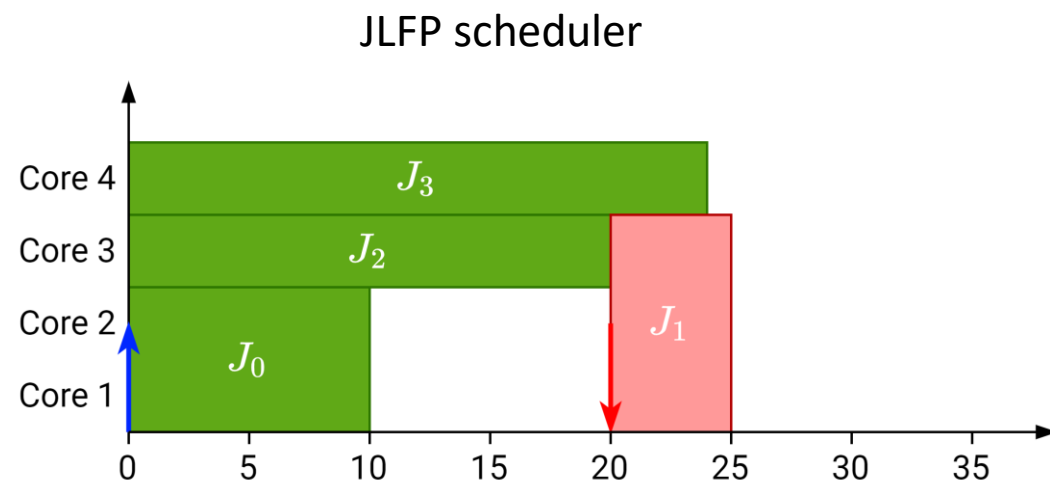
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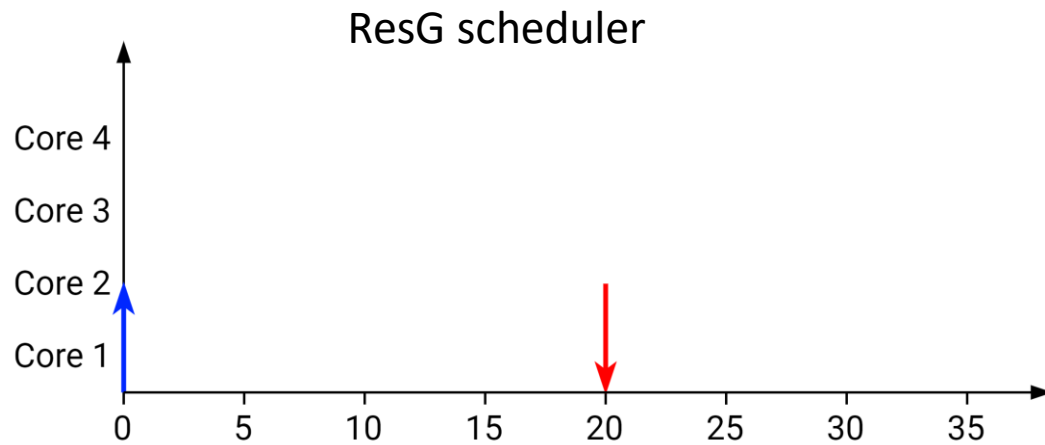
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Reservation-based gang scheduler

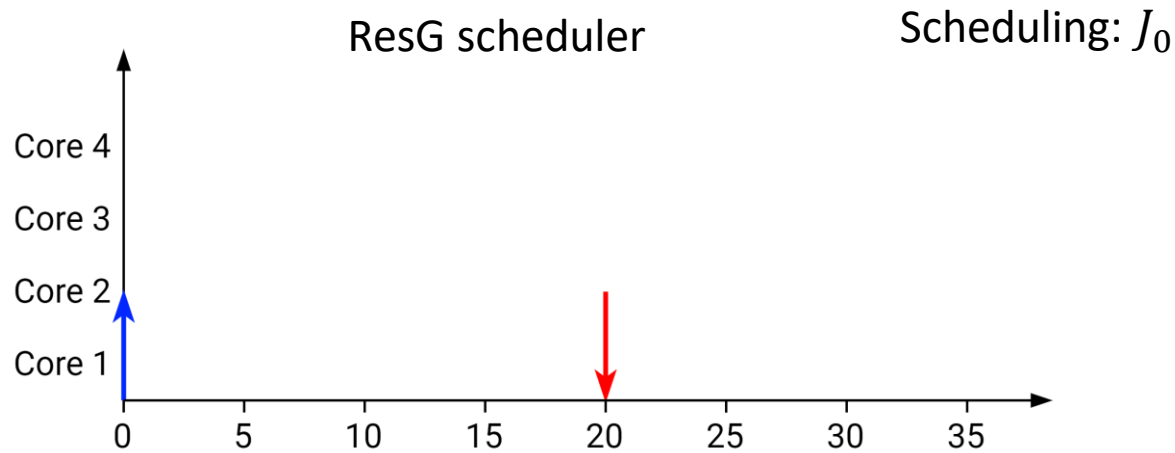
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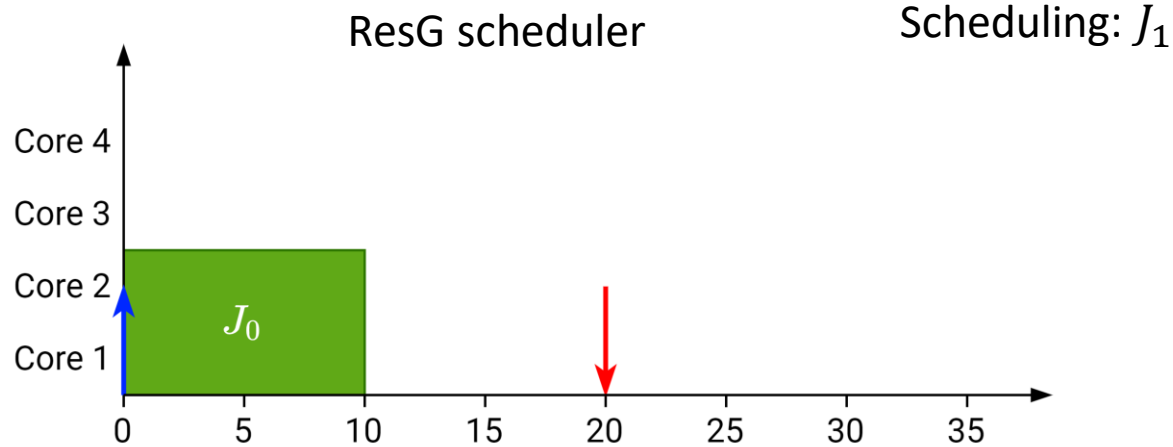
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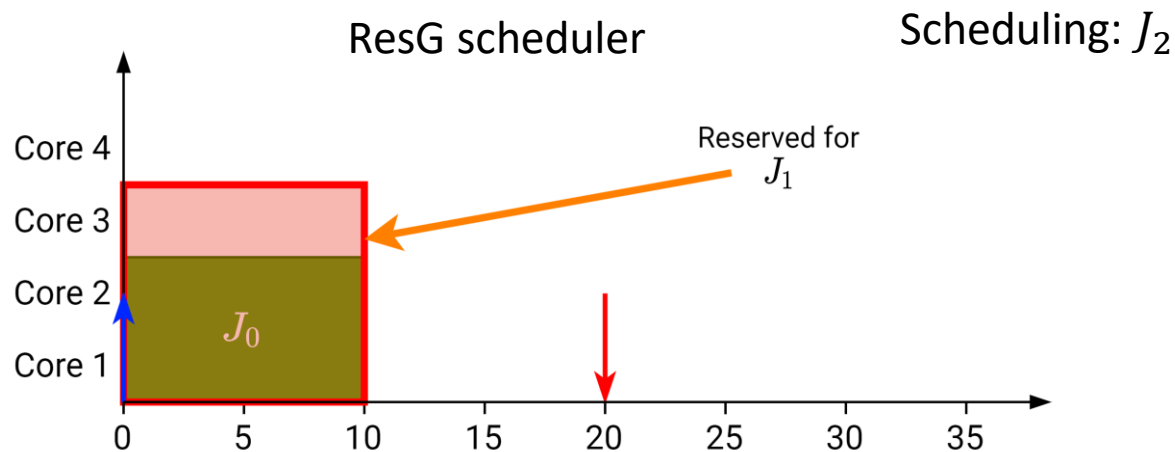
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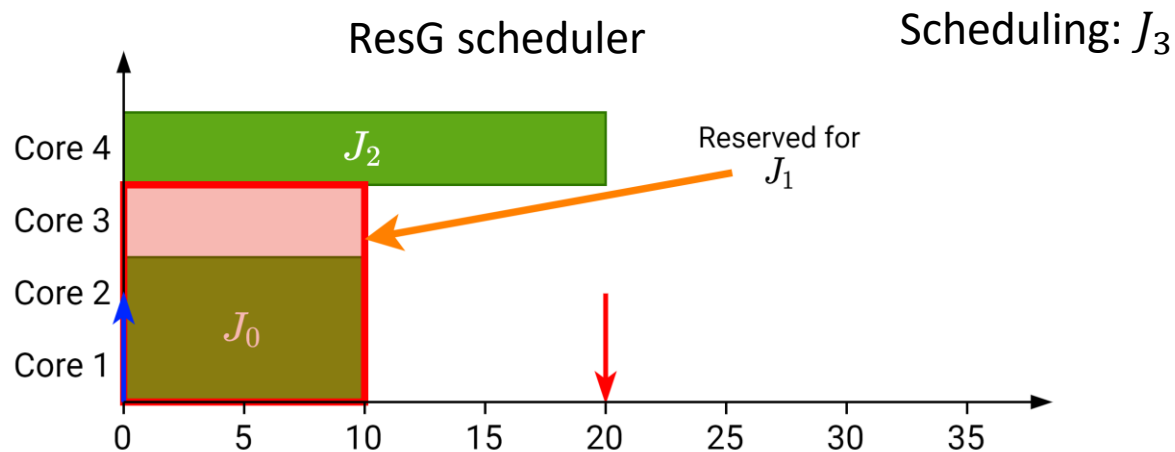
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- Reserve cores of higher-priority tasks and distribute the remaining ones among lower priority tasks



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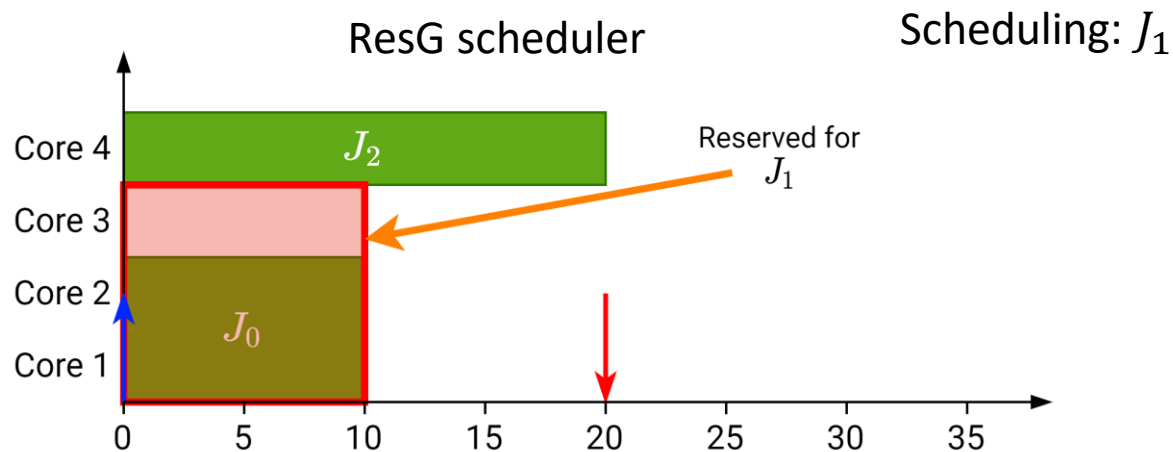
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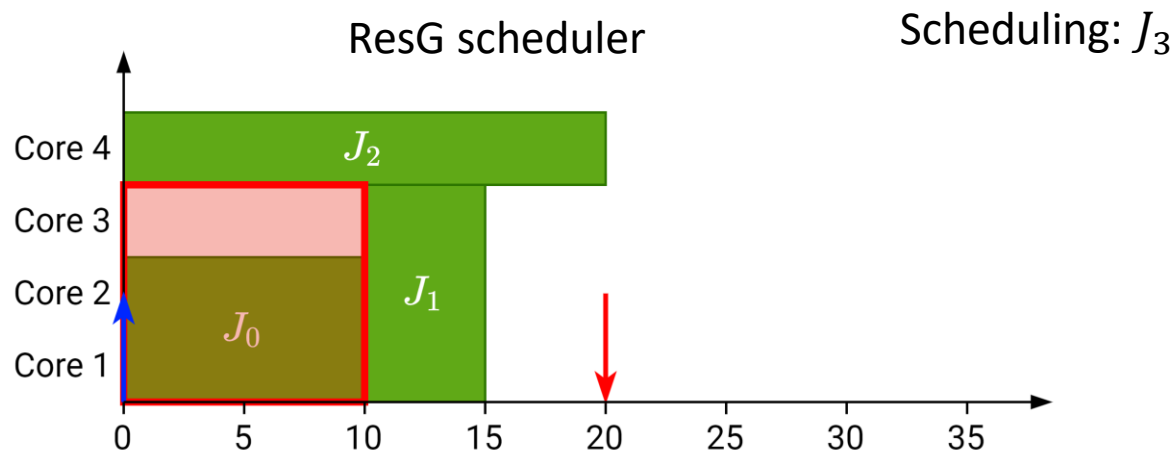
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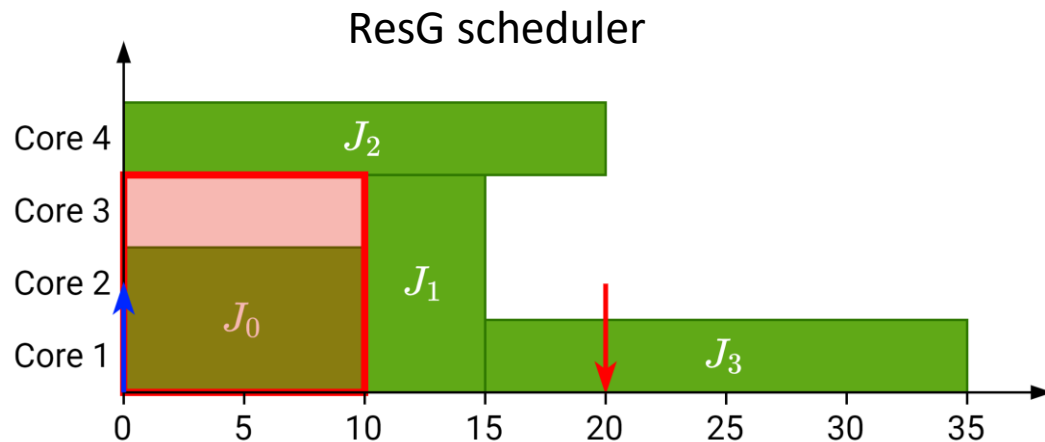
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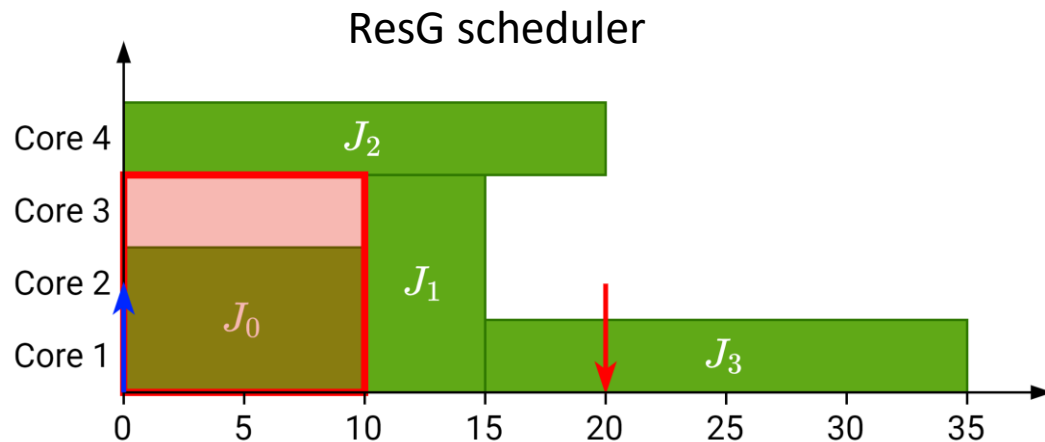
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Reservation-based gang scheduler

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- Reserve cores of higher-priority tasks and distribute the remaining ones among lower priority tasks
- Non-work conserving scheduler



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Results JLFP vs ResG in simulator

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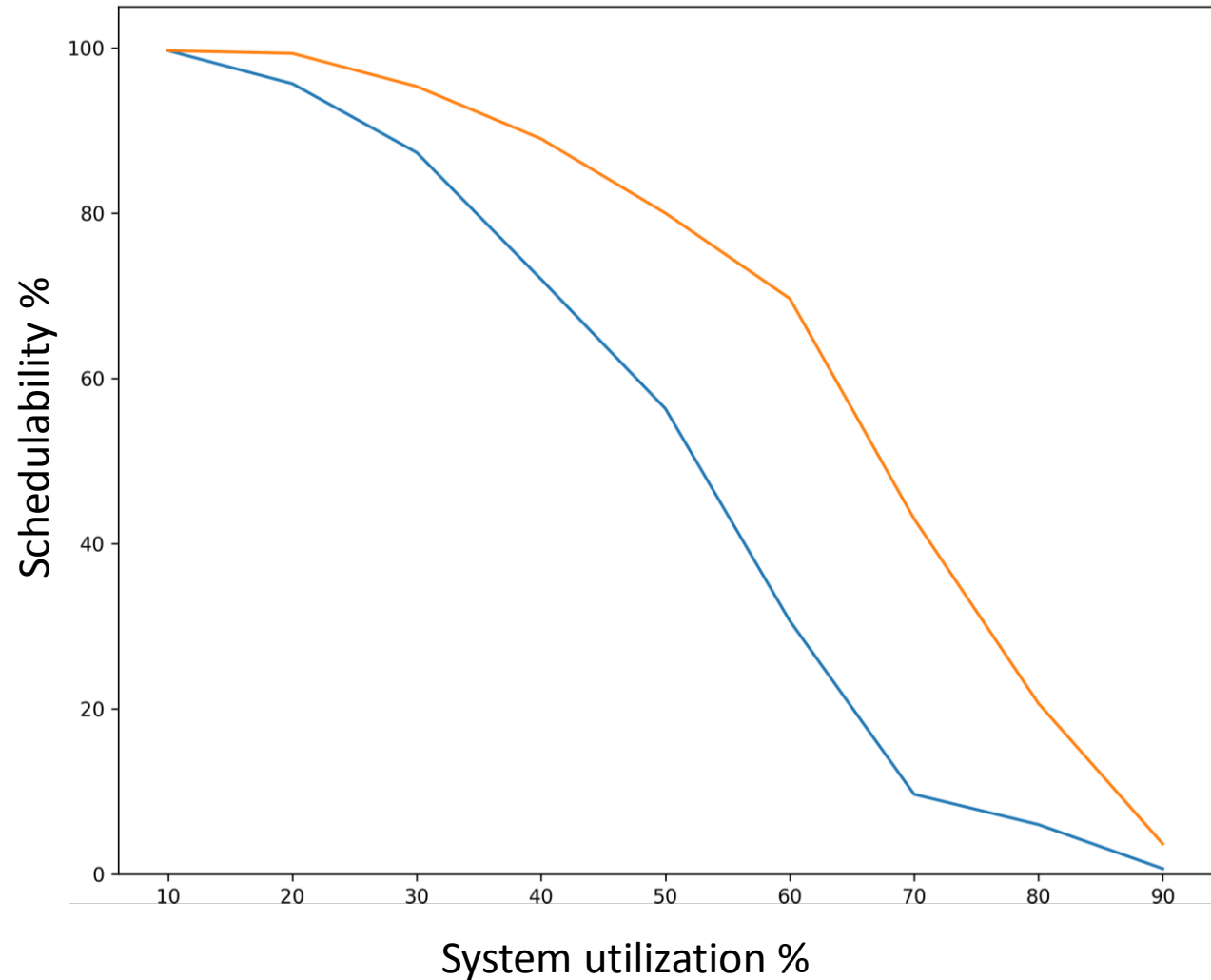
- Evaluated in simulator

Results JLFP vs ResG in simulator

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- Randomly generated task sets

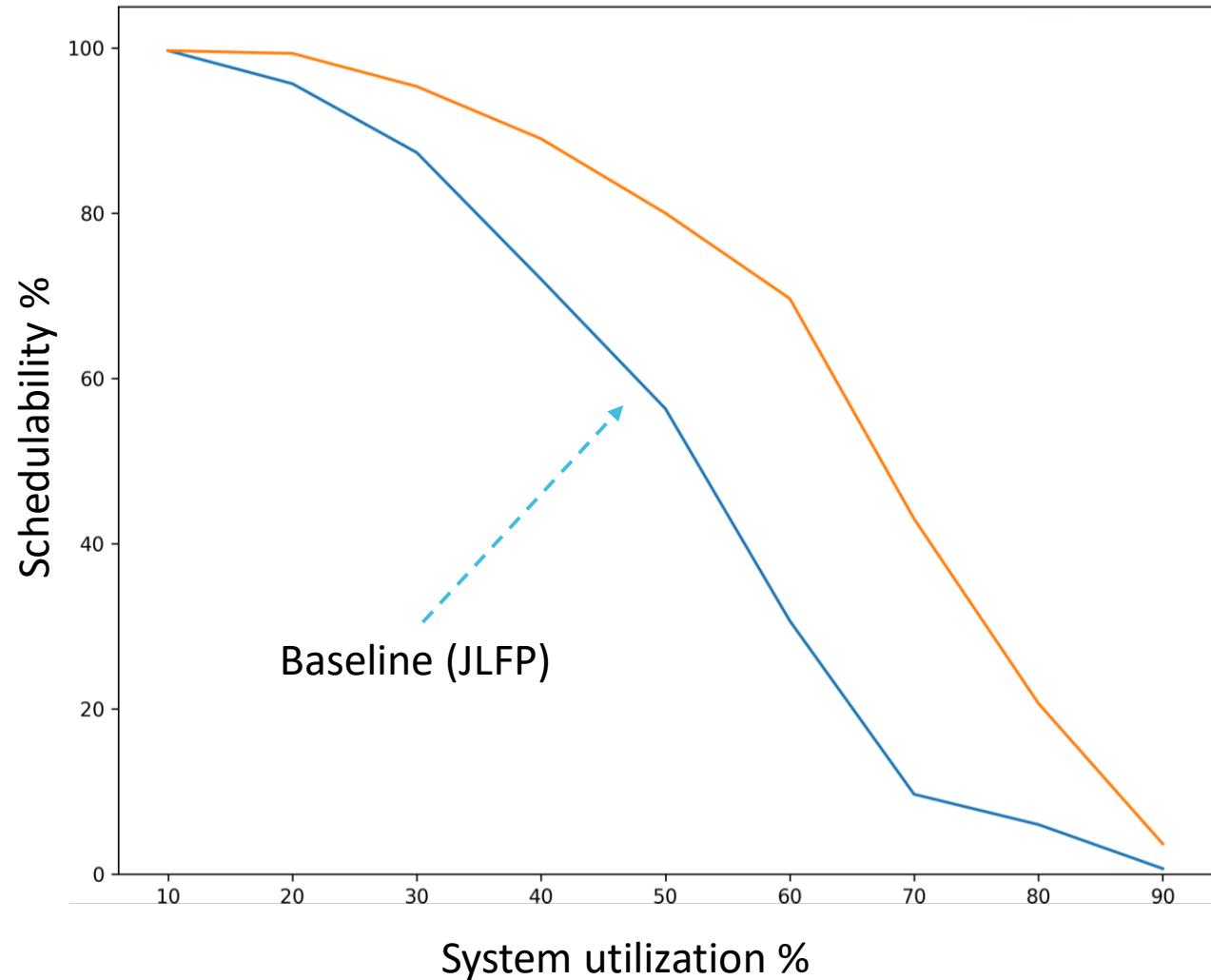
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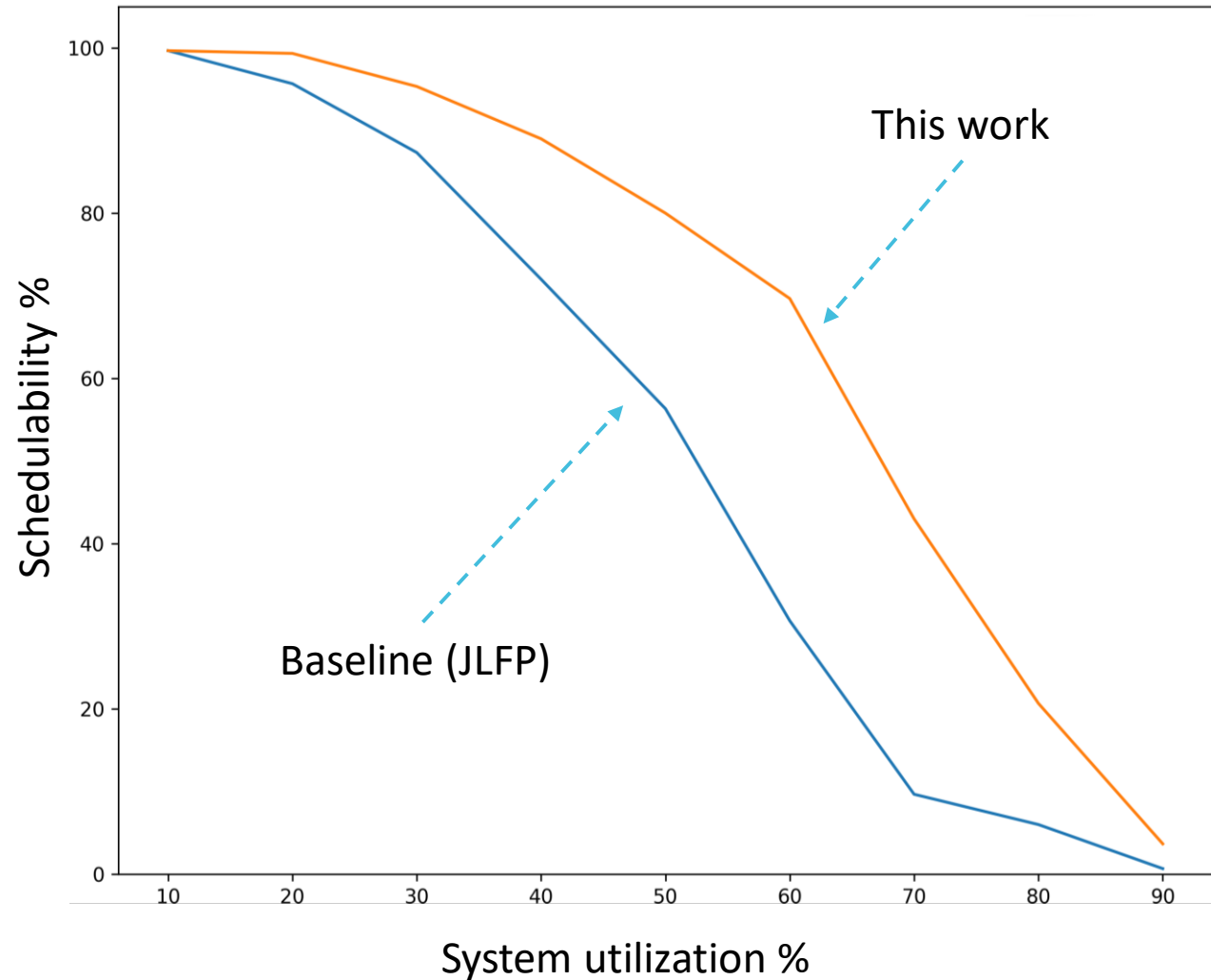
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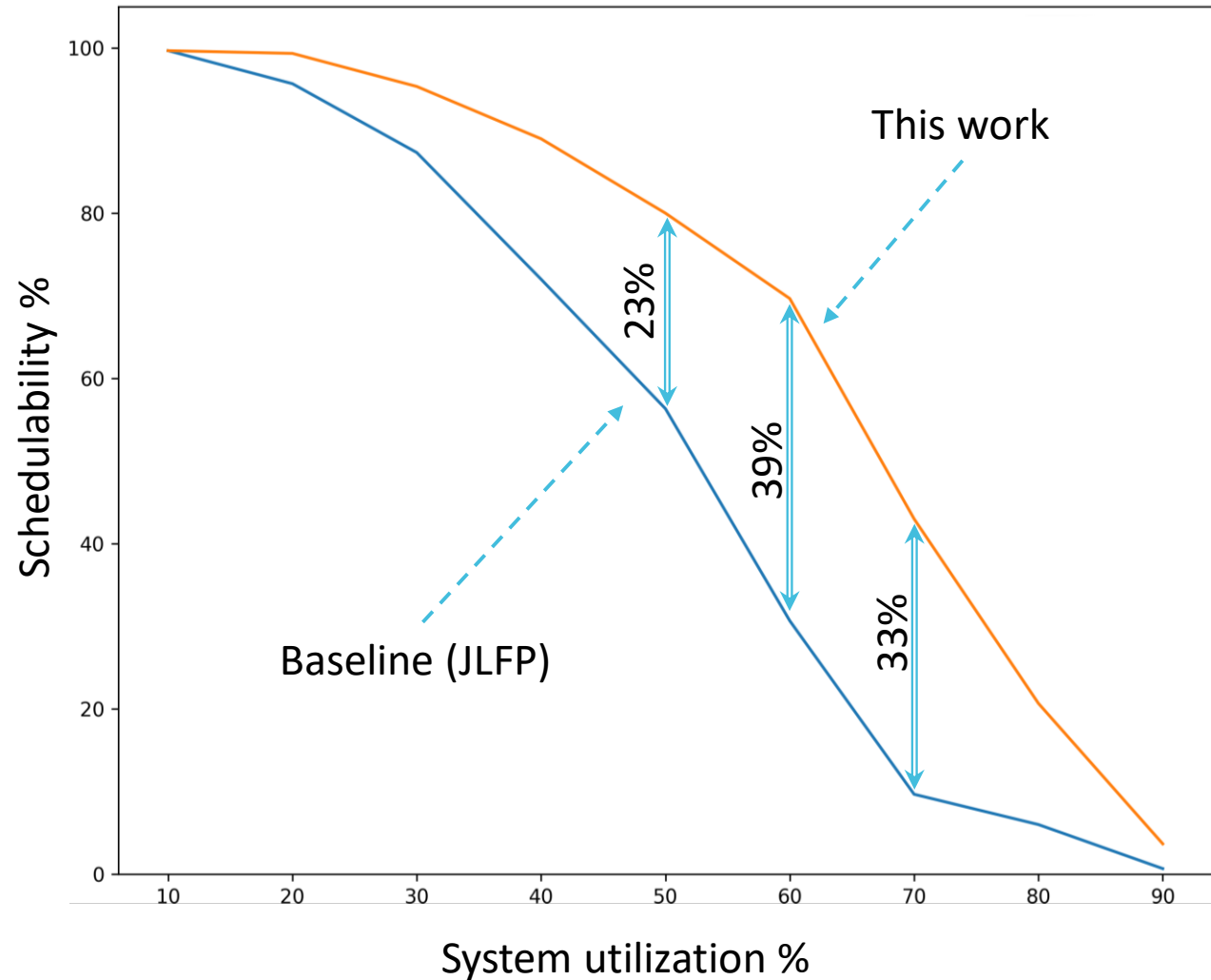
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Conclusions

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- With a better scheduling policy one can improve the schedulability of moldable gang tasks

Summary

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- A new analysis for gang tasks using SAG has been defined

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- A new analysis for gang tasks using SAG has been defined
- A new scheduling policy that uses gang moldable properties has been created

Next steps

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- Further reduce sources of pessimism

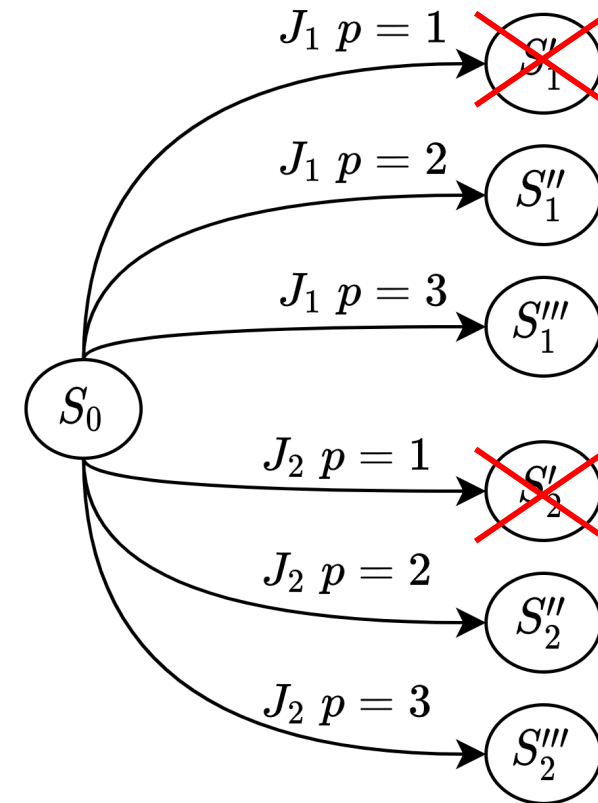
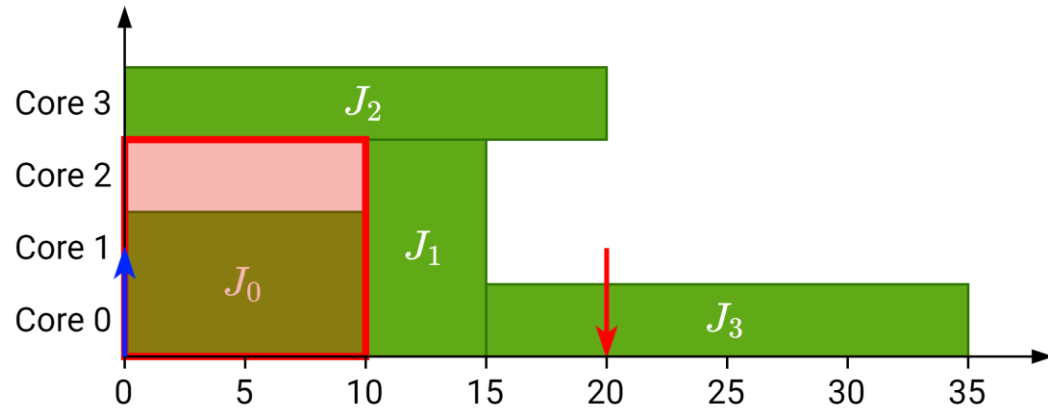
Next steps

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- Provide analysis for ResG scheduler and respective proofs
- Thorough evaluation of results using SURFSara cluster

Questions?



SAG analysis changes for gang

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$$LST_i = \min\{t_{wc}, t_{high} - 1\}$$

$$t_{wc} = \max\{A_1^{\max}, \min\{R_x^{\max} \mid J_x \in \mathcal{R}^p\}\}$$

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$$t_{wc} = \min_{J_j \in \mathcal{R}^v} \left\{ \max\{R_j^{\max}, A_{m_j^{\min}}^{\max}\} \right\}$$

$$t_{high} = \min_{J_j \in \{hp_i \cap \mathcal{R}^v\}} \left\{ th_x(J_i, J_j), \max\{LFT_y^* \mid J_y \in pred(J_j) \setminus pred(J_i)\} \right\}$$

$$th(J_i, J_j) = \begin{cases} r_j^{\max} & \text{if } m_j^{\min} \leq p \\ \max\{r_j^{\max}, A_{m_j^{\min}}^{\max}\} & \text{otherwise} \end{cases}$$

$$t_{gang} = \begin{cases} A_p^{\min} & \text{if } p = m_i^{\max} \\ A_p^{exact} & \text{otherwise} \end{cases} \quad t_{avail} = \begin{cases} A_{p+1}^{\max} - 1 & \text{if } p < m_i^{\max} \\ +\infty & \text{otherwise} \end{cases}$$

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Check if execution with p cores is possible

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p cores available

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$$EST_i = \max\{R_i^{\min}, A_1^{\min}\}$$

$$LST_i = \min\{t_{wc}, t_{high} - 1\}$$

$$t_{wc} = \max\{A_1^{\max}, \min\{R_x^{\max} \mid J_x \in \mathcal{R}^p\}\}$$

$$t_{high} = \min\{th_x(J_i) \mid J_x \in \mathcal{R}^p \wedge p_x < p_i\}$$

$$th_x(J_i) = \max\{r_x^{\max}, \max\{LFT_y^* \mid J_y \in pred(J_x) \setminus pred(J_i)\}\}$$

$$EST_i^p = \max\{R_i^{\min}, t_{gang}\}$$

$$LST_i^p = \min\{t_{avail}, t_{wc}, t_{high} - 1\}$$

$$t_{wc} = \min_{J_j \in \mathcal{R}^p} \left\{ \max\{R_j^{\max}, A_{m_j^{\min}}^{\max}\} \right\}$$

$$t_{high} = \min_{J_j \in \{hp_i \cap \mathcal{R}^p\}} \left\{ th_x(J_i, J_j), \max\{LFT_y^* \mid J_y \in pred(J_j) \setminus pred(J_i)\} \right\}$$

$$th(J_i, J_j) = \begin{cases} r_j^{\max} & \text{if } m_j^{\min} \leq p \\ \max\{r_j^{\max}, A_{m_j^{\min}}^{\max}\} & \text{otherwise} \end{cases}$$

$$t_{gang} = \begin{cases} A_p^{\min} & \text{if } p = m_i^{\max} \\ A_p^{\text{exact}} & \text{otherwise} \end{cases}$$

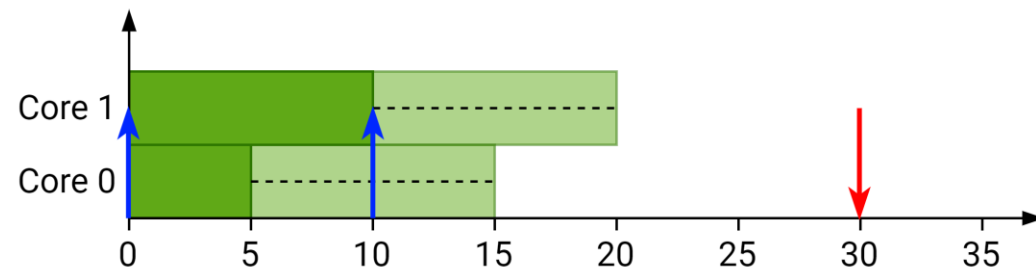
$$t_{avail} = \begin{cases} A_{p+1}^{\max} - 1 & \text{if } p < m_i^{\max} \\ +\infty & \text{otherwise} \end{cases}$$

$p + 1$ cores **not** available

How does SAG work?

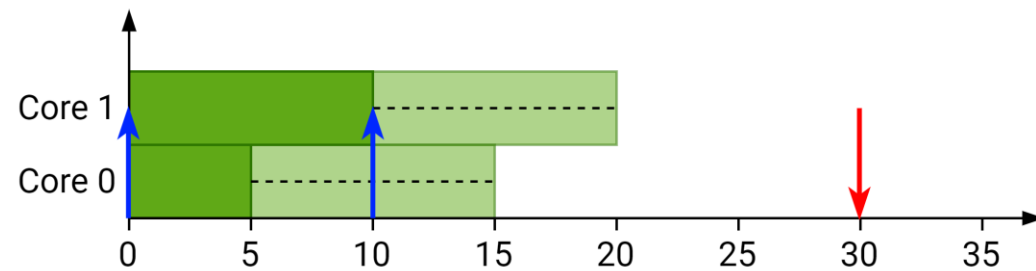
How does SAG work?

J_i	C_i^{min}	C_i^{max}	r_i	d_i	P_i
J_1	10	15	10	30	1
J_2	5	5	0	100	2



How does SAG work?

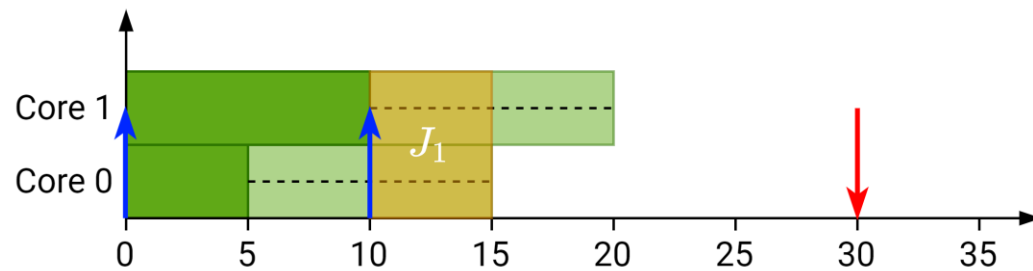
J_i	C_i^{min}	C_i^{max}	r_i	d_i	P_i
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J_2	5	5	0	100	2



[5, 15]
[10, 20]

How does SAG work?

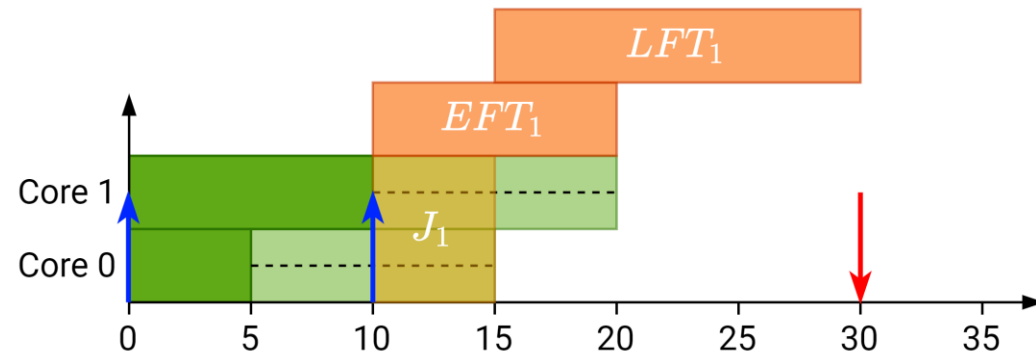
J_i	C_i^{min}	C_i^{max}	r_i	d_i	P_i
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[5, 15]
[10, 20]

How does SAG work?

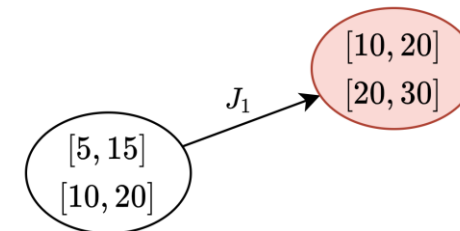
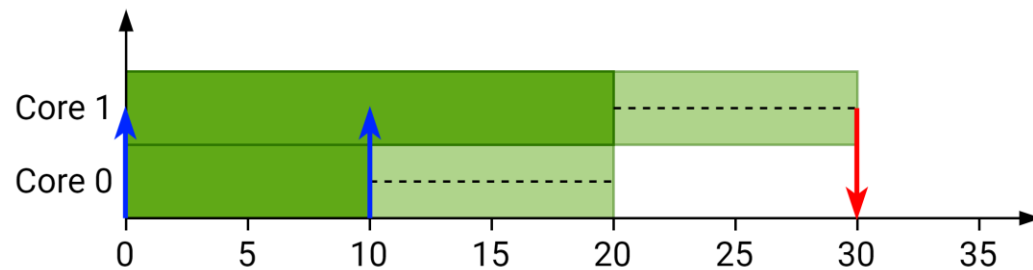
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[5, 15]
[10, 20]

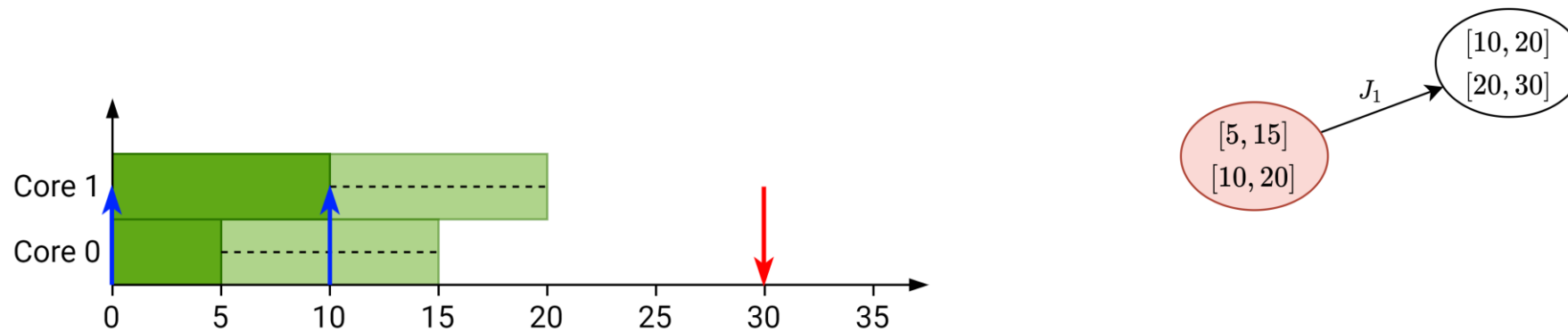
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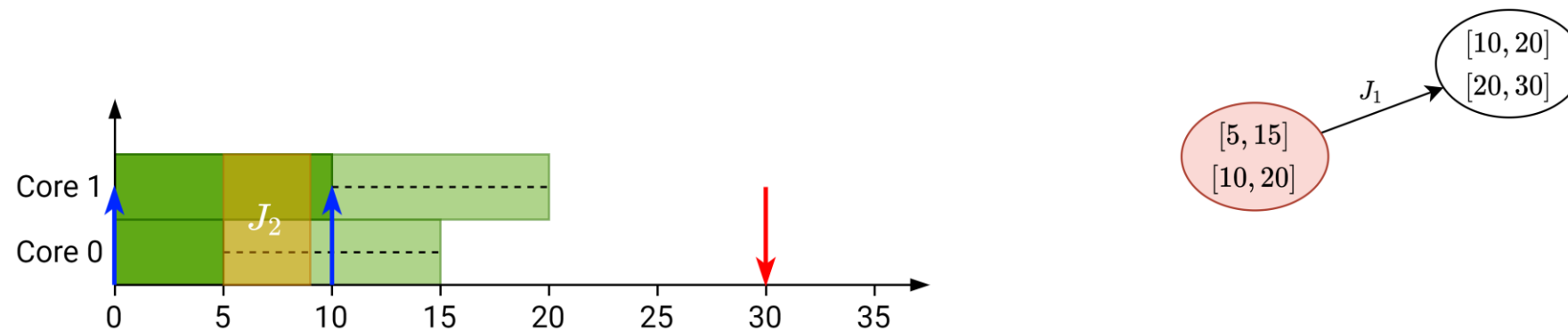
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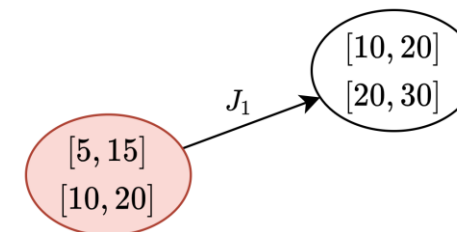
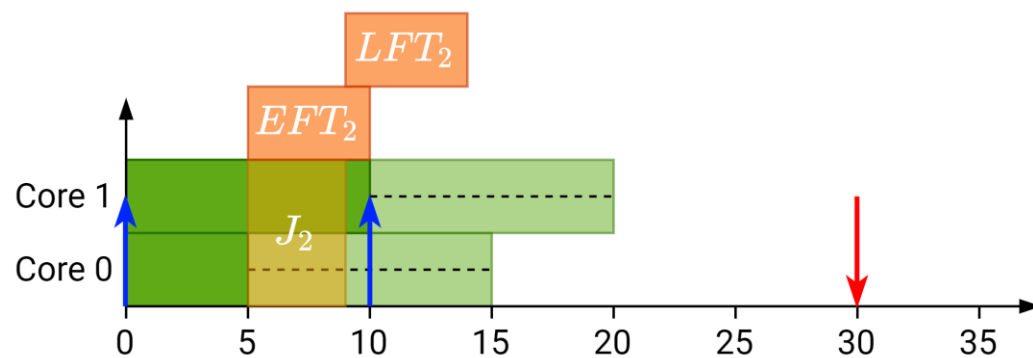
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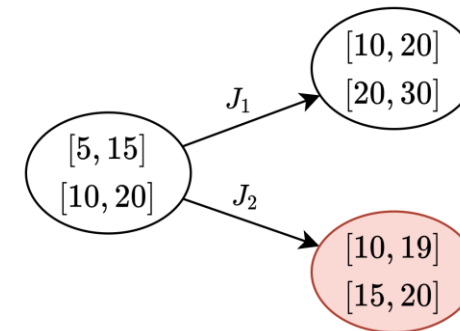
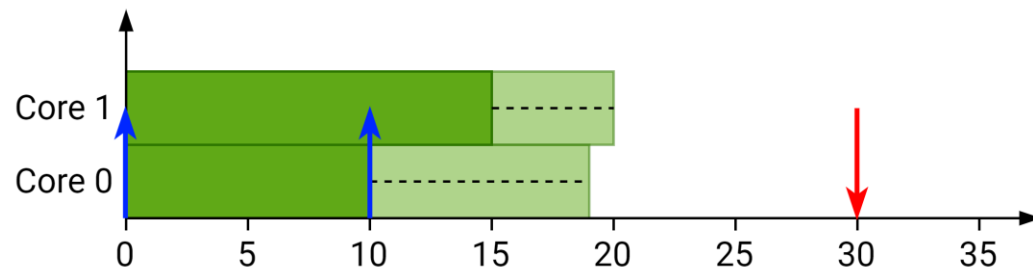
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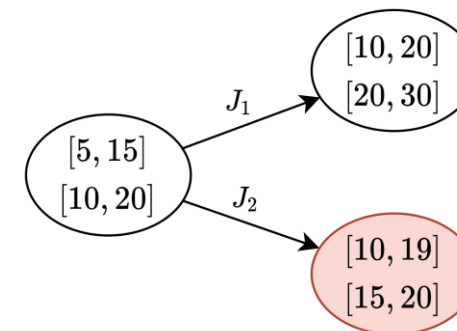
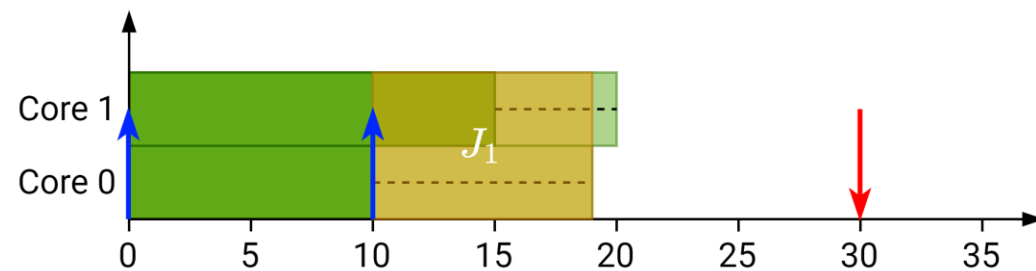
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