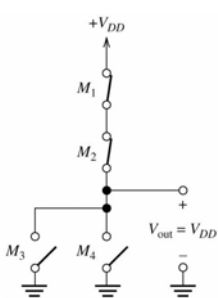
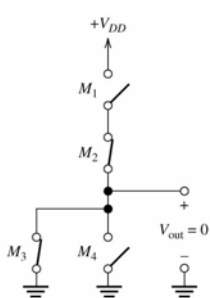


Figure 12.33 Two-input CMOS NOR gate.



(a)  $A$  and  $B$  both low



(b)  $A$  high and  $B$  low