

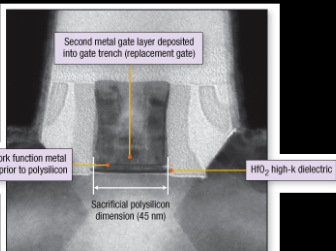
Tensión (NMOS)



Compresión (PMOS)



Intel Penryn PMOS transistor structure



Source: Semiconductor Insights

