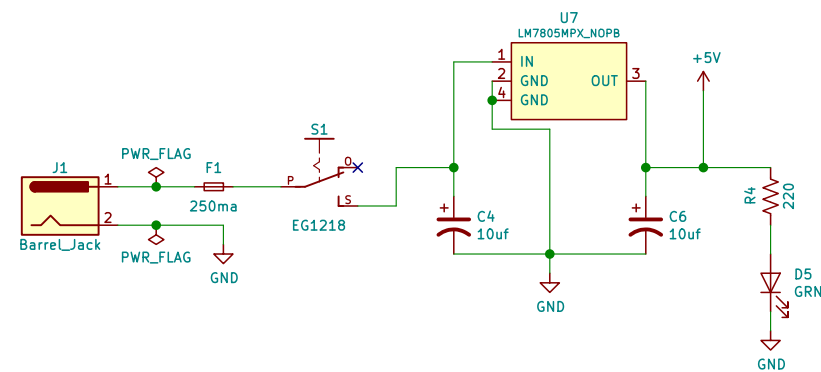
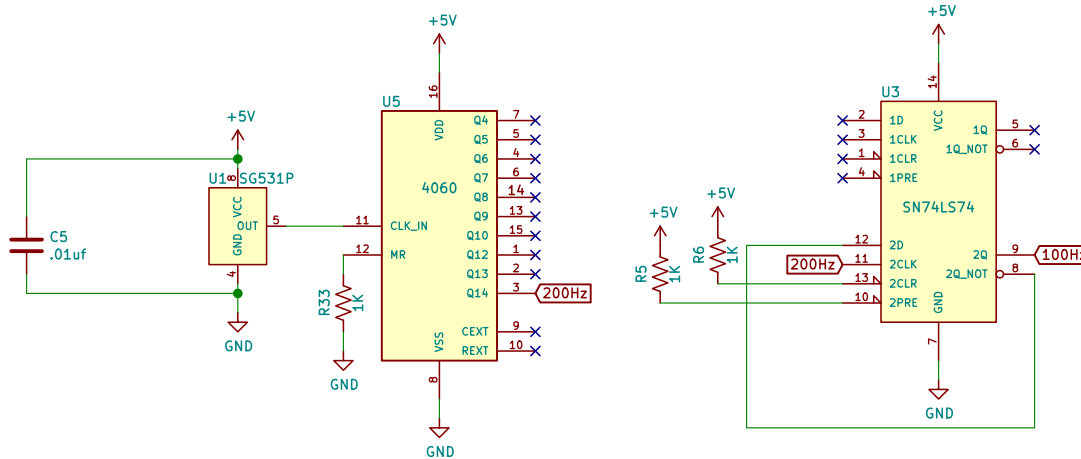


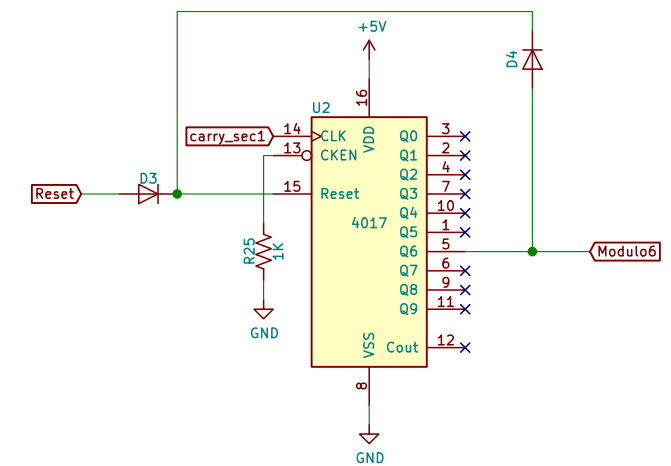
## Power



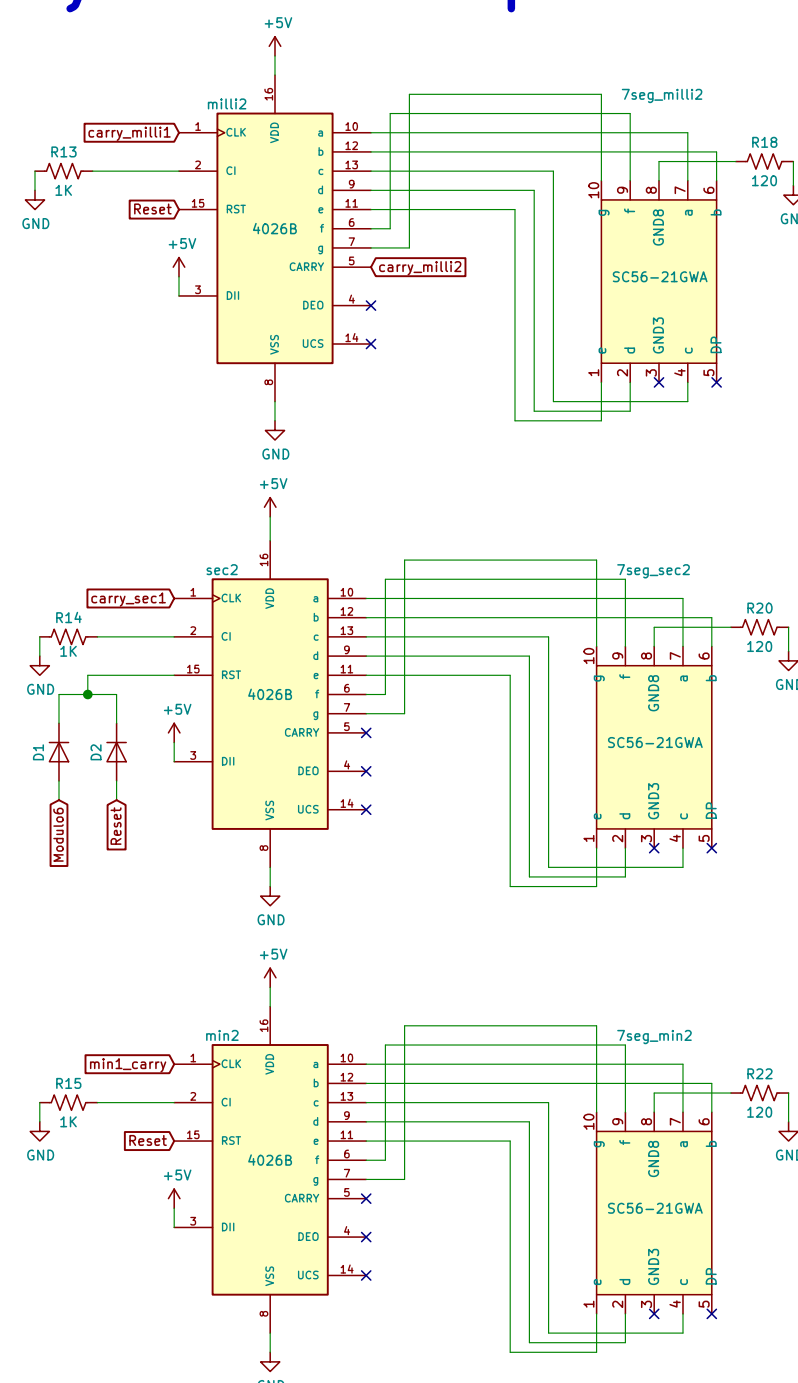
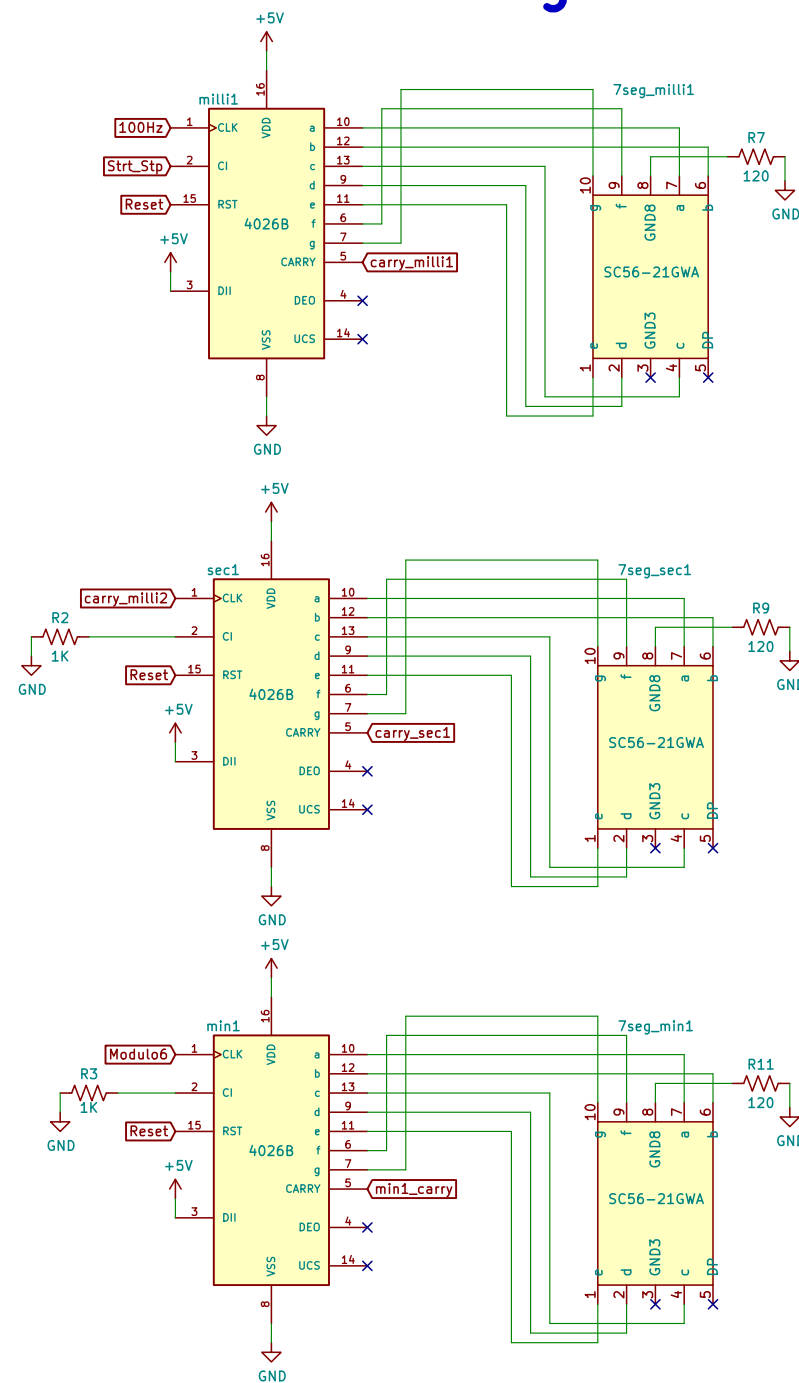
## 3.2768 Mhz CLK & frequency divider



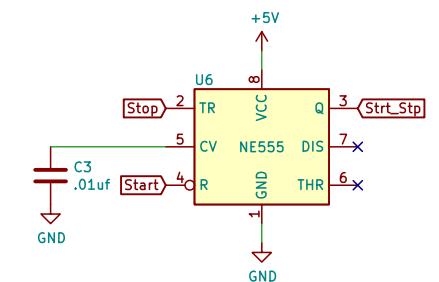
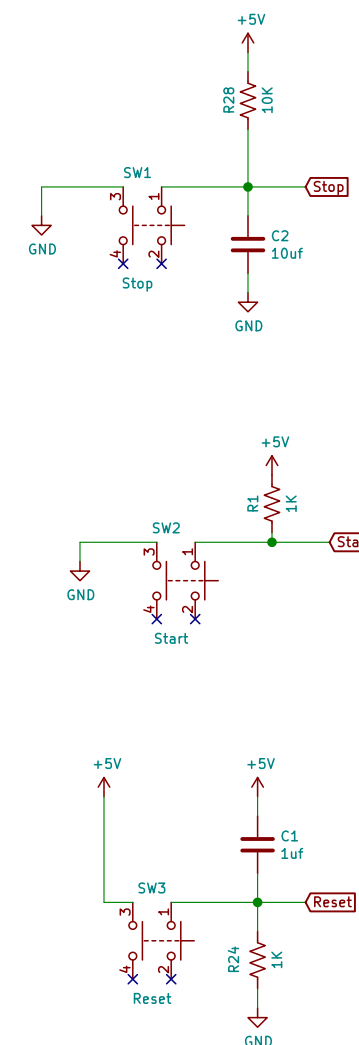
## Sixty-second reset



## 7-Segment displays & driver chips



## Start, Stop & Reset buttons



Author: James Morar

Sheet: /  
File: counter.sch

**Title: counter**

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