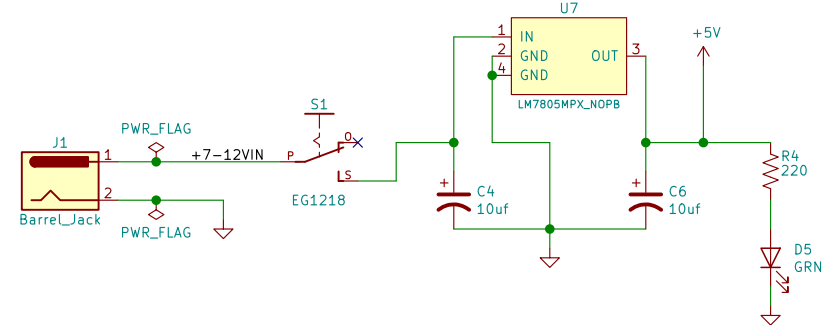
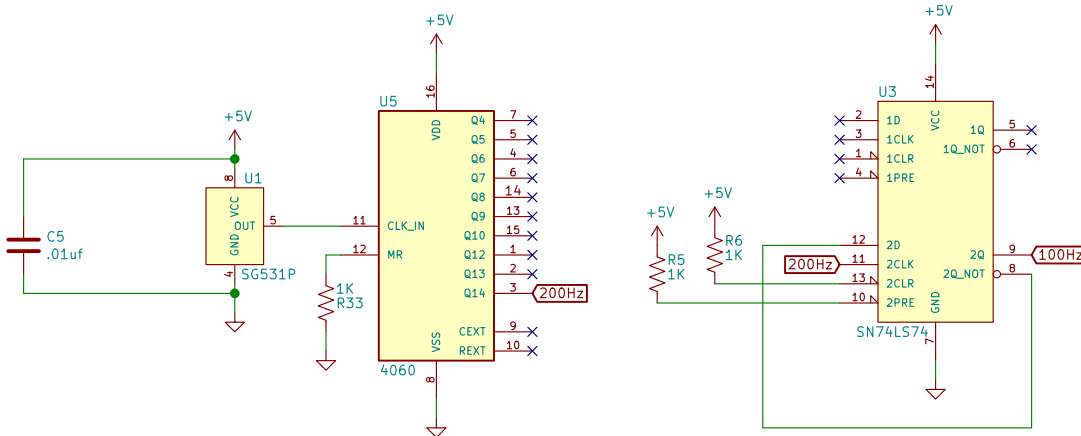


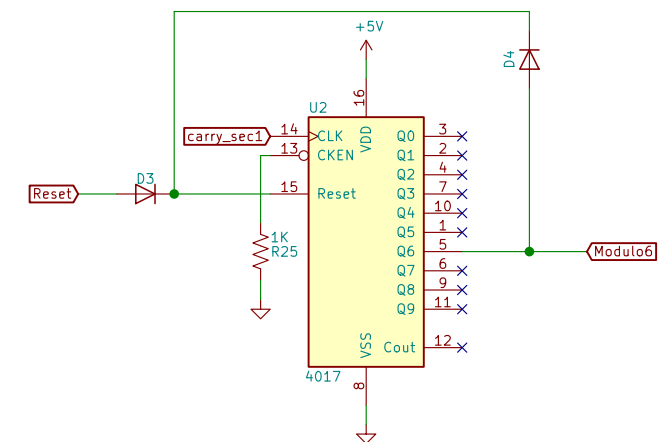
Power



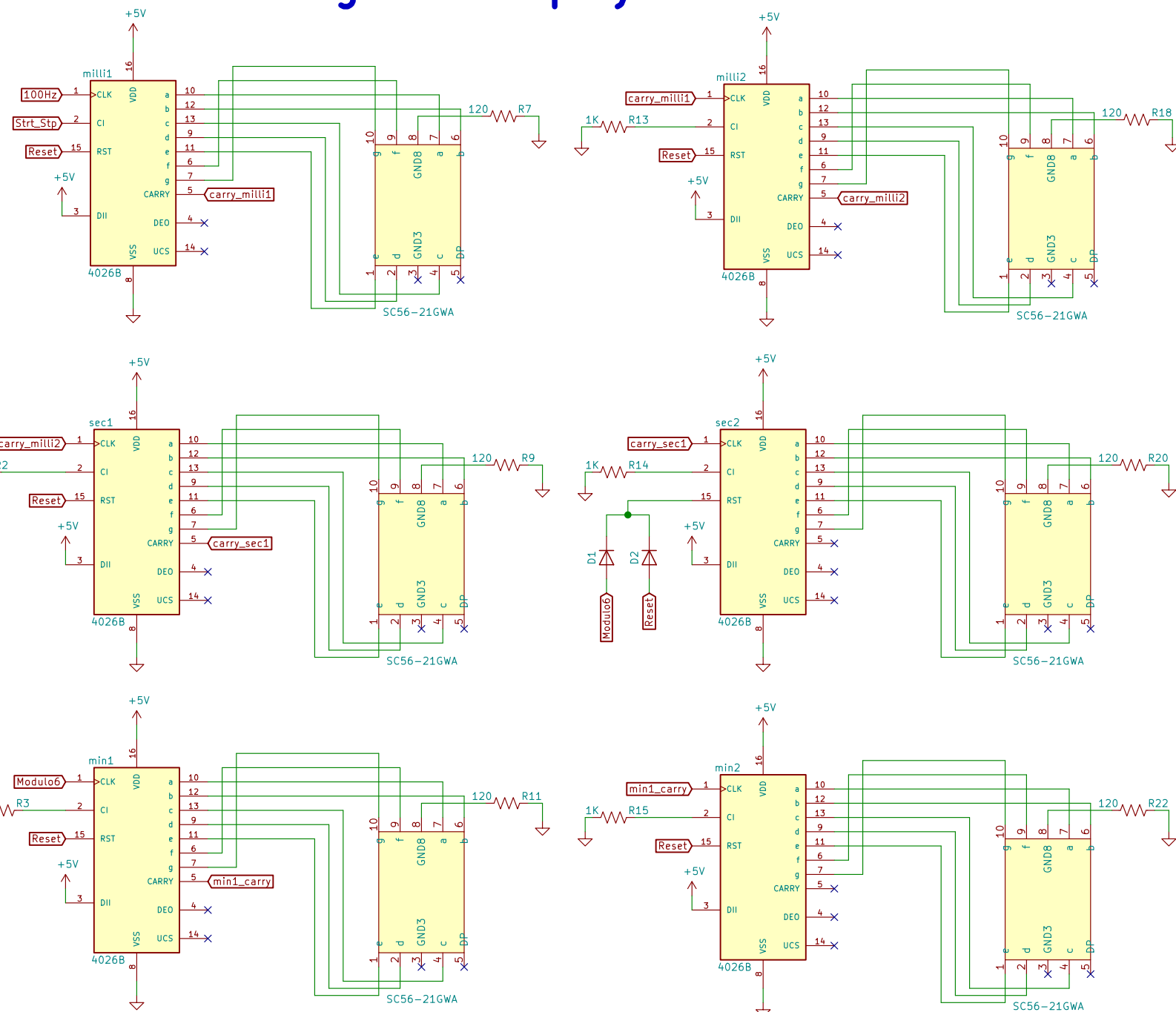
3.2768 Mhz CLK & Frequency Divider



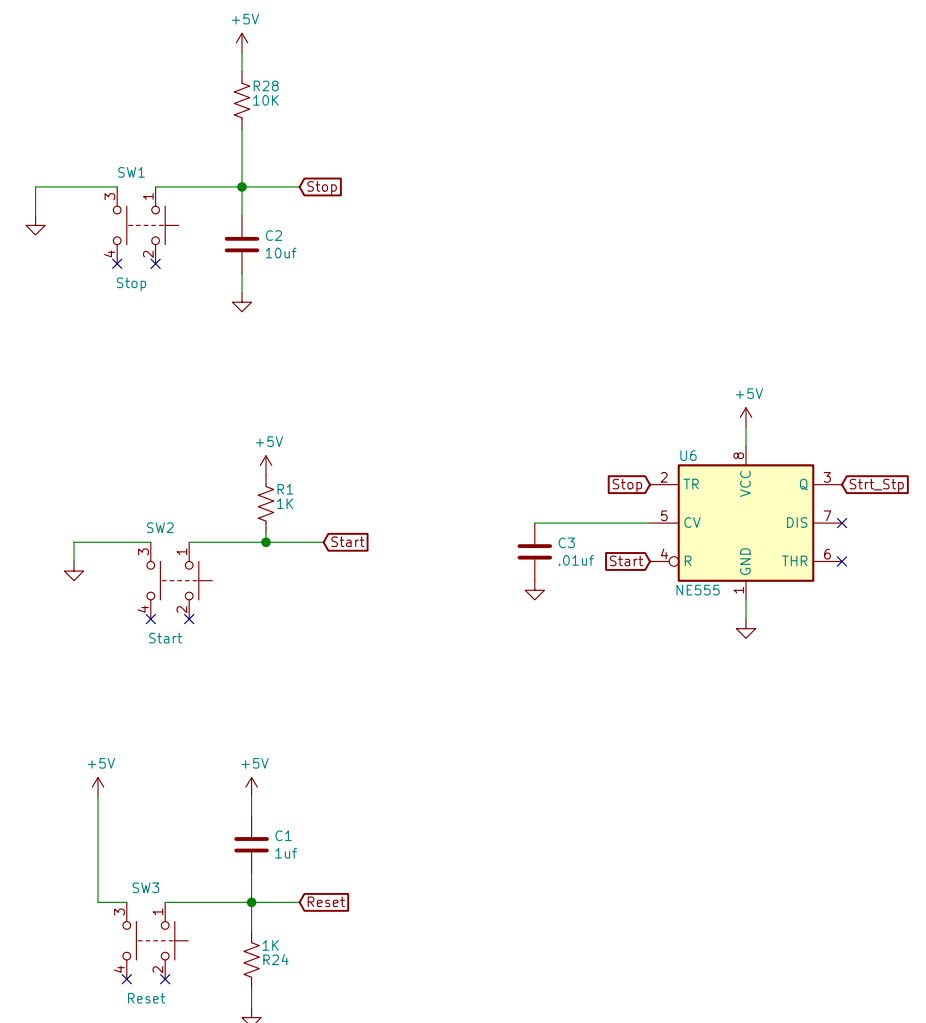
Sixty-Second Reset



7-Segment Displays & Drivers



Start, Stop & Reset



Author: James Morar

Sheet: /
File: counter.sch

Title: COUNTER_V01

Size: A3 Date: 2020-03-04
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