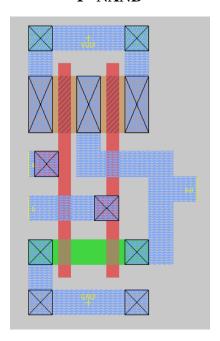
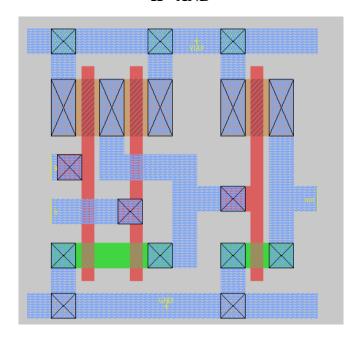
Lab 1

I - NAND



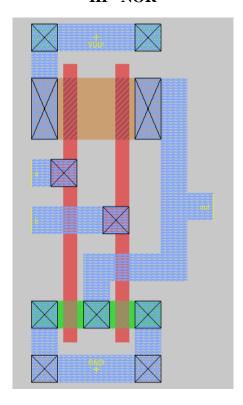


II - AND



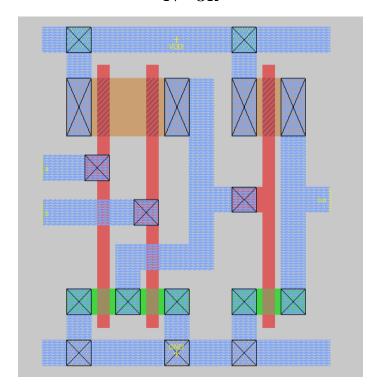


III - NOR



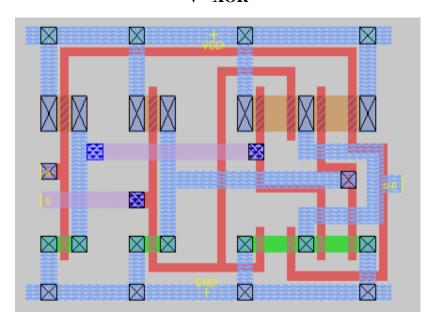


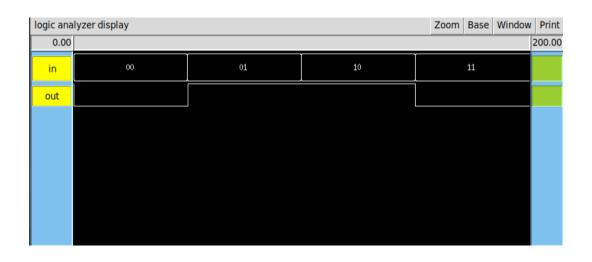
IV - OR



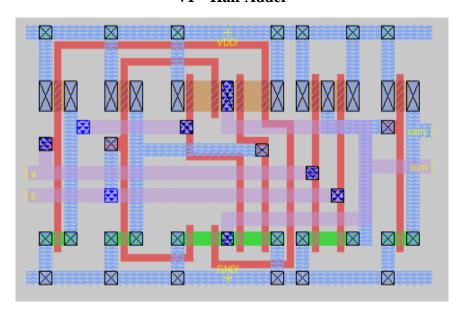


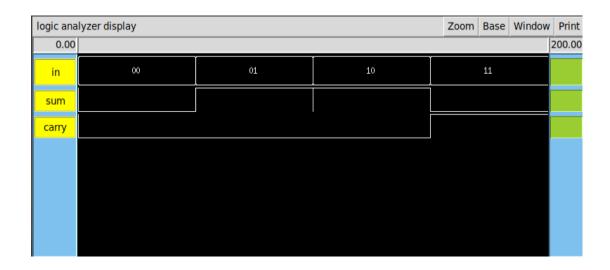
V - XOR



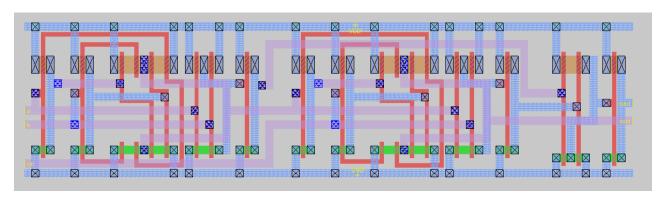


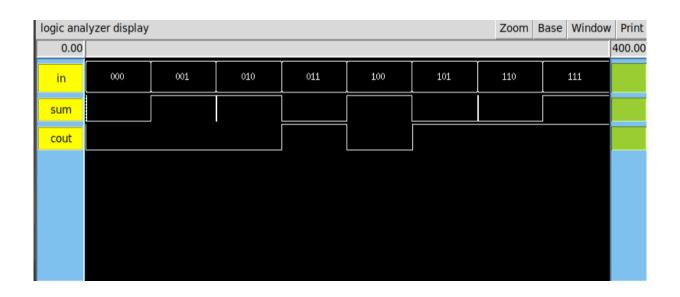
VI – Half Adder





VII – Full Adder





VII – Data Flip Flop

