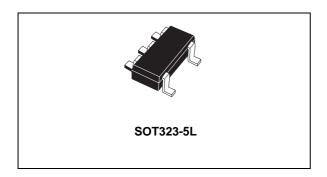


150 mA low noise and high PSRR linear voltage regulator

Datasheet - production data



Features

- Input voltage from 2.4 to 5.5 V
- Very low quiescent current (31 μA typ. at no load, 75 μA typ. at 150 mA load, 1 μA max. in OFF mode)
- Very low noise (20 μV_{RMS} at V_{OUT} = 1.5 V)
- Output voltage tolerance: ± 1.8% at 25 °C
- 150 mA guaranteed output current
- Wide range of output voltages available on request: 0.8 V to 3.3 V in 100 mV steps
- Logic-controlled electronic shutdown
- · Compatible with ceramic capacitors
- Very high PSRR (80 dB @ 100 Hz, 76 @ 10 kHz, 54 @ 100 kHz)
- Internal current and thermal limit

Package: SOT323-5L

Temperature range: -40 °C to 125 °C

Description

The LD59015 provides 150 mA maximum current with an input voltage range from 2.4 V to 5.5 V, and a typical dropout voltage of 150 mV. It is stable with ceramic capacitors. High PSRR, low quiescent current and low noise features make it suitable for low power battery-powered applications. Power supply rejection is 80 dB at low frequency and starts to roll off at 10 kHz. The enable logic control function puts the LD59015 in shutdown mode, allowing a total current consumption lower than 1 µA. The device also includes short-circuit constant current limiting and thermal protection. Typical applications are mobile phones, personal digital assistants (PDAs), cordless phones and similar batterypowered systems.

Table 1. Device summary

Order codes	Output voltages
LD59015C08R	0.8 V
LD59015C12R	1.2 V
LD59015C15R	1.5 V
LD59015C18R	1.8 V
LD59015C25R	2.5 V
LD59015C30R	3.0 V
LD59015C33R	3.3 V

Contents LD59015

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LD59015 Block diagram

1 Block diagram

BandGap
0.8 V

Trimming

Thermal
Protection

Enable

EN

GND

Figure 1. LD59015 block diagram

Pin configuration LD59015

2 Pin configuration

Figure 2. Pin connection (top view)

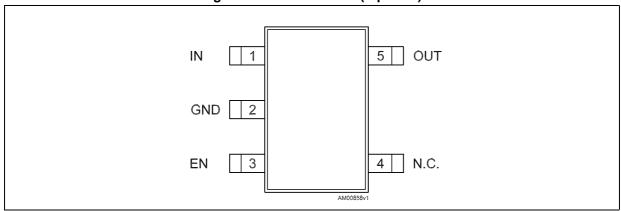


Table 2. Pin description

Pin n°	Symbol	Function
1	IN	Input voltage
2	GND	Ground
3	EN	Enable input Set $V_{EN} > 0.9$ to turn on the device Set $V_{EN} < 0.4$ to turn off the device
4	N.C.	Not connected
5	OUT	Output voltage

LD59015 Typical application

3 Typical application

V_{IN} IN OUT 1 μF Load

V_{EN} EN GND

Figure 3. Typical application circuit

Maximum ratings LD59015

4 Maximum ratings

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{IN}	DC input voltage	- 0.3 to 7	V
V _{OUT}	DC output voltage	- 0.3 to V _I + 0.3 (max. 7)	V
V _{EN}	Enable input voltage	- 0.3 to V _I + 0.3 (max. 7)	V
I _{OUT}	Output current	Internally limited	mA
P _D	Power dissipation	Internally limited	mW
ESD	Human body model	± 3	kV
E3D	Machine model	± 300	V
T _{STG}	Storage temperature range	-65 to 150	°C
T _{OP}	Operating junction temperature range	-40 to 125	°C

Note:

Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied. All values are referred to GND.

Table 4. Thermal data

Symbol	Parameter	Value	Unit	
R _{thJA}	Thermal resistance junction-ambient	645.69	°C/W	
R _{thJC}	R _{thJC} Thermal resistance junction-case		°C/W	

5 Electrical characteristics

 T_J = 25 °C, V_{IN} = $V_{OUT(NOM)}$ + 1 V, C_{IN} = C_{OUT} = 1 $\mu F,\,I_{OUT}$ = 1 mA, V_{EN} = $V_{IN},\,unless$ otherwise specified.

Table 5. Electrical characteristics (1)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{IN}	Operating input voltage		2.4		5.5	
	Turn-on threshold			2.0	2.15	V
V _{UVLO}	Turn-off threshold		1.90	1.95		
V _{OUT}	V _{OUT} accuracy	I _{OUT} = 1mA, -40°C < T _J < 125°C	-1.8		1.8	%
ΔV _{OUT}	Static line regulation	$V_{OUT} + 1V \le V_{IN} \le 5.5V$, $I_{OUT} = 1mA$		0.001		%/V
ΔV_{OUT}	Static load regulation	I _{OUT} = 1mA to 150mA		0.001		%/mA
V _{DROP}	Dropout voltage (2)	I _{OUT} = 150mA, V _{OUT} > 2.2V -40°C < T _J < 125°C		150	210	mV
e _N	Output noise voltage	10Hz to 100kHz, I _{OUT} = 10mA, V _{OUT} = 1.5V		20		μV _{RMS} /V _{OUT}
	Supply voltage rejection V _{OUT} = 1.5V	$V_{IN} = V_{OUTNOM} + 1V + /-V_{RIPPLE}$ $V_{RIPPLE} = 0.5V$, frequency = 1kHz $I_{OUT} = 1$ mA		76		dB
SVR		$V_{\rm IN} = V_{\rm OUTNOM} + 1V + / - V_{\rm RIPPLE}$ $V_{\rm RIPPLE} = 0.5V$, frequency = 10kHz $I_{\rm OUT} = 1$ mA		76		
		$V_{IN} = V_{OUTNOM} + 1V + /-V_{RIPPLE}$ $V_{RIPPLE} = 0.5V$, frequency =100kHz $I_{OUT} = 1$ mA		54		
		I _{OUT} = 0mA		31		μΑ
	Quiescent current	I _{OUT} = 0mA, -40°C < T _J < 125°C			60	
		I _{OUT} = 0 to 150mA		75		
IQ		I _{OUT} = 0 to 150mA -40°C < T _J < 125°C			110	
		V _{IN} input current in OFF mode: V _{EN} = GND		0.001	1	
I _{SC}	Short-circuit current	R _L = 0	200			mA
V	Enable input logic low	V _{IN} = 2.4V to 5.5V, -40°C < T _J < 85°C			0.4	V
V _{EN}	Enable input logic high	V _{IN} = 2.4V to 5.5V, -40°C < T _J < 85°C	0.9			V
I _{EN}	Enable pin input current	V _{SHDN} = 5.5V		0.1	100	nA
T _{ON}	Turn-on time (3)			200		μs

Electrical characteristics LD59015

Table 5. Electrical characteristics (continued) (1)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
т	Thermal shutdown			160		°C
I SHDN	Hysteresis			20		
C _{OUT}	Output capacitor	Capacitance (see typical performance characteristics for stability)	1		4.7	μF

^{1.} For $V_{OUT(NOM)} < 1.3 \text{ V}$, $V_{IN} = 2.4 \text{ V}$.

Note: All transient values are guaranteed by design, not tested in production.



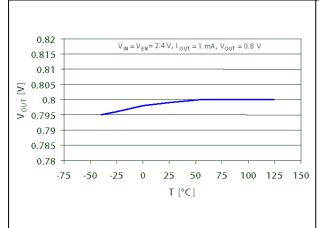
^{2.} Dropout voltage is the input-to-output voltage difference at which the output voltage is 100 mV below its nominal value. This specification does not apply to output voltages below 1.7 V.

^{3.} Turn-on time is time measured between the enable input just exceeding V_{EN} high value and the output voltage just reaching 95% of its nominal value.

6 Typical performance characteristics

 $C_{IN} = C_{OUT} = 1 \mu F$

Figure 4. V_{OUT} vs. temperature ($V_{OUT} = 0.8 \text{ V}$) Figure 5. V_{OUT} vs. temperature ($V_{OUT} = 3.3 \text{ V}$)



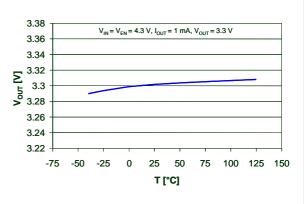


Figure 6. Quiescent current vs. temperature

50.00 45.00 V_{IN} = 2.4 V, V_O = 0.8 V V_{IN} = 4.3 V, V_O = 3.3 V 40.00 35.00 25.00 20.00 15.00 -40 -20 00 20 40 60 80 100 120 140 Temperature [°C]

Figure 7. Quiescent current vs. I_{OUT}

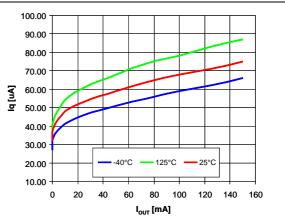


Figure 8. V_{OUT} vs. V_{IN}

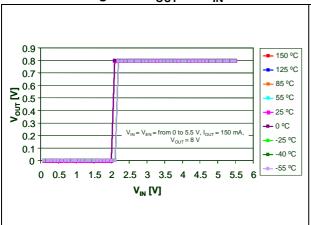
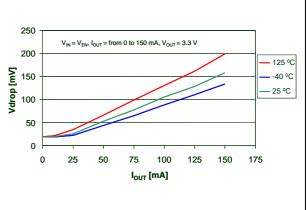


Figure 9. V_{DROP} vs. I_{OUT}



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Figure 11. ESR vs. C_{OUT} Figure 10. V_{UVLO} vs. temperature 10.00 2.50 turn-off turn on 2.30 1.00 ESR at 100 kHz [Ohm] UVLO Thresholds [V] ESR MAX 2.10 ESR MIN 0.10 1.90 0.01 1.70 0.00 1.50 100 120 140 0.0 1.0 2.0 3.0 4.0 5.0 6.0 7.0 8.0 9.0 10.0 -40 -20 60 80 C_{ουτ} [μF] Temperature [°C]

Figure 12. Supply voltage rejection vs. frequency

Figure 13. Supply voltage rejection vs. I_{OUT}

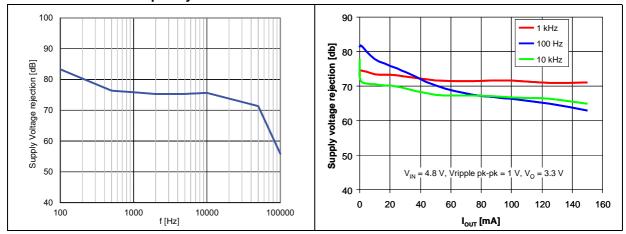


Figure 14. I_{SC} vs. V_{DROP}

Figure 15. Line transient (V_{OUT} = 0.8 V)

Cout = 1 µF; V_{IN} = V_{EN} = from 2.4 to 3.3 V; V_{OUT} = 0.8 V; I_{OUT} = 1 mA; t_{rise} = t_{fall} = 25 µs

0.4 V_{IN} = V_{EN} 0.35 0.3 0.25 0.2 <u>9</u> 0.15 - 125 °C 0.1 -40 °C 0.05 25 °C 3 5 6 Vdrop [V]

Figure 16. Line transient (V_{OUT} = 3.3 V)

Figure 17. Load transient (V_{OUT} = 0.8 V)

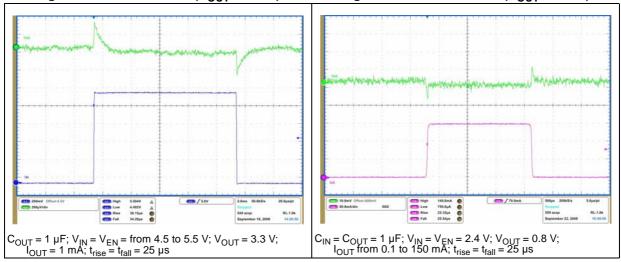


Figure 18. Load transient (V_{OUT} = 3.3 V)

Figure 19. Start-up transient

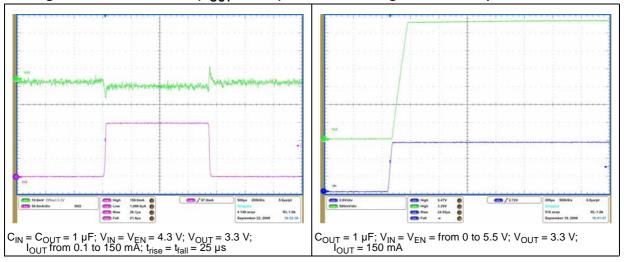
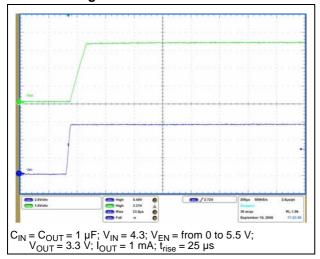


Figure 20. Enable transient





7 Package mechanical data

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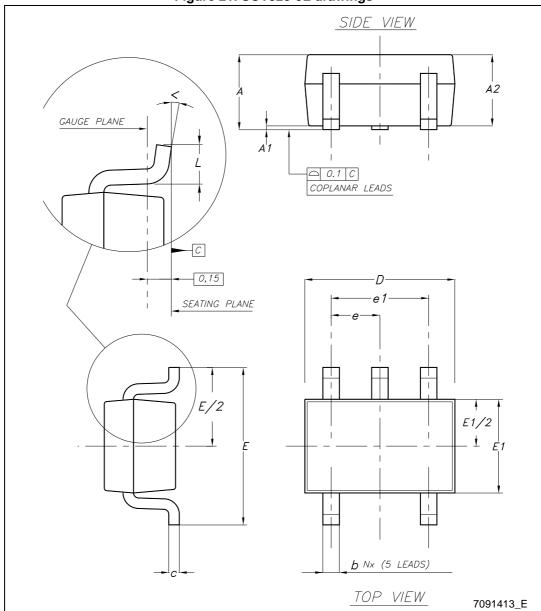


Figure 21. SOT323-5L drawings

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Table 6. SOT323-5L mechanical data

Dim.	mm			
	Min.	Тур.	Max.	
А	0.80		1.10	
A1	0		0.10	
A2	0.80	0.90	1	
b	0.15		0.30	
С	0.10		0.22	
D	1.80	2	2.20	
E	1.80	2.10	2.40	
E1	1.15	1.25	1.35	
е		0.65		
e1		1.30		
L	0.26	0.36	0.46	
<	0°		8°	



8 Packaging mechanical data

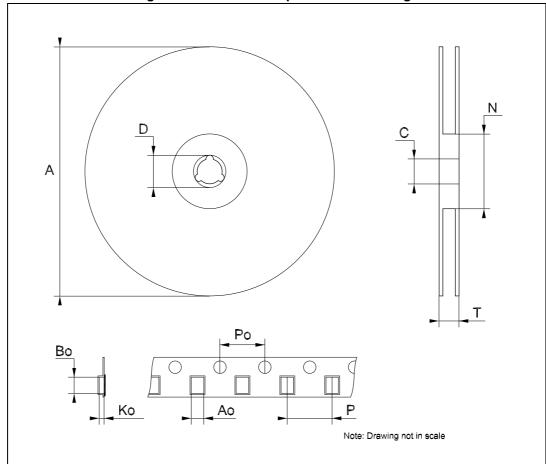


Figure 22. SOT323-5L tape and reel drawings

Table 7. SOT323-5L tape and reel mechanical data

Dim.	mm			
Dilli.	Min.	Тур.	Max.	
А	175	180	185	
С	12.8	13	13.2	
D	20.2			
N	59.5	60	60.5	
Т			14.4	
Ao		2.25		
Во		3.17		
Ko		1.2		
Ро	3.9	4.0	4.1	
Р	3.9	4.0	4.2	



Revision history LD59015

9 Revision history

Table 8. Document revision history

Date	Revision	Changes
10-May-2010	1	Initial release.
21-Dec-2011	2	Modified: operating input voltage (V _{IN}) min. value <i>Table 5 on page 7</i> . Availability LD59015C08R code <i>Table 1 on page 1</i> .
06-Jul-2012	3	Updated: Table 1 on page 1.
24-Apr-2014	4	Part number LD59015xx changed to LD59015. Updated the description in cover page and Section 7: Package mechanical data. Added Section 8: Packaging mechanical data. Minor text changes.

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