Neural Network Topology Patent Specification

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Nexus Intelligent Systems, Inc.

1. PRELIMINARY SPECIFICATIONS

1.1 Patent Identification

Patent Title: Adaptive Neural Network Topology Optimization Method

Patent Application Serial Number: NIS-2024-PA-0037

Inventor(s): Dr. Elena Rodriguez, Michael Chen

Filing Date: January 22, 2024

1.2 Technical Field

This patent specification relates to advanced machine learning architectures, specifically dynamic neural network topology optimization techniques for predictive analytics and intelligent system design.

2. TECHNICAL BACKGROUND

2.1 Problem Domain

The present invention addresses critical limitations in existing neural network architectures, specifically:

- Inefficient computational resource allocation
- Static network topology constraints
- Limited adaptive learning capabilities
- Suboptimal performance in complex, multi-dimensional predictive environments

2.2 Prior Art Limitations

Existing neural network methodologies demonstrate substantial constraints in:

- Real-time topology reconfiguration
- Intelligent layer pruning mechanisms
- Dynamic computational graph optimization
- Adaptive learning rate modulation

3. INVENTION SUMMARY

3.1 Technical Overview

The proposed neural network topology optimization method enables:

- Autonomous network structural reconfiguration
- Intelligent computational resource allocation
- Dynamic layer complexity management
- Predictive performance optimization through adaptive learning strategies

3.2 Key Innovation Components

Adaptive Topology Reconfiguration Algorithm

Intelligent Layer Pruning Mechanism

Dynamic Computational Resource Allocation Framework

Performance-Driven Network Optimization Protocol

4. DETAILED TECHNICAL DESCRIPTION

4.1 Architectural Framework

The neural network topology optimization method comprises:

- Modular neural network architecture
- Autonomous reconfiguration engine
- Performance monitoring subsystem
- Adaptive learning rate controller

4.2 Operational Methodology

The invention implements a multi-stage optimization process:

4.2.1 Initial Configuration

- Establish baseline neural network topology
- Define performance metrics
- Initialize computational resource allocation parameters

4.2.2 Dynamic Adaptation

- Continuous performance monitoring
- Intelligent layer complexity assessment

- Autonomous topology modification

4.2.3 Optimization Cycle

- Real-time computational graph restructuring

- Adaptive learning rate modulation

- Efficiency-driven network refinement

5. CLAIMS

5.1 Primary Claims

A method for dynamically optimizing neural network topology through autonomous computational

resource allocation

A system for intelligent layer complexity management in machine learning architectures

A process for adaptive neural network performance enhancement

5.2 Unique Technological Contributions

- First-principles approach to neural network topology optimization

- Autonomous computational resource management

- Performance-driven architectural reconfiguration

6. LEGAL PROVISIONS

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6.2 Confidentiality

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7. EXECUTION

7.1 Inventor Signatures

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Date: January 22, 2024

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8. CERTIFICATION

The undersigned hereby certifies that the foregoing patent specification represents a true and accurate description of the claimed invention.