HiFive Unmatched KiCad Port

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File: hifive_unmatched_kicad_port.sch	
Title:	
Size: A2	Date:
KiCad E.D.A. kicad (5.1.10-1-10_14)	**Rev:** Id: 1/7

Sheet: /fu740_chiplink_gmii/
File: fu740_chiplink_gmii.sch

Title:

Size: A2	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.10-1-10_14)		Id: 2/7

U1B
FU740-C000

A1	DDR_VDDQ	DDR_MEM_DATA_0	AA3
AA1	DDR_VDDQ	DDR_MEM_DATA_1	AA2
AB8	DDR_VDDQ	DDR_MEM_DATA_2	AA4
AC5	DDR_VDDQ	DDR_MEM_DATA_3	AB1
AD2	DDR_VDDQ	DDR_MEM_DATA_4	V4
C5	DDR_VDDQ	DDR_MEM_DATA_5	Y1
D2	DDR_VDDQ	DDR_MEM_DATA_6	W1
F6	DDR_VDDQ	DDR_MEM_DATA_7	W3
G3	DDR_VDDQ	DDR_MEM_DQS_P_0	Y3
J7	DDR_VDDQ	DDR_MEM_DQS_M_0	Y2
K4	DDR_VDDQ	DDR_MEM_DM_0	W4
L1	DDR_VDDQ		
M8	DDR_VDDQ	DDR_MEM_DATA_8	V3
N5	DDR_VDDQ	DDR_MEM_DATA_9	V1
P2	DDR_VDDQ	DDR_MEM_DATA_10	V2
T4	DDR_VDDQ	DDR_MEM_DATA_11	T4
U3	DDR_VDDQ	DDR_MEM_DATA_12	T3
W7	DDR_VDDQ	DDR_MEM_DATA_13	R2
Y4	DDR_VDDQ	DDR_MEM_DATA_14	R3
AF3	DDR_VDDQCK	DDR_MEM_DATA_15	R1
		DDR_MEM_DQS_P_1	U2
		DDR_MEM_DQS_M_1	U1
N8	DDR_VDDPLL	DDR_MEM_DM_1	T2
R8	DDR_VDDPLL	DDR_MEM_DATA_16	AC2
		DDR_MEM_DATA_17	AB2
J15	DRR_PLL_AVDD	DDR_MEM_DATA_18	AD1
H15	DRR_PLL_AVSS	DDR_MEM_DATA_19	AC1
		DDR_MEM_DATA_20	AC4
		DDR_MEM_DATA_21	AD3
N2	DDR_MEM_ADDRESS_0	DDR_MEM_DATA_22	AD4
N1	DDR_MEM_ADDRESS_1	DDR_MEM_DATA_23	AE3
N7	DDR_MEM_ADDRESS_2	DDR_MEM_DQS_P_2	AE1
M4	DDR_MEM_ADDRESS_3	DDR_MEM_DQS_M_2	AE2
M2	DDR_MEM_ADDRESS_4	DDR_MEM_DM_2	AC3
M6	DDR_MEM_ADDRESS_5	DDR_MEM_DATA_24	AA5
M5	DDR_MEM_ADDRESS_7	DDR_MEM_DATA_25	Y7
L3	DDR_MEM_ADDRESS_8	DDR_MEM_DATA_26	AB6
L2	DDR_MEM_ADDRESS_9	DDR_MEM_DATA_27	AA7
L1	DDR_MEM_ADDRESS_10	DDR_MEM_DATA_28	Y6
M7	DDR_MEM_ADDRESS_11	DDR_MEM_DATA_29	Y5
L4	DDR_MEM_ADDRESS_12	DDR_MEM_DATA_30	W6
K4	DDR_MEM_ADDRESS_13	DDR_MEM_DATA_31	W5
L7	DDR_MEM_ADDRESS_14	DDR_MEM_DQS_P_3	AB5
K2	DDR_MEM_ADDRESS_15	DDR_MEM_DQS_M_3	AB4
		DDR_MEM_DM_3	AB7
N4	DDR_MEM_BANK_0	DDR_MEM_DATA_32	J1
N3	DDR_MEM_BANK_1	DDR_MEM_DATA_33	H1
N6	DRR_MEM_BANK_2	DDR_MEM_DATA_34	H2
P3	DRR_MEM_RAS_N	DDR_MEM_DATA_35	J3
P6	DRR_MEM_CAS_N	DDR_MEM_DATA_36	H3
P1	DRR_MEM_WE_N	DDR_MEM_DATA_37	F2
K3	DRR_MEM_PARITY_IN	DDR_MEM_DATA_38	H4
B8	DRR_MEM_ERROR_N	DDR_MEM_DATA_39	G4
B4	DRR_MEM_ODT_0	DDR_MEM_DQS_P_4	G1
C4	DRR_MEM_ODT_1	DDR_MEM_DQS_M_4	G2
A4	DRR_MEM_CS_N_0	DDR_MEM_DM_4	J4
B2	DRR_MEM_CS_N_1	DDR_MEM_DATA_40	F5
A2	DRR_MEM_RESET_N	DDR_MEM_DATA_41	G7
		DDR_MEM_DATA_42	H7
		DDR_MEM_DATA_43	H6
A2	DRR_MEM_CKE_0	DDR_MEM_DATA_44	K7
B1	DRR_MEM_CKE_1	DDR_MEM_DATA_45	J5
P4	DRR_MEM_CLK	DDR_MEM_DATA_46	K6
P5	DRR_MEM_CLK_N	DDR_MEM_DATA_47	J6
		DDR_MEM_DQS_P_5	G5
		DDR_MEM_DQS_M_5	G6
		DDR_MEM_DM_5	K5
		DDR_MEM_DATA_48	C7
		DDR_MEM_DATA_49	D8
V6	DRR_MEM_ECC_DATA_0	DDR_MEM_DATA_50	F8
U6	DRR_MEM_ECC_DATA_1	DDR_MEM_DATA_51	E8
V7	DRR_MEM_ECC_DATA_2	DDR_MEM_DATA_52	F7
U7	DRR_MEM_ECC_DATA_3	DDR_MEM_DATA_53	E6
R7	DRR_MEM_ECC_DATA_4	DDR_MEM_DATA_54	E5
T5	DRR_MEM_ECC_DATA_5	DDR_MEM_DATA_55	E7
R6	DRR_MEM_ECC_DATA_6	DDR_MEM_DQS_P_6	C6
R5	DRR_MEM_ECC_DATA_7	DDR_MEM_DQS_M_6	D6
U4	DRR_MEM_ECC_DQS_P	DDR_MEM_DM_6	D5
U5	DRR_MEM_ECC_DQS_M	DDR_MEM_DATA_56	C3
T7	DRR_MEM_ECC_DM	DDR_MEM_DATA_57	C2
		DDR_MEM_DATA_58	D4
		DDR_MEM_DATA_59	E3
AC6	DRR_PLL_TESTOUT_P	DDR_MEM_DATA_60	D1
AC7	DRR_PLL_TESTOUT_N	DDR_MEM_DATA_61	F4
		DDR_MEM_DATA_62	F3
AF2	DRR_ATB0	DDR_MEM_DATA_63	D3
AF4	DRR_ATB1	DDR_MEM_DQS_P_7	E2
		DDR_MEM_DQS_M_7	E1
Y9	DRR_CAL_0	DDR_MEM_DM_7	C1

Sheet: /fu740_ddr4/
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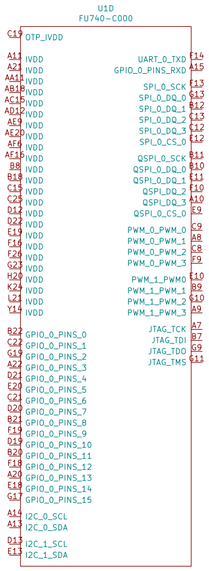

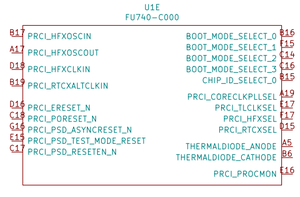

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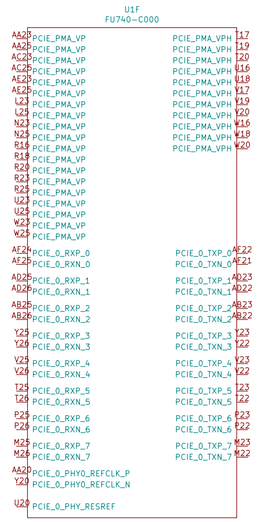
KiCad E.D.A. kicad (5.1.10-1-10_14)		Id: 4/7



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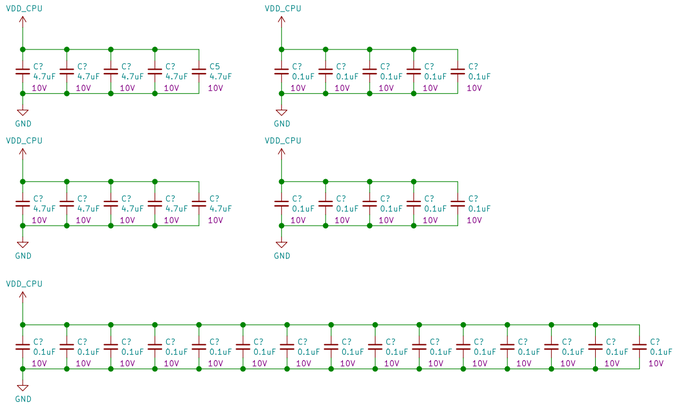
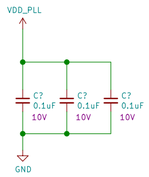
FU740 Power

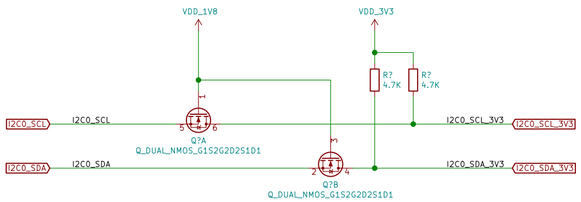
VDD_CPU bypass capacitors



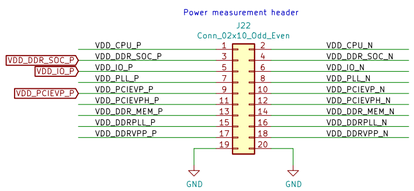
VDD_PLL bypass capacitors



I2C_0 level shift

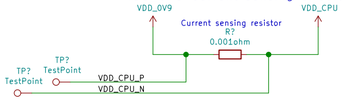


Power measurement header

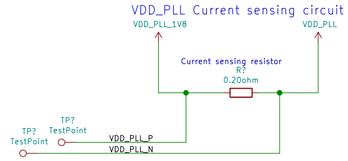


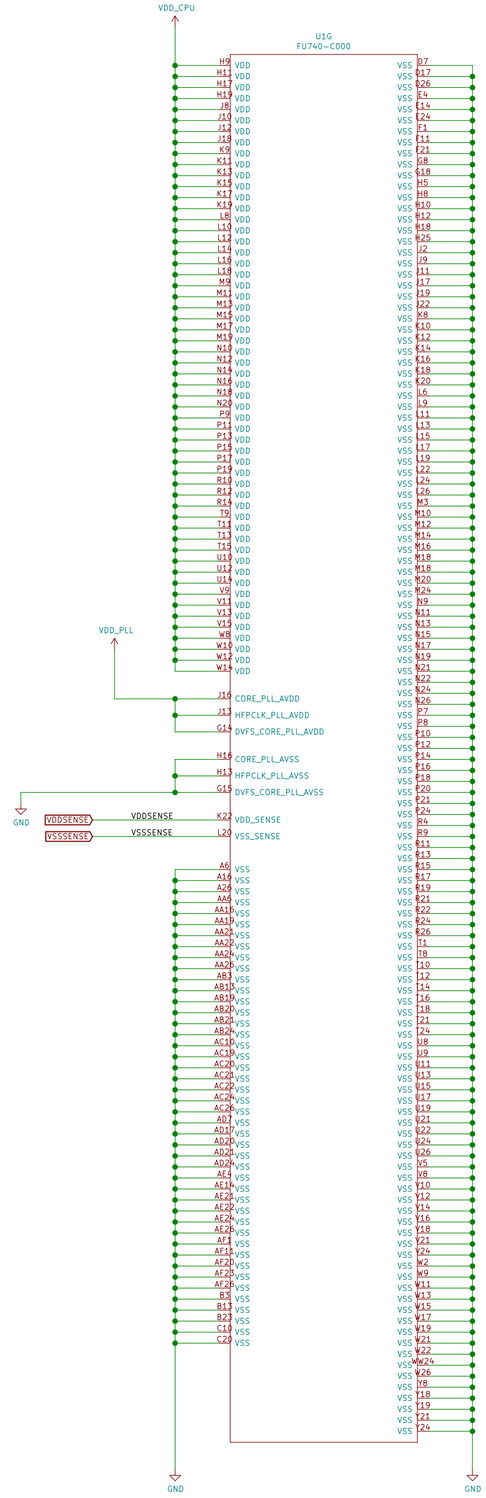
Layout note:

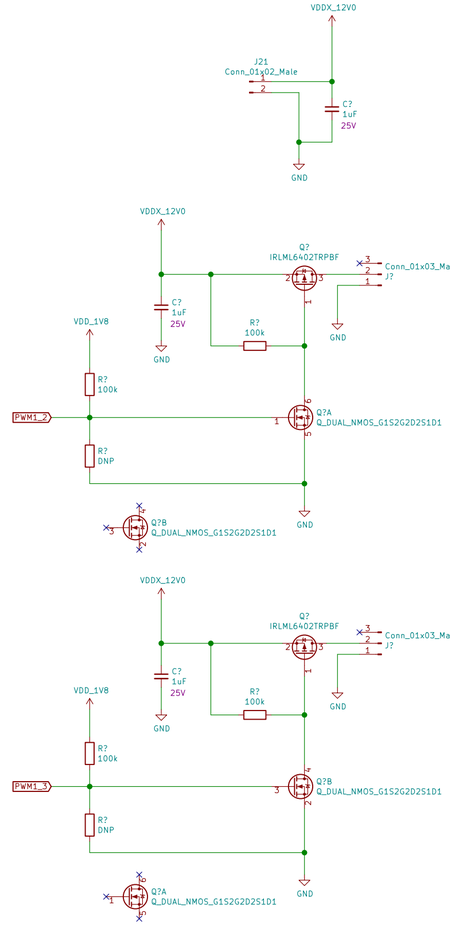
VDD_CPU Current sensing circuit

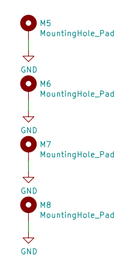


VDD_PLL Current sensing circuit









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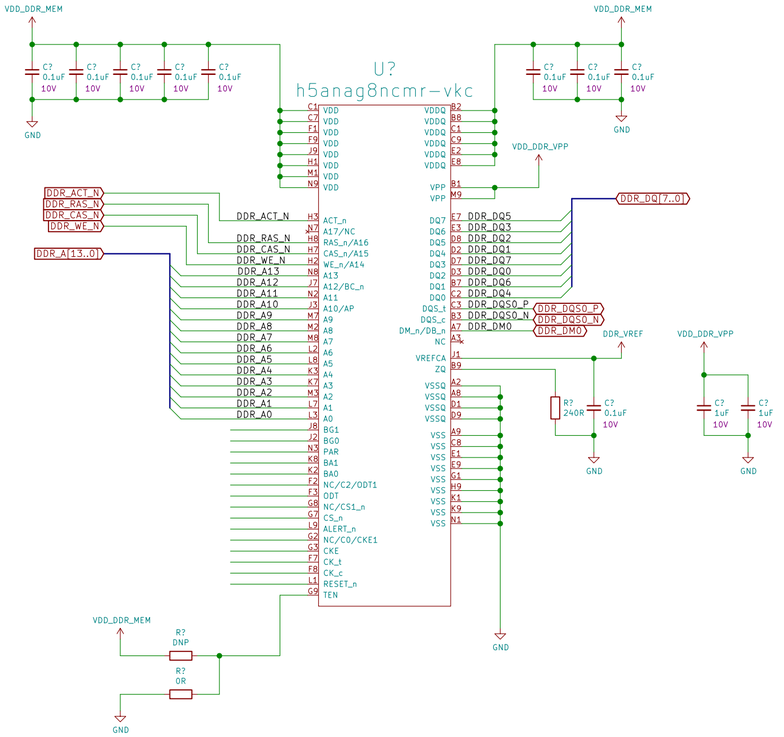
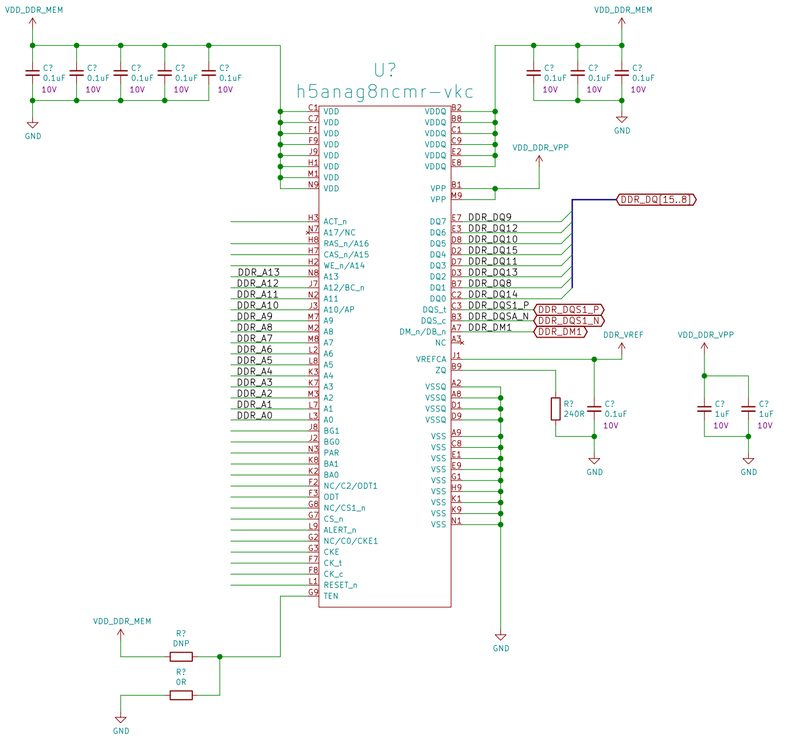
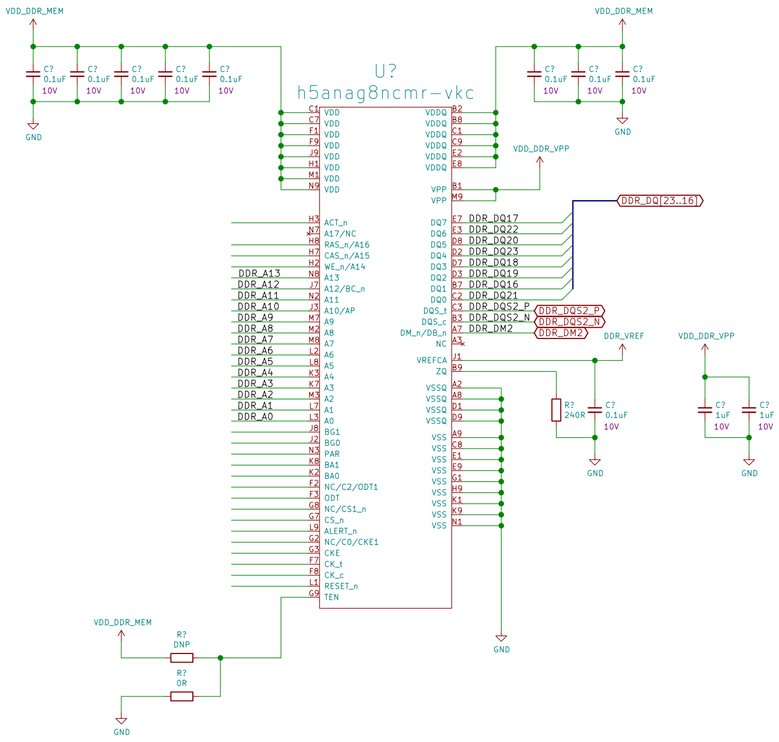
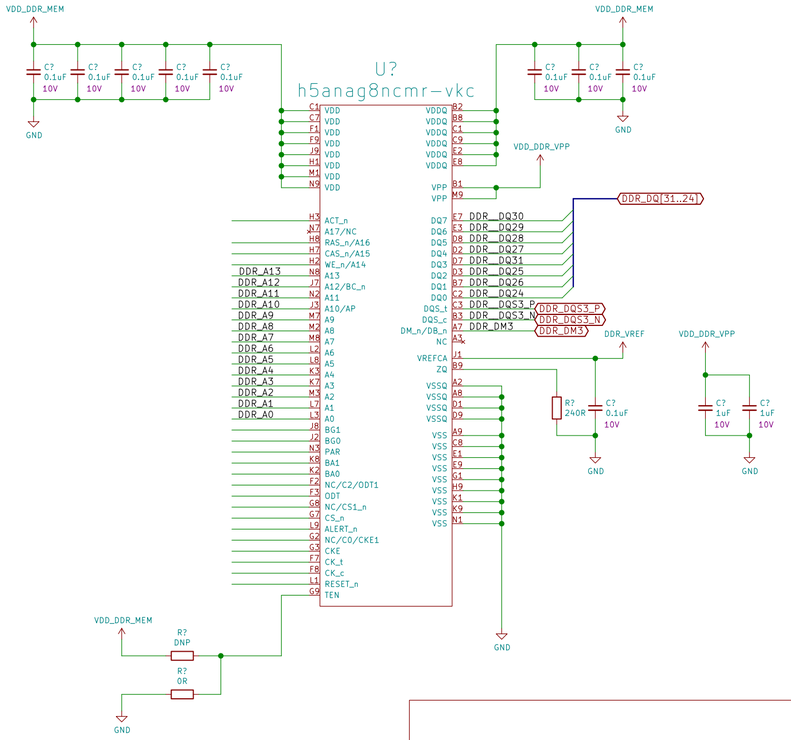
DDR4 RAM









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