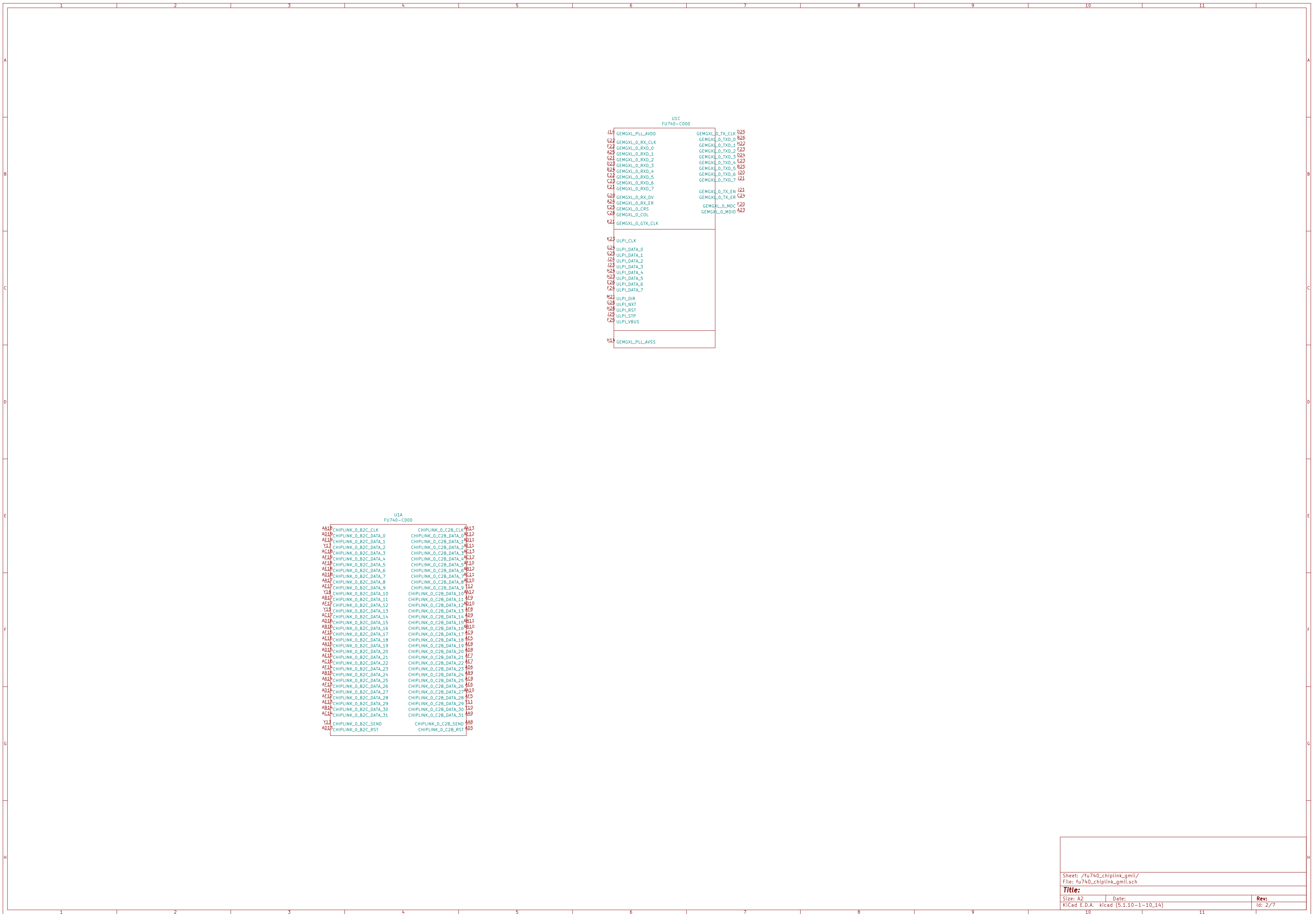


HiFive Unmatched KiCad Port

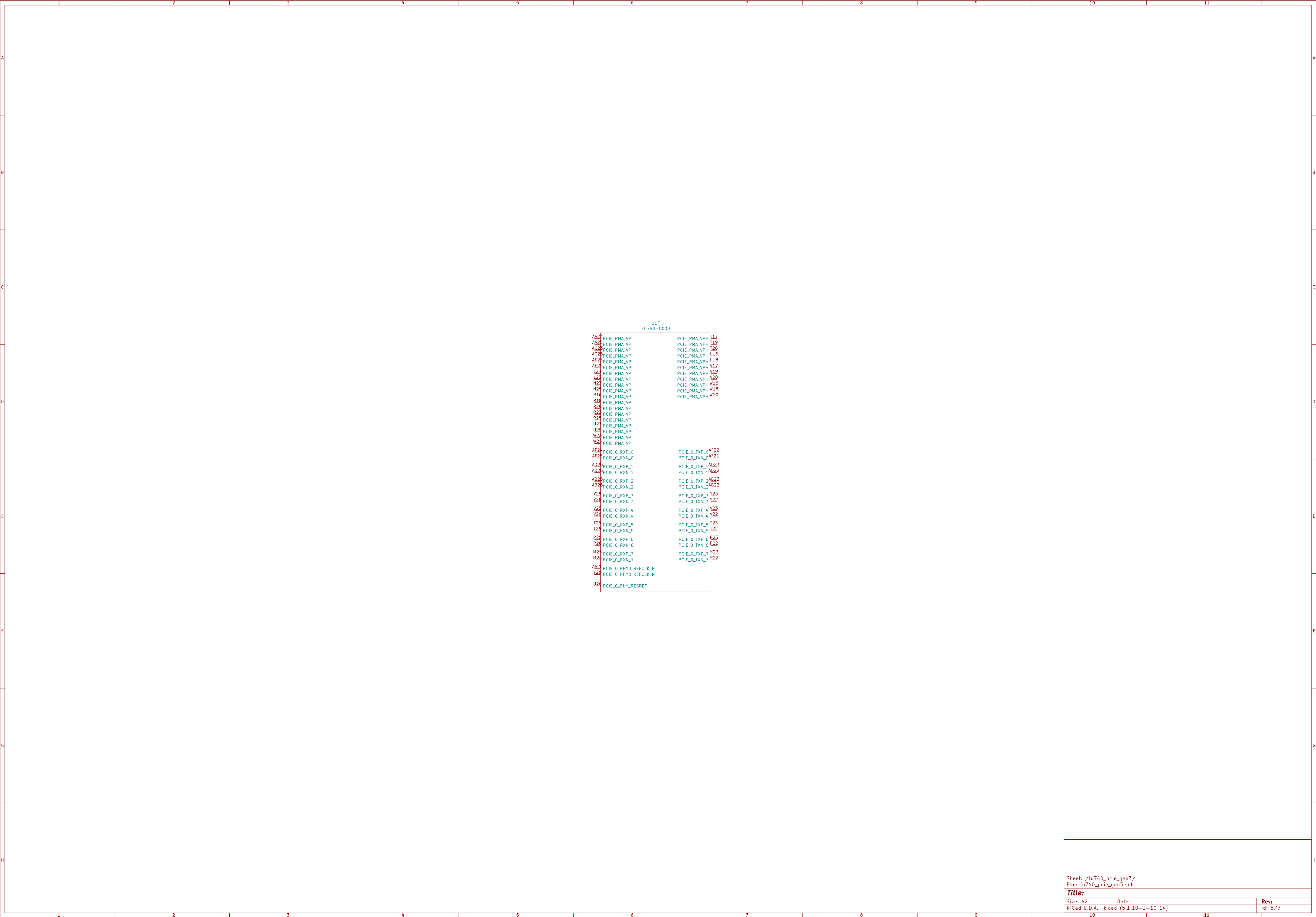
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KiCad E.D.A.	kicad (5.1.10-1-10_14)	Id: 1/7



1	2	3	4	5	6	7	8	9	10	11
A										
B										
C										
D										
E										
F										
G										
H										

U1B FU740-C000			
A1	DDR_VDDQ	DDR_MEM_DATA_0	A63
AA1	DDR_VDDQ	DDR_MEM_DATA_1	AA2
AB9	DDR_VDDQ	DDR_MEM_DATA_2	AA9
AC3	DDR_VDDQ	DDR_MEM_DATA_3	AB1
AD1	DDR_VDDQ	DDR_MEM_DATA_4	VA
CE	DDR_VDDQ	DDR_MEM_DATA_5	V1
D2	DDR_VDDQ	DDR_MEM_DATA_6	M1
EH	DDR_VDDQ	DDR_MEM_DATA_7	M3
GS	DDR_VDDQ	DDR_MEM_DQS_P_0	V2
J7	DDR_VDDQ	DDR_MEM_DQS_M_0	V2
K4	DDR_VDDQ	DDR_MEM_DM_0	M4
L1	DDR_VDDQ		
M8	DDR_VDDQ	DDR_MEM_DATA_8	V3
N5	DDR_VDDQ	DDR_MEM_DATA_9	V1
P2	DDR_VDDQ	DDR_MEM_DATA_10	V2
T6	DDR_VDDQ	DDR_MEM_DATA_11	T4
UX	DDR_VDDQ	DDR_MEM_DATA_12	V3
W7	DDR_VDDQ	DDR_MEM_DATA_13	E2
Y4	DDR_VDDQ	DDR_MEM_DATA_14	R2
AE3	DDR_VDDQCK	DDR_MEM_DATA_15	R1
		DDR_MEM_DQS_P_1	U2
		DDR_MEM_DQS_M_1	U1
		DDR_MEM_DM_1	T2
N8	DDR_VDDPLL		
R8	DDR_VDDPLL	DDR_MEM_DATA_16	AC2
J19	DDR_PLL_AVDD	DDR_MEM_DATA_17	AB2
H19	DDR_PLL_AVSS	DDR_MEM_DATA_18	AD1
		DDR_MEM_DATA_19	AC1
		DDR_MEM_DATA_20	AC9
		DDR_MEM_DATA_21	AD5
N2	DDR_MEM_ADDRESS_0	DDR_MEM_DATA_22	AD4
N4	DDR_MEM_ADDRESS_1	DDR_MEM_DATA_23	AE3
N7	DDR_MEM_ADDRESS_2	DDR_MEM_DQS_P_2	AE1
M1	DDR_MEM_ADDRESS_3	DDR_MEM_DQS_M_2	AE2
M2	DDR_MEM_ADDRESS_4	DDR_MEM_DM_2	AC3
M6	DDR_MEM_ADDRESS_5		
ME	DDR_MEM_ADDRESS_7	DDR_MEM_DATA_24	AA5
L5	DDR_MEM_ADDRESS_8	DDR_MEM_DATA_25	V7
L2	DDR_MEM_ADDRESS_9	DDR_MEM_DATA_26	AB6
L3	DDR_MEM_ADDRESS_10	DDR_MEM_DATA_27	AA7
M1	DDR_MEM_ADDRESS_11	DDR_MEM_DATA_28	V6
L4	DDR_MEM_ADDRESS_12	DDR_MEM_DATA_29	V5
K1	DDR_MEM_ADDRESS_13	DDR_MEM_DATA_30	M6
L7	DDR_MEM_ADDRESS_14	DDR_MEM_DATA_31	M5
K2	DDR_MEM_ADDRESS_15	DDR_MEM_DQS_P_3	AB5
		DDR_MEM_DQS_M_3	AB9
		DDR_MEM_DM_3	AB7
N4	DDR_MEM_BANK_0	DDR_MEM_DATA_32	J1
N3	DDR_MEM_BANK_1	DDR_MEM_DATA_33	H1
N6	DDR_MEM_BANK_2	DDR_MEM_DATA_34	H2
P6	DDR_MEM_RAS_N	DDR_MEM_DATA_35	J3
PE	DDR_MEM_CAS_N	DDR_MEM_DATA_36	H3
P1	DDR_MEM_WE_N	DDR_MEM_DATA_37	E2
K3	DDR_MEM_PARITY_IN	DDR_MEM_DATA_38	H4
B5	DDR_MEM_ERROR_N	DDR_MEM_DATA_39	G4
B4	DDR_MEM_ODT_0	DDR_MEM_DQS_P_4	G1
C1	DDR_MEM_ODT_1	DDR_MEM_DQS_M_4	G2
A4	DDR_MEM_CS_N_0	DDR_MEM_DM_4	J4
B2	DDR_MEM_CS_N_1		
AS	DDR_MEM_RESET_N	DDR_MEM_DATA_40	F5
		DDR_MEM_DATA_41	G7
		DDR_MEM_DATA_42	H7
		DDR_MEM_DATA_43	H6
A2	DDR_MEM_CKE_0	DDR_MEM_DATA_44	K7
B1	DDR_MEM_CKE_1	DDR_MEM_DATA_45	J5
P3	DDR_MEM_CLK	DDR_MEM_DATA_46	K6
P4	DDR_MEM_CLK_N	DDR_MEM_DATA_47	J6
		DDR_MEM_DQS_P_5	G5
		DDR_MEM_DQS_M_5	G6
		DDR_MEM_DM_5	K5
		DDR_MEM_DATA_48	K7
		DDR_MEM_DATA_49	D8
		DDR_MEM_DATA_50	F8
Y6	DDR_MEM_ECC_DATA_0	DDR_MEM_DATA_51	E8
U6	DDR_MEM_ECC_DATA_1	DDR_MEM_DATA_52	F7
V7	DDR_MEM_ECC_DATA_2	DDR_MEM_DATA_53	E6
U7	DDR_MEM_ECC_DATA_3	DDR_MEM_DATA_54	E5
R7	DDR_MEM_ECC_DATA_4	DDR_MEM_DATA_55	E7
T5	DDR_MEM_ECC_DATA_5	DDR_MEM_DQS_P_6	C6
R6	DDR_MEM_ECC_DATA_6	DDR_MEM_DQS_M_6	D6
RS	DDR_MEM_ECC_DATA_7	DDR_MEM_DM_6	D5
U4	DDR_MEM_ECC_DQS_P		
U5	DDR_MEM_ECC_DQS_M	DDR_MEM_DATA_56	C2
T7	DDR_MEM_ECC_DM	DDR_MEM_DATA_57	G2
		DDR_MEM_DATA_58	D4
		DDR_MEM_DATA_59	E3
		DDR_MEM_DATA_60	D1
AC4	DDR_PLL_TESTOUT_P	DDR_MEM_DATA_61	F1
AE1	DDR_PLL_TESTOUT_N	DDR_MEM_DATA_62	E3
		DDR_MEM_DATA_63	D3
AE2	DDR_ATB0	DDR_MEM_DQS_P_7	E2
AE1	DDR_ATB1	DDR_MEM_DQS_M_7	E1
Y9	DDR_CAL_0	DDR_MEM_DM_7	C1

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Size: A2	Date:	Rev:	
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Title:

Size: A2

Date:

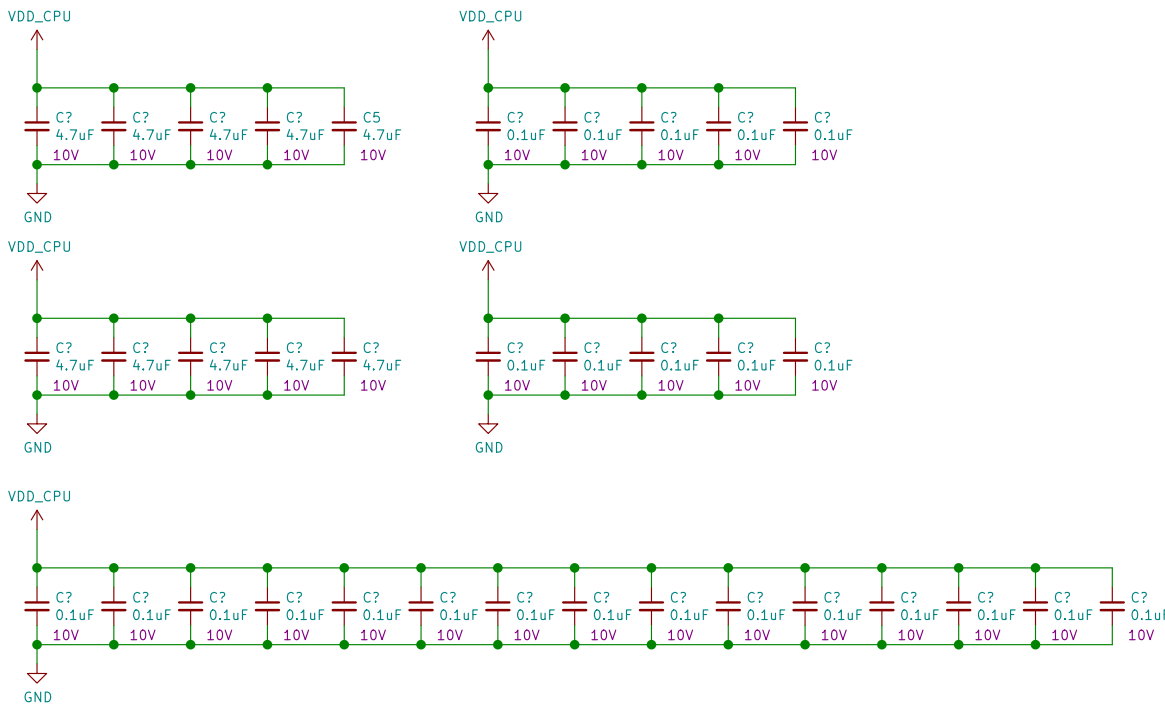
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Rev:

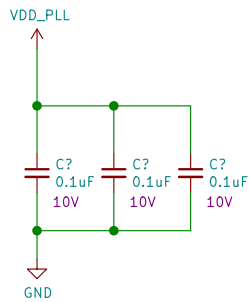
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FU740 Power

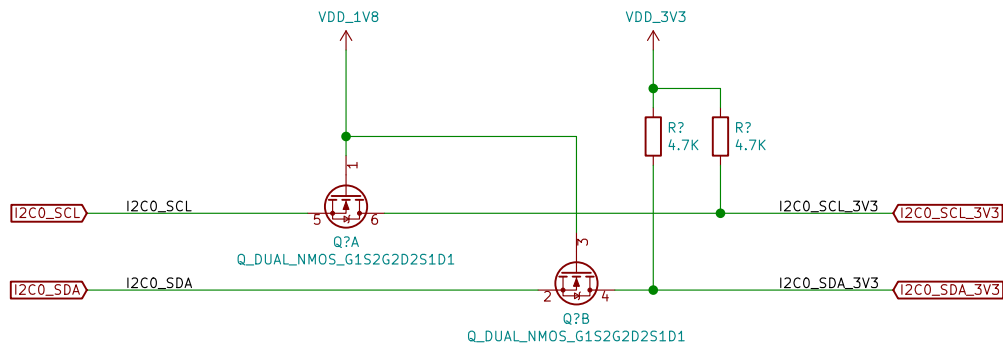
VDD_CPU bypass capacitors



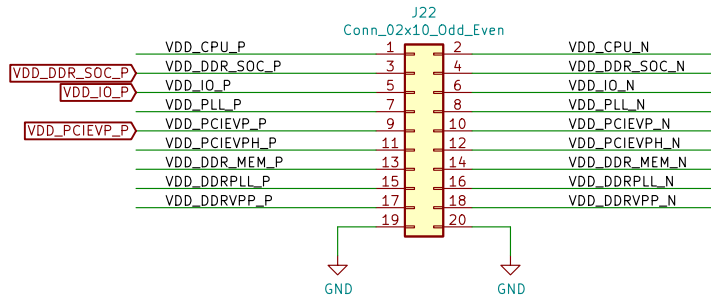
VDD_PLL bypass capacitors



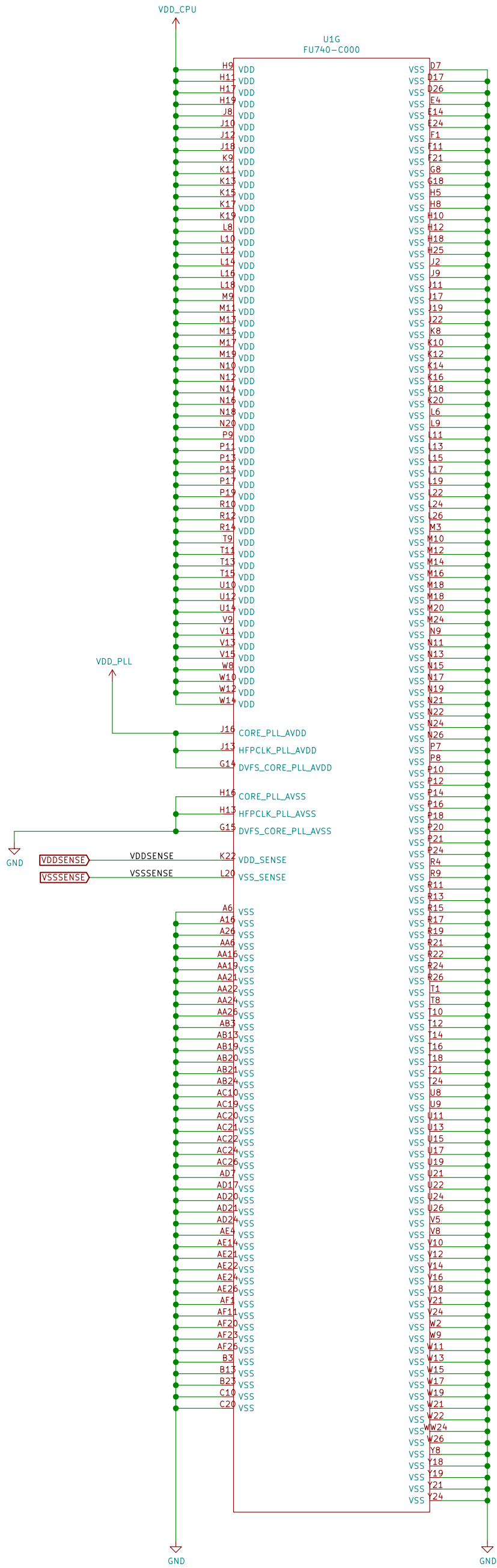
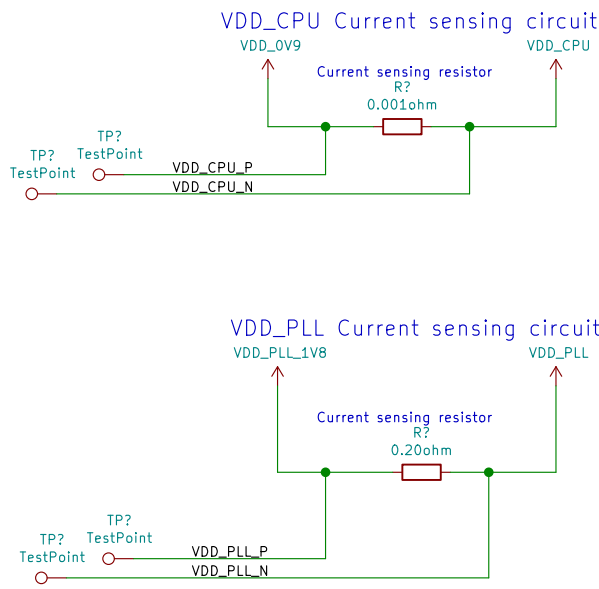
I2C_0 level shift



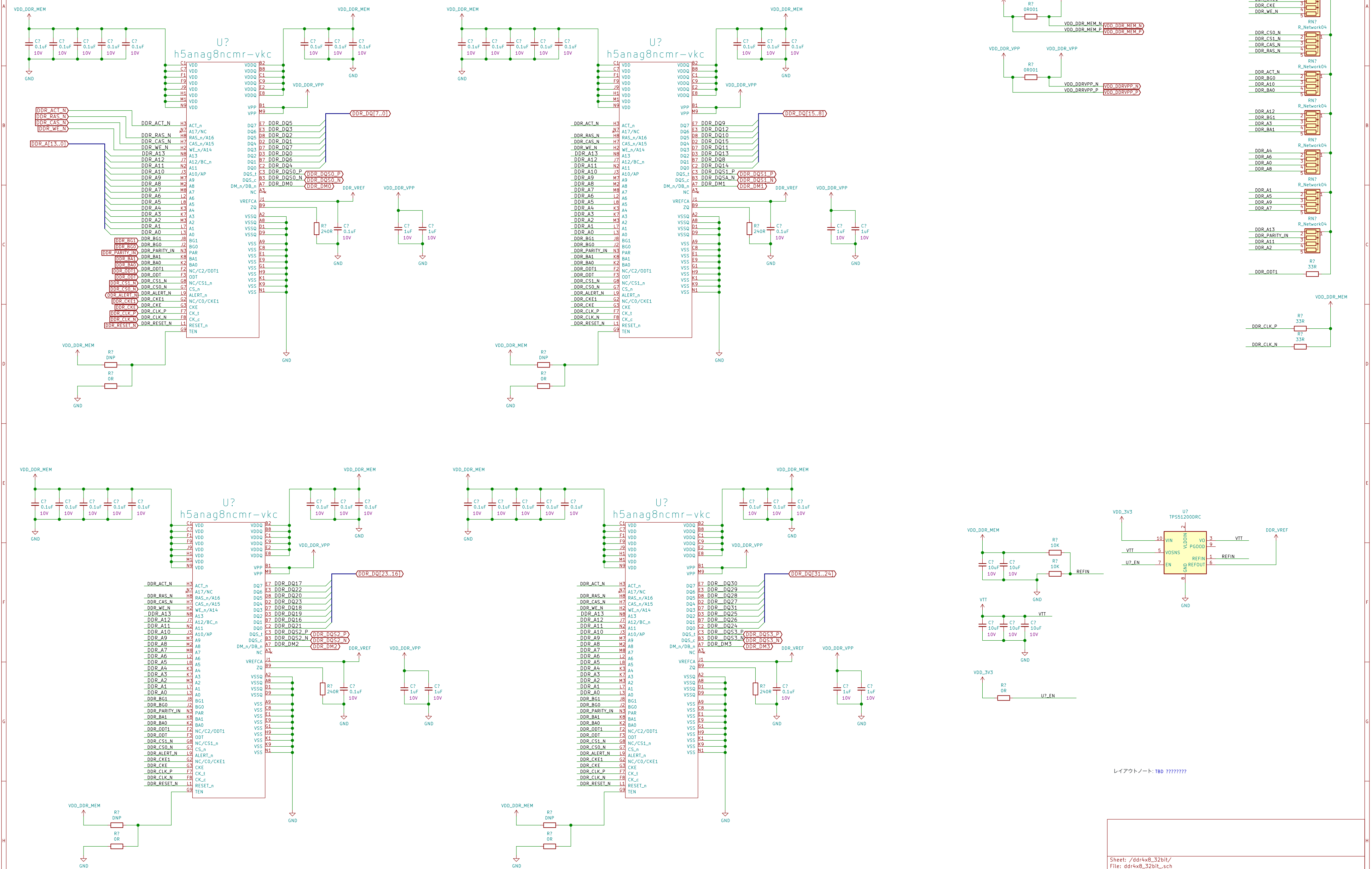
Power measurement header



Layout note:



DDR4 RAM



レイアウトノート: TBD ????????