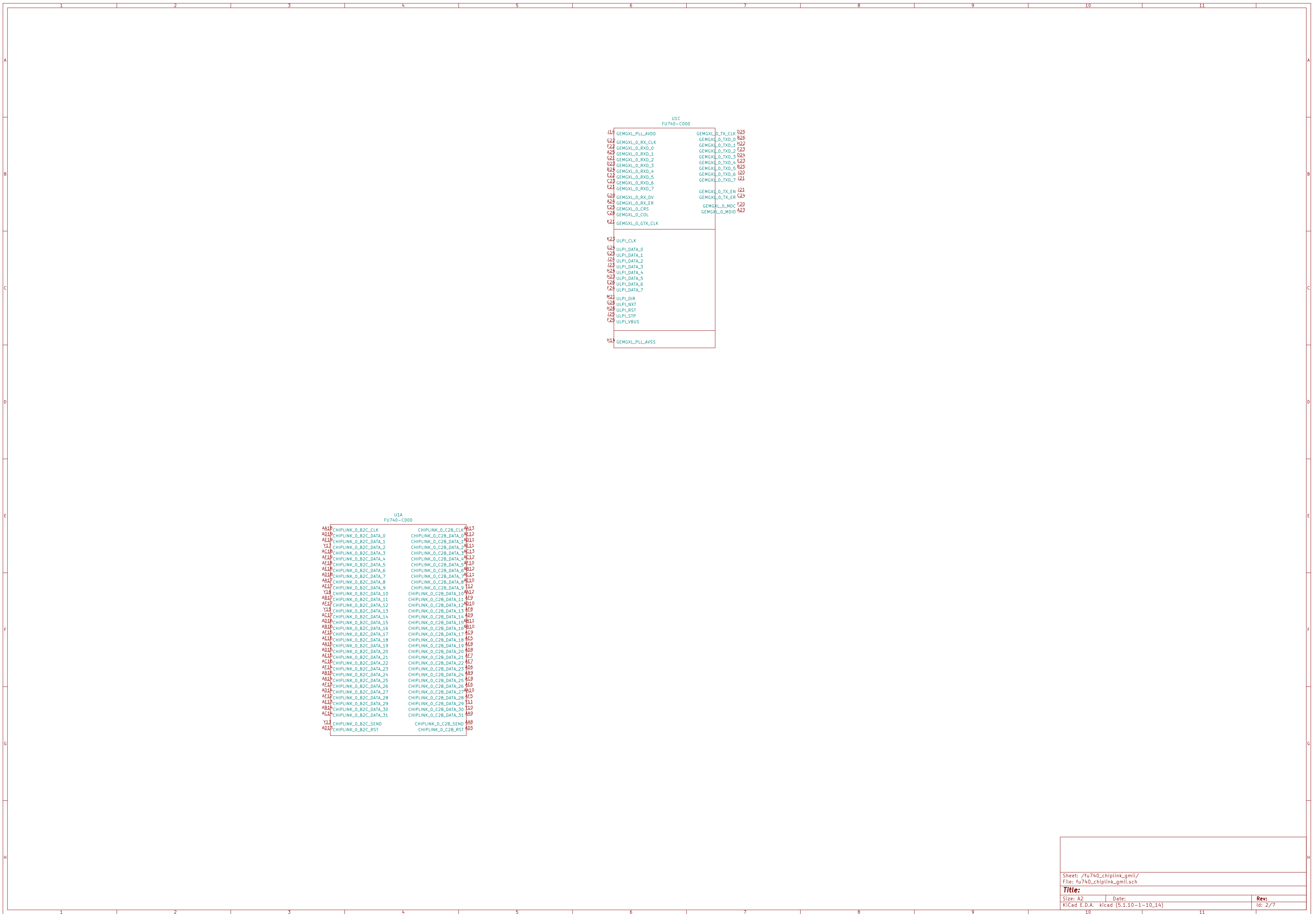


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C												C
D												D
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1	2	3	4	5	6	7	8	9	10	11
A										A
B										B
C										C
D										D
E										E
F										F
G										G
H										H

U18
FU740-C000

A1	DDR_VDDQ	DDR_MEM_DATA_0	A57
AA1	DDR_VDDQ	DDR_MEM_DATA_1	A62
AB1	DDR_VDDQ	DDR_MEM_DATA_2	A67
AC1	DDR_VDDQ	DDR_MEM_DATA_3	A74
AD1	DDR_VDDQ	DDR_MEM_DATA_4	V4
CE	DDR_VDDQ	DDR_MEM_DATA_5	V1
DE	DDR_VDDQ	DDR_MEM_DATA_6	M4
FE	DDR_VDDQ	DDR_MEM_DATA_7	M3
CE	DDR_VDDQ	DDR_MEM_DATA_7	V3
J7	DDR_VDDQ	DDR_MEM_DQS_P_0	V2
K4	DDR_VDDQ	DDR_MEM_DQS_M_0	M4
L1	DDR_VDDQ	DDR_MEM_DM_0	V3
ME	DDR_VDDQ	DDR_MEM_DATA_8	V1
NE	DDR_VDDQ	DDR_MEM_DATA_9	V2
P2	DDR_VDDQ	DDR_MEM_DATA_10	T4
T6	DDR_VDDQ	DDR_MEM_DATA_11	T3
U3	DDR_VDDQ	DDR_MEM_DATA_12	E4
W1	DDR_VDDQ	DDR_MEM_DATA_13	E3
Y4	DDR_VDDQ	DDR_MEM_DATA_14	R1
AF1	DDR_VDDQCK	DDR_MEM_DATA_15	U2
		DDR_MEM_DQS_P_1	V1
		DDR_MEM_DQS_M_1	T2
NE	DDR_VDDPLL	DDR_MEM_DM_1	A62
RE	DDR_VDDPLL	DDR_MEM_DATA_16	A67
L13	DDR_PLL_AVDD	DDR_MEM_DATA_17	A61
H17	DDR_PLL_AVSS	DDR_MEM_DATA_18	AC1
		DDR_MEM_DATA_19	AC4
		DDR_MEM_DATA_20	A67
N2	DDR_MEM_ADDRESS_0	DDR_MEM_DATA_21	A63
NE	DDR_MEM_ADDRESS_1	DDR_MEM_DATA_22	A67
N7	DDR_MEM_ADDRESS_2	DDR_MEM_DATA_23	A61
M1	DDR_MEM_ADDRESS_3	DDR_MEM_DQS_P_2	A62
MA	DDR_MEM_ADDRESS_4	DDR_MEM_DQS_M_2	A67
ME	DDR_MEM_ADDRESS_5	DDR_MEM_DATA_24	A65
LE	DDR_MEM_ADDRESS_7	DDR_MEM_DATA_25	T7
L6	DDR_MEM_ADDRESS_8	DDR_MEM_DATA_26	A66
L2	DDR_MEM_ADDRESS_9	DDR_MEM_DATA_27	A67
L3	DDR_MEM_ADDRESS_10	DDR_MEM_DATA_28	V6
M1	DDR_MEM_ADDRESS_11	DDR_MEM_DATA_29	V5
L4	DDR_MEM_ADDRESS_12	DDR_MEM_DATA_30	M6
K4	DDR_MEM_ADDRESS_13	DDR_MEM_DATA_31	M5
L7	DDR_MEM_ADDRESS_14	DDR_MEM_DQS_P_3	A65
K4	DDR_MEM_ADDRESS_15	DDR_MEM_DQS_M_3	A64
		DDR_MEM_DM_3	A67
NE	DDR_MEM_BANK_0	DDR_MEM_DATA_32	V1
NE	DDR_MEM_BANK_1	DDR_MEM_DATA_33	H1
NE	DDR_MEM_BANK_2	DDR_MEM_DATA_34	H2
PE	DDR_MEM_BANK_3	DDR_MEM_DATA_35	H3
PE	DDR_MEM_BANK_4	DDR_MEM_DATA_36	H4
PE	DDR_MEM_BANK_5	DDR_MEM_DATA_37	E2
K3	DDR_MEM_BANK_6	DDR_MEM_DATA_38	H4
BE	DDR_MEM_BANK_7	DDR_MEM_DATA_39	G4
CE	DDR_MEM_BANK_8	DDR_MEM_DATA_40	G1
AE	DDR_MEM_BANK_9	DDR_MEM_DATA_41	G2
BE	DDR_MEM_BANK_10	DDR_MEM_DATA_42	H7
AE	DDR_MEM_BANK_11	DDR_MEM_DATA_43	H6
A2	DDR_MEM_BANK_12	DDR_MEM_DATA_44	V5
B1	DDR_MEM_BANK_13	DDR_MEM_DATA_45	K6
P1	DDR_MEM_BANK_14	DDR_MEM_DATA_46	V6
PE	DDR_MEM_BANK_15	DDR_MEM_DATA_47	G5
		DDR_MEM_DQS_P_5	G6
		DDR_MEM_DQS_M_5	K5
		DDR_MEM_DM_5	C7
		DDR_MEM_DATA_48	D8
		DDR_MEM_DATA_49	F8
Y6	DDR_MEM_ECC_DATA_0	DDR_MEM_DATA_50	E8
U6	DDR_MEM_ECC_DATA_1	DDR_MEM_DATA_51	F7
V7	DDR_MEM_ECC_DATA_2	DDR_MEM_DATA_52	E6
U7	DDR_MEM_ECC_DATA_3	DDR_MEM_DATA_53	E5
TE	DDR_MEM_ECC_DATA_4	DDR_MEM_DATA_54	E7
RE	DDR_MEM_ECC_DATA_5	DDR_MEM_DATA_55	C6
RE	DDR_MEM_ECC_DATA_6	DDR_MEM_DATA_56	D6
RE	DDR_MEM_ECC_DATA_7	DDR_MEM_DATA_57	D5
U4	DDR_MEM_ECC_DQS_P	DDR_MEM_DATA_58	C3
U5	DDR_MEM_ECC_DQS_M	DDR_MEM_DATA_59	C2
T7	DDR_MEM_ECC_DM	DDR_MEM_DATA_60	D4
		DDR_MEM_DATA_61	E3
AC6	DDR_PLL_TESTOUT_P	DDR_MEM_DATA_62	D3
AC1	DDR_PLL_TESTOUT_N	DDR_MEM_DATA_63	E2
AF1	DDR_ATB0	DDR_MEM_DQS_P_7	E1
AF1	DDR_ATB1	DDR_MEM_DQS_M_7	K1
Y9	DDR_CAL_0	DDR_MEM_DM_7	K1

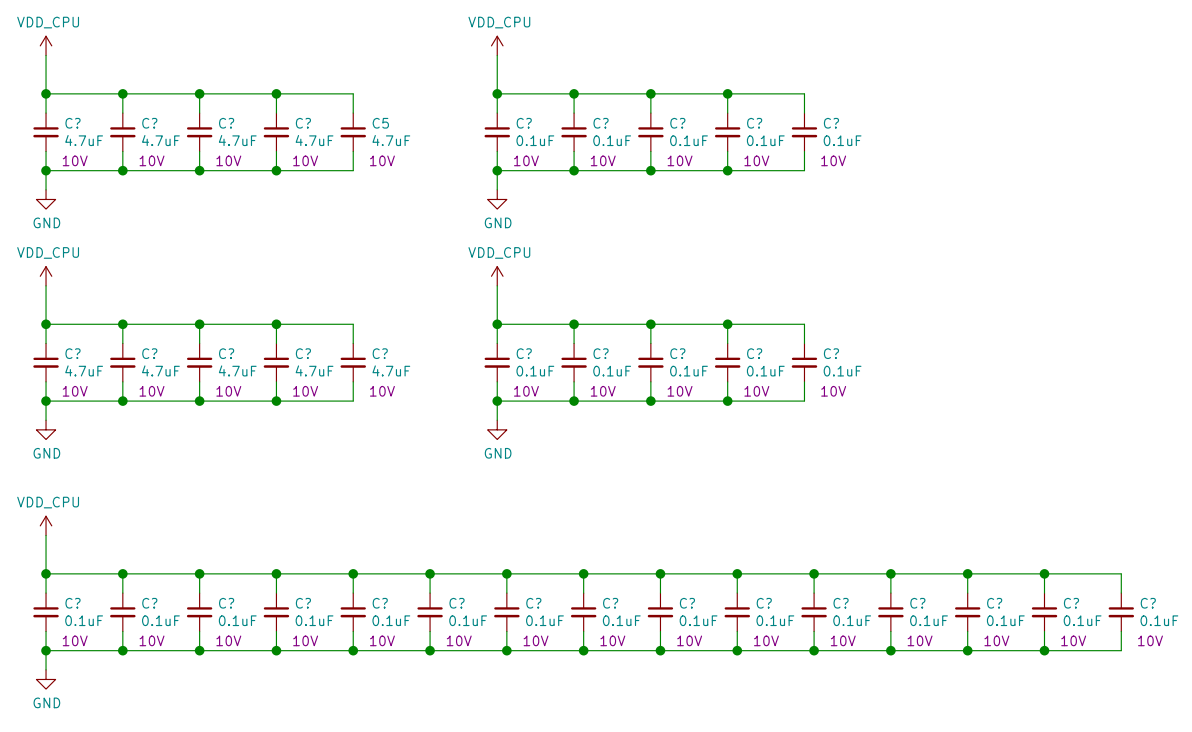
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File: fu740_ddr4.sch

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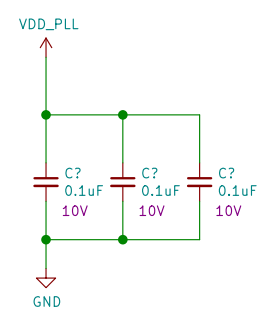
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FU740 Power

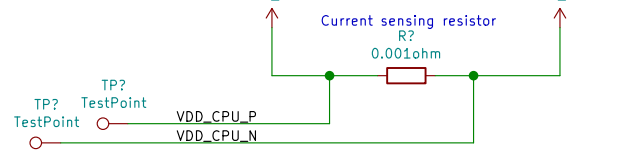
VDD_CPU bypass capacitors



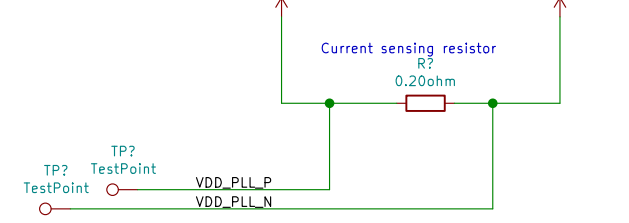
VDD_PLL bypass capacitors



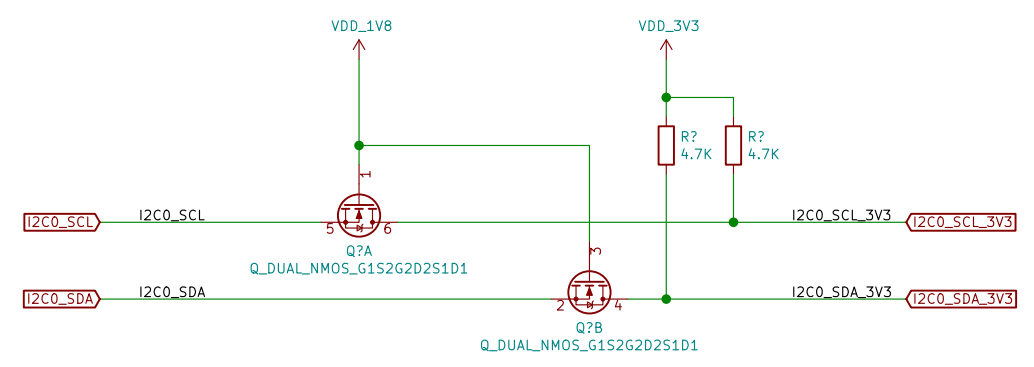
VDD_CPU Current sensing circuit



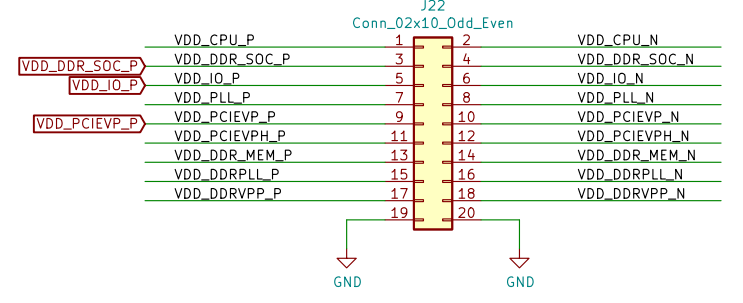
VDD_PLL Current sensing circuit



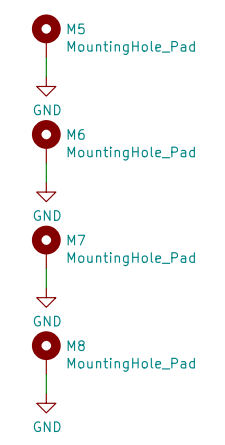
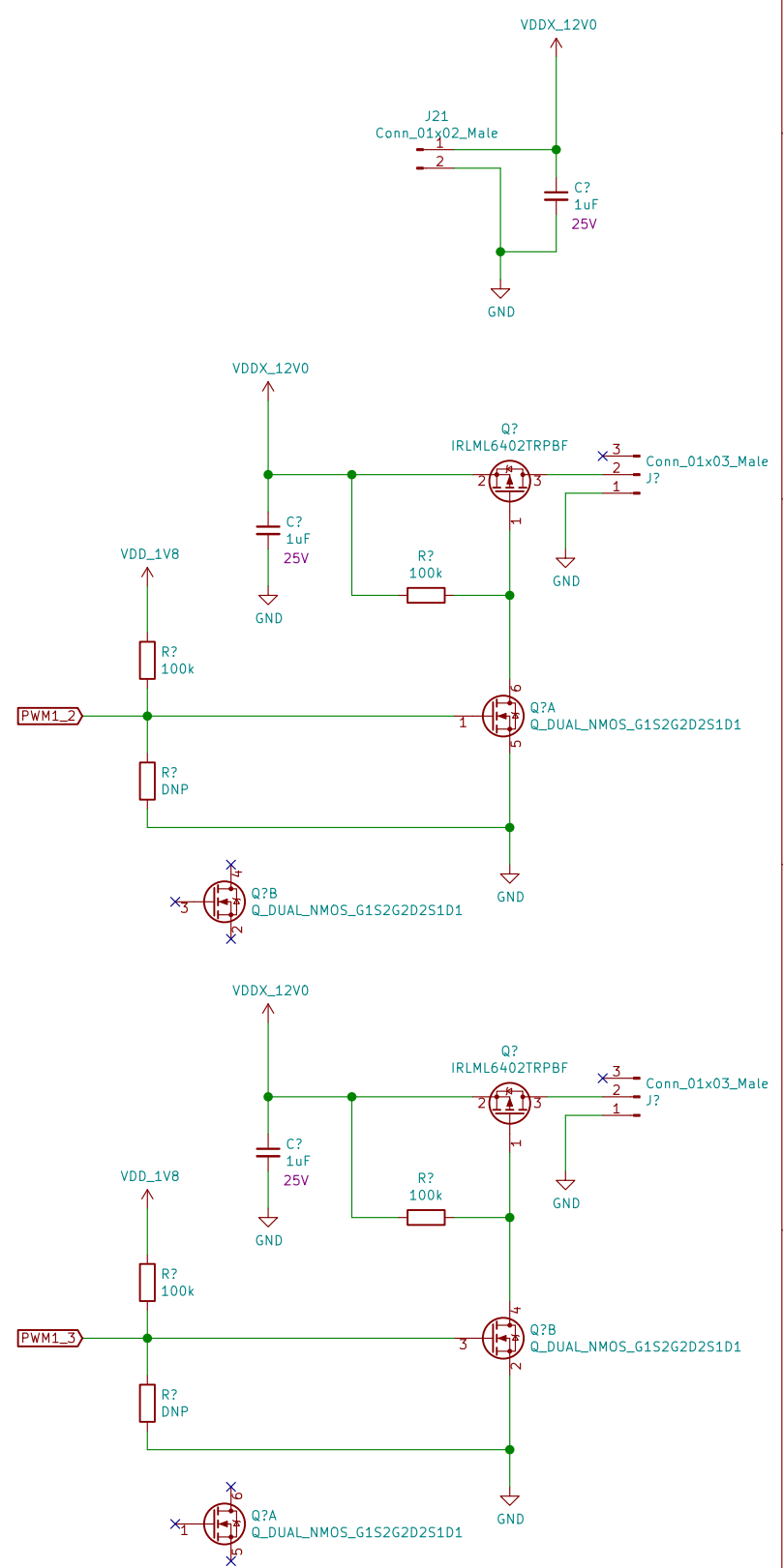
I2C_0 level shift



Power measurement header



Layout note:



DDR4 RAM

