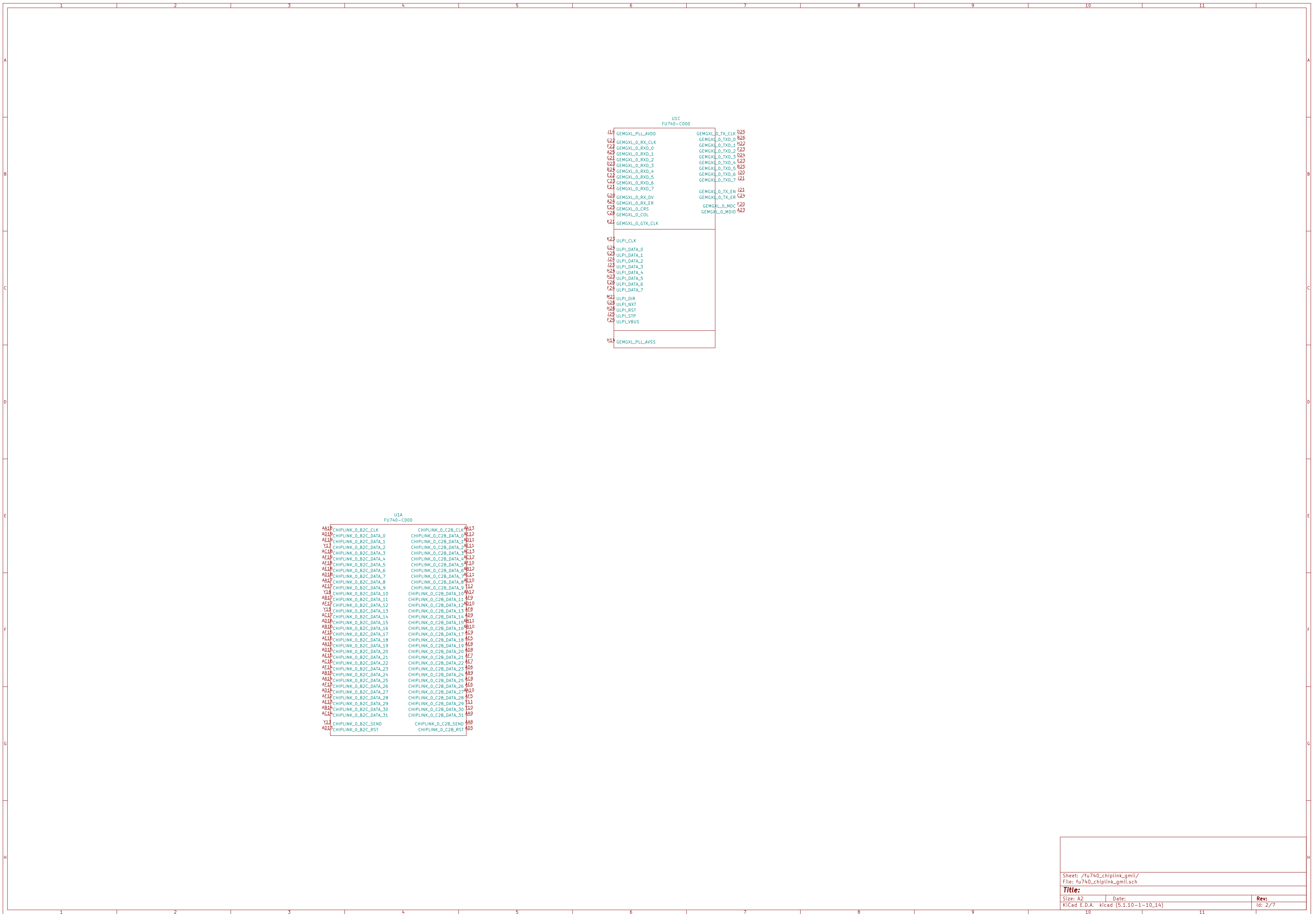


HiFive Unmatched KiCad Port

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KiCad E.D.A.	kicad (5.1.10-1-10_14)	Id: 1/7



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A										A
B										B
C										C
D										D
E										E
F										F
G										G
H										H

U18
FU740-C000

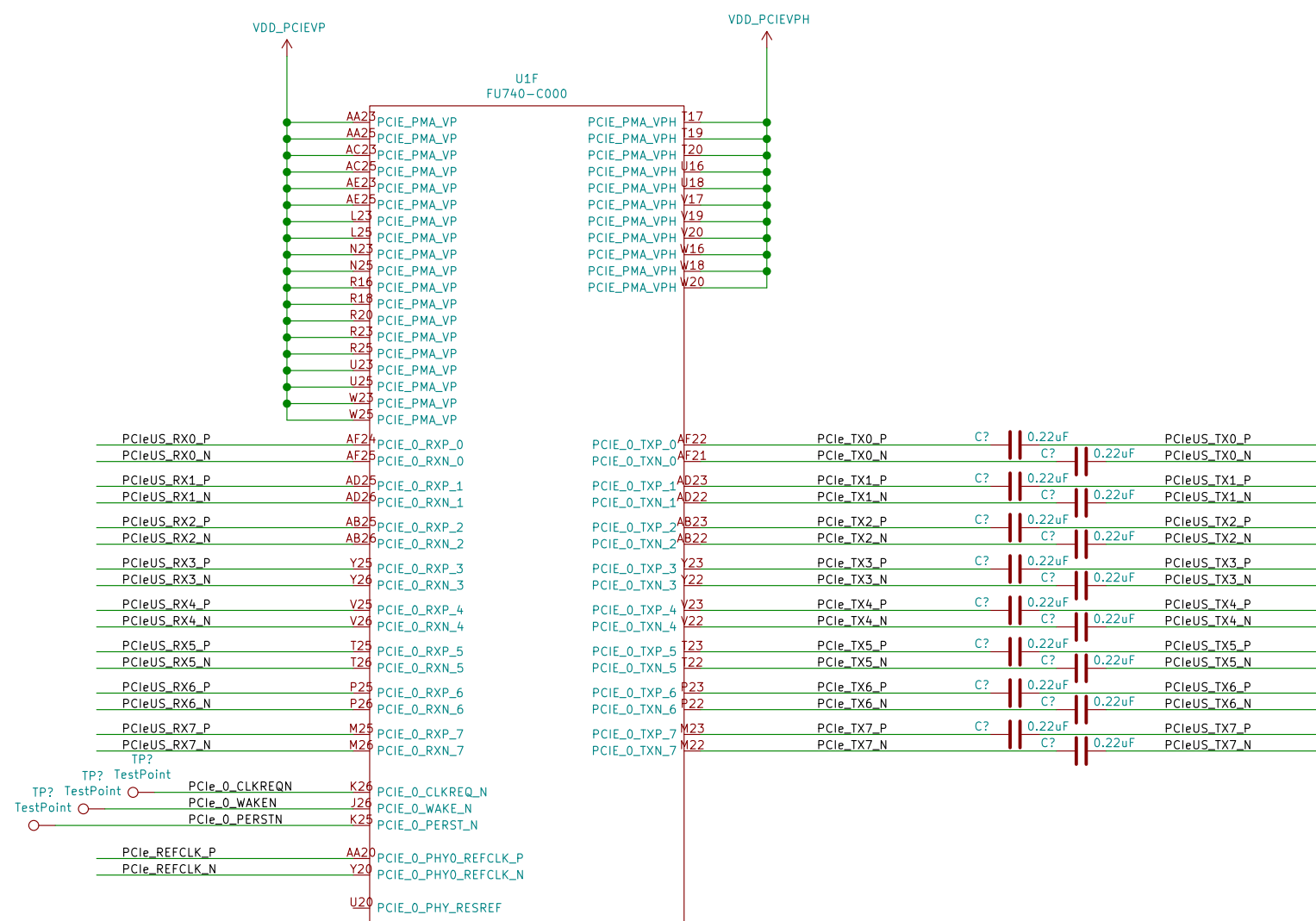
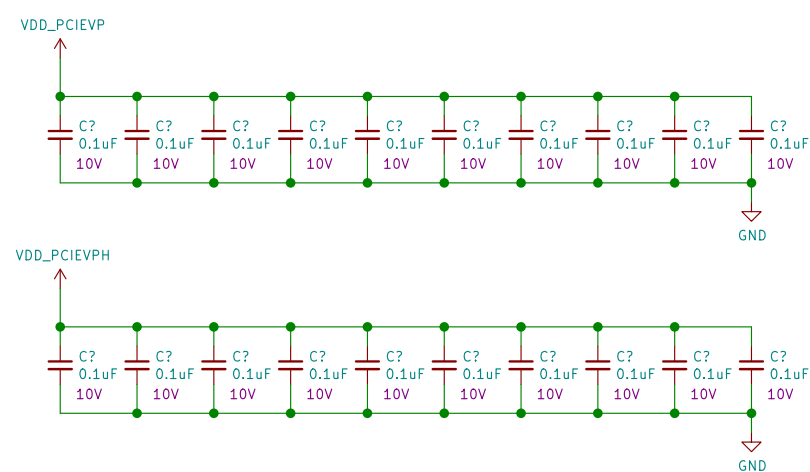
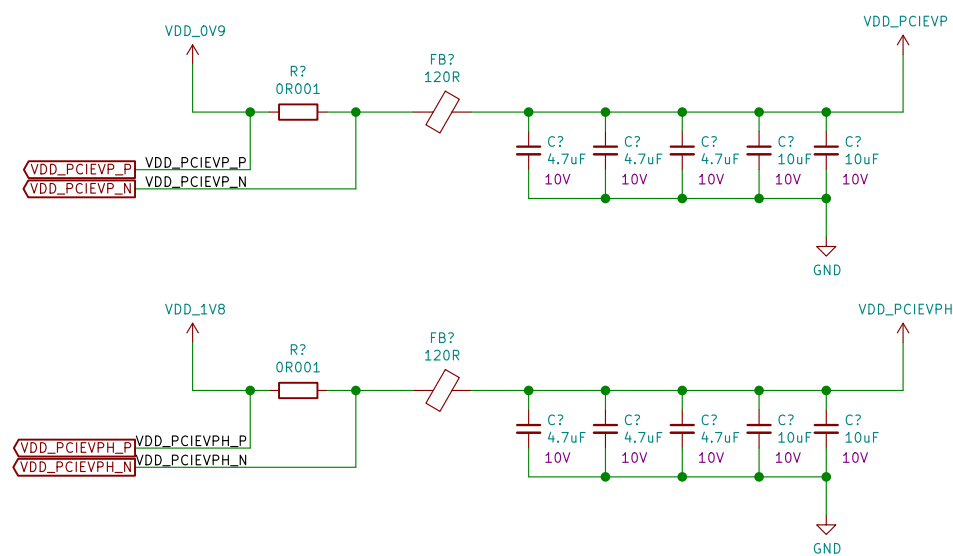
A1	DDR_VDDQ	DDR_MEM_DATA_0	A57
AA1	DDR_VDDQ	DDR_MEM_DATA_1	A62
AB1	DDR_VDDQ	DDR_MEM_DATA_2	A67
AC1	DDR_VDDQ	DDR_MEM_DATA_3	A74
AD1	DDR_VDDQ	DDR_MEM_DATA_4	V4
CE1	DDR_VDDQ	DDR_MEM_DATA_5	V1
DE1	DDR_VDDQ	DDR_MEM_DATA_6	M4
FE1	DDR_VDDQ	DDR_MEM_DATA_7	M3
CE1	DDR_VDDQ	DDR_MEM_DATA_7	V3
J7	DDR_VDDQ	DDR_MEM_DQS_P_0	V2
K4	DDR_VDDQ	DDR_MEM_DQS_M_0	V2
L1	DDR_VDDQ	DDR_MEM_DM_0	M4
M1	DDR_VDDQ	DDR_MEM_DM_0	V3
NE1	DDR_VDDQ	DDR_MEM_DATA_8	V1
P2	DDR_VDDQ	DDR_MEM_DATA_9	V2
T6	DDR_VDDQ	DDR_MEM_DATA_10	V2
U3	DDR_VDDQ	DDR_MEM_DATA_11	T4
W1	DDR_VDDQ	DDR_MEM_DATA_12	T3
Y2	DDR_VDDQ	DDR_MEM_DATA_12	T3
Y2	DDR_VDDQ	DDR_MEM_DATA_13	E4
AF1	DDR_VDDQ	DDR_MEM_DATA_14	E3
	DDR_VDDQCK	DDR_MEM_DATA_15	R1
		DDR_MEM_DQS_P_1	U2
		DDR_MEM_DQS_M_1	U1
		DDR_MEM_DM_1	T2
NE1	DDR_VDDPLL	DDR_MEM_DATA_16	A62
RE1	DDR_VDDPLL	DDR_MEM_DATA_17	A67
L13	DDR_PLL_AVDD	DDR_MEM_DATA_18	AD1
H17	DDR_PLL_AVSS	DDR_MEM_DATA_19	AC1
		DDR_MEM_DATA_20	AC4
		DDR_MEM_DATA_21	AD3
N2	DDR_MEM_ADDRESS_0	DDR_MEM_DATA_22	CE3
NE1	DDR_MEM_ADDRESS_1	DDR_MEM_DATA_23	CE3
N7	DDR_MEM_ADDRESS_2	DDR_MEM_DQS_P_2	A61
M1	DDR_MEM_ADDRESS_3	DDR_MEM_DQS_M_2	A62
MA1	DDR_MEM_ADDRESS_4	DDR_MEM_DM_2	AC3
M1	DDR_MEM_ADDRESS_5	DDR_MEM_DATA_24	A65
ME1	DDR_MEM_ADDRESS_7	DDR_MEM_DATA_25	T7
L6	DDR_MEM_ADDRESS_8	DDR_MEM_DATA_26	A66
L2	DDR_MEM_ADDRESS_9	DDR_MEM_DATA_27	A67
L3	DDR_MEM_ADDRESS_10	DDR_MEM_DATA_28	V6
M1	DDR_MEM_ADDRESS_11	DDR_MEM_DATA_29	V5
L4	DDR_MEM_ADDRESS_12	DDR_MEM_DATA_30	M6
K3	DDR_MEM_ADDRESS_13	DDR_MEM_DATA_31	M5
L7	DDR_MEM_ADDRESS_14	DDR_MEM_DQS_P_3	A65
K4	DDR_MEM_ADDRESS_15	DDR_MEM_DQS_M_3	A64
		DDR_MEM_DM_3	A67
NE1	DDR_MEM_BANK_0	DDR_MEM_DATA_32	U1
NE1	DDR_MEM_BANK_1	DDR_MEM_DATA_33	H1
NE1	DDR_MEM_BANK_2	DDR_MEM_DATA_34	H2
PE1	DDR_MEM_BANK_2	DDR_MEM_DATA_34	V3
PE1	DDR_MEM_BANK_2	DDR_MEM_DATA_35	V3
PE1	DDR_MEM_BANK_2	DDR_MEM_DATA_36	H3
PE1	DDR_MEM_BANK_2	DDR_MEM_DATA_37	E2
K3	DDR_MEM_BANK_2	DDR_MEM_DATA_38	H4
BE1	DDR_MEM_BANK_2	DDR_MEM_DATA_39	G4
BE1	DDR_MEM_BANK_2	DDR_MEM_DATA_40	G1
CE1	DDR_MEM_BANK_2	DDR_MEM_DQS_P_4	G2
AE1	DDR_MEM_BANK_2	DDR_MEM_DQS_M_4	U4
B2	DDR_MEM_BANK_2	DDR_MEM_DM_4	U4
AE1	DDR_MEM_BANK_2	DDR_MEM_DATA_41	F5
AE1	DDR_MEM_BANK_2	DDR_MEM_DATA_42	G7
		DDR_MEM_DATA_43	H6
		DDR_MEM_DATA_44	K7
A2	DDR_MEM_BANK_2	DDR_MEM_DATA_45	U5
B1	DDR_MEM_BANK_2	DDR_MEM_DATA_46	K6
P1	DDR_MEM_BANK_2	DDR_MEM_DATA_47	U6
PE1	DDR_MEM_BANK_2	DDR_MEM_DQS_P_5	G5
		DDR_MEM_DQS_M_5	G6
		DDR_MEM_DM_5	K5
		DDR_MEM_DATA_48	C7
		DDR_MEM_DATA_49	D8
		DDR_MEM_DATA_50	F8
Y6	DDR_MEM_BANK_2	DDR_MEM_DATA_51	E8
U6	DDR_MEM_BANK_2	DDR_MEM_DATA_52	F7
V7	DDR_MEM_BANK_2	DDR_MEM_DATA_53	E6
U7	DDR_MEM_BANK_2	DDR_MEM_DATA_54	E5
FE1	DDR_MEM_BANK_2	DDR_MEM_DATA_55	E7
T5	DDR_MEM_BANK_2	DDR_MEM_DQS_P_6	C6
RE1	DDR_MEM_BANK_2	DDR_MEM_DQS_M_6	D6
RE1	DDR_MEM_BANK_2	DDR_MEM_DM_6	D5
U2	DDR_MEM_BANK_2	DDR_MEM_DATA_56	C3
U2	DDR_MEM_BANK_2	DDR_MEM_DATA_57	C2
T2	DDR_MEM_BANK_2	DDR_MEM_DATA_58	D4
		DDR_MEM_DATA_59	E3
		DDR_MEM_DATA_60	D1
AC6	DDR_PLL_TESTOUT_P	DDR_MEM_DATA_61	F4
AC6	DDR_PLL_TESTOUT_N	DDR_MEM_DATA_62	E3
AF2	DDR_ATB0	DDR_MEM_DATA_63	D3
AE1	DDR_ATB1	DDR_MEM_DQS_P_7	E2
		DDR_MEM_DQS_M_7	E1
Y5	DDR_CAL_0	DDR_MEM_DM_7	K1

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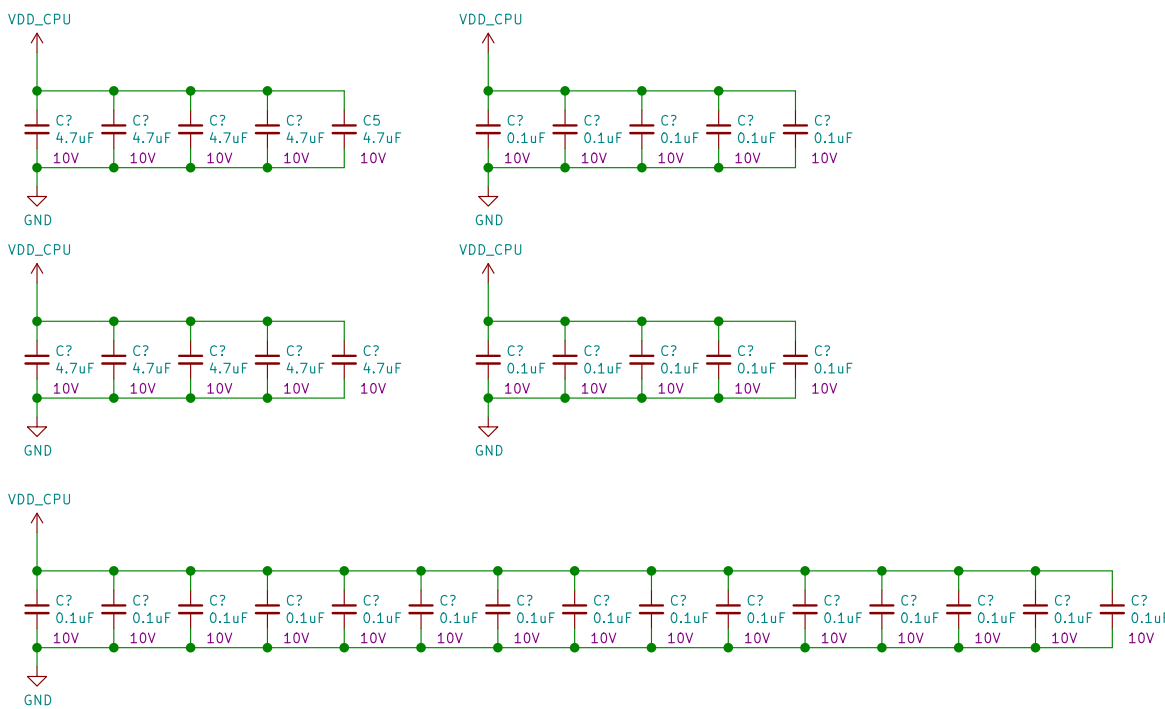
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FU740 – PCIe Gen3

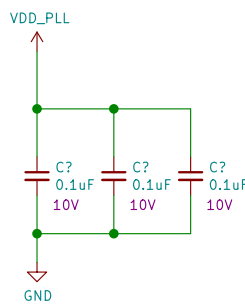


FU740 Power パワー

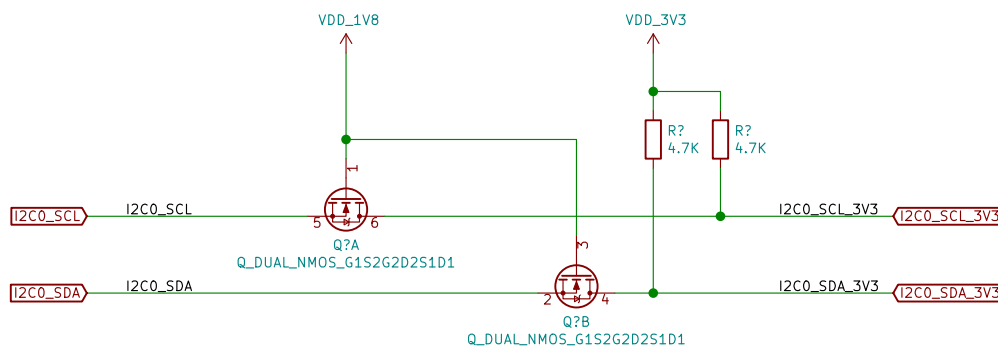
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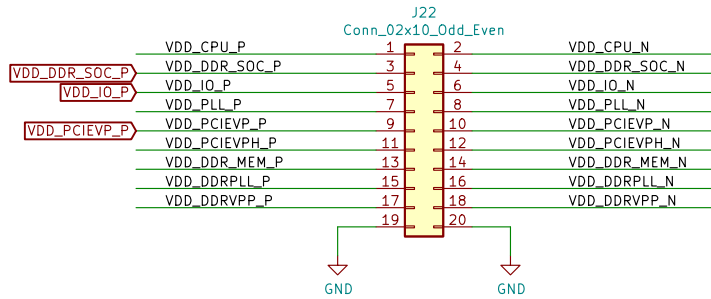
VDD_PLL bypass capacitors



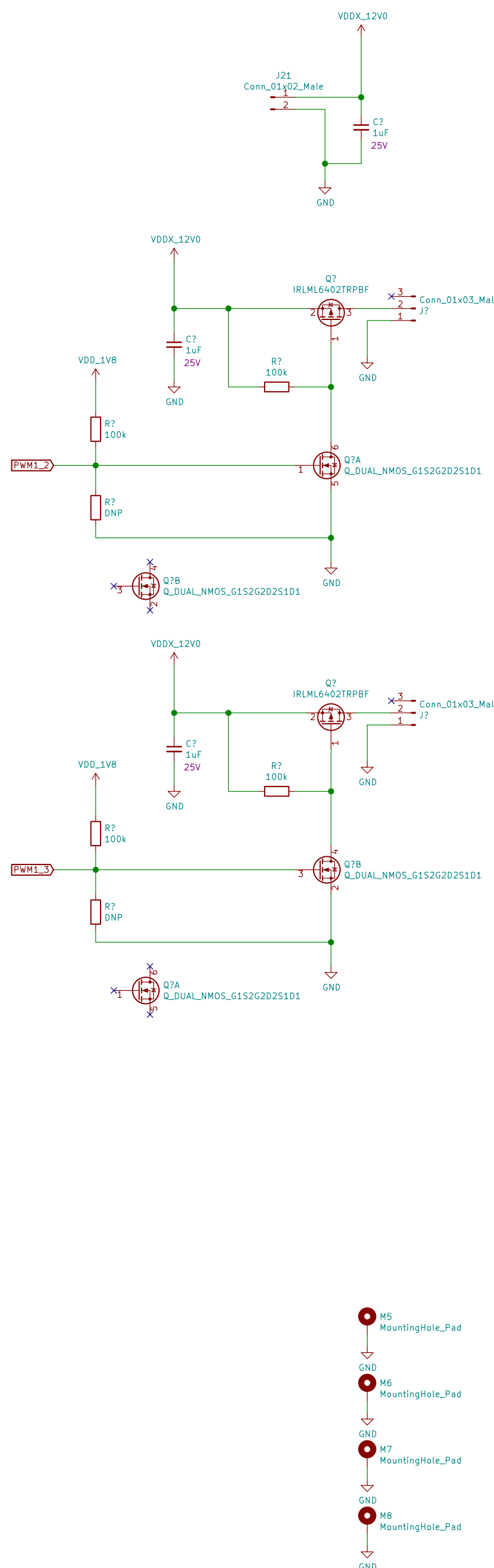
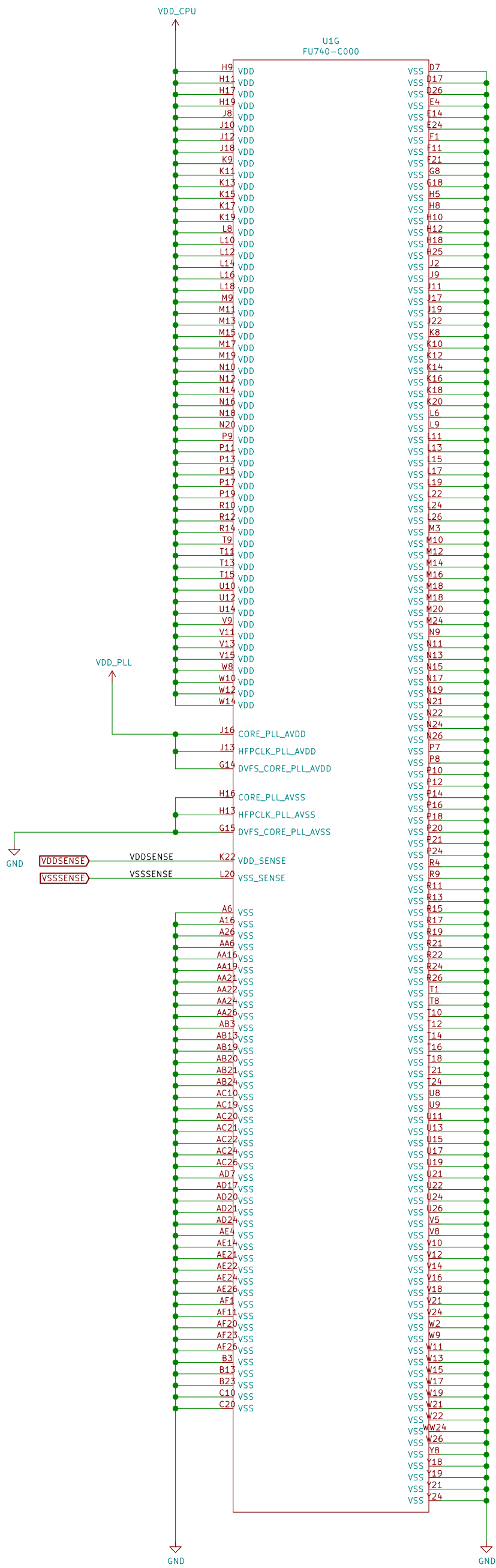
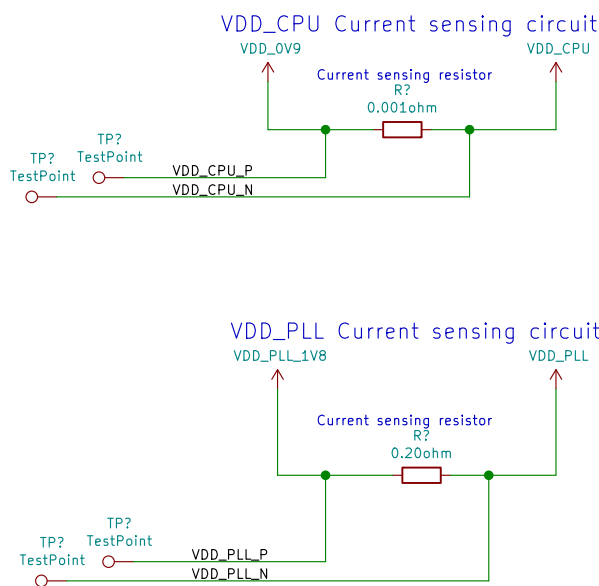
I2C_0 level shift



Power measurement header



Layout note:



DDR4 RAM メモリー

