

A1B CHIPLINK_0_B2C_DATA_1

A1G CHIPLINK_0_B2C_DATA_1

CHIPLINK_0_B2C_DATA_2

A1G CHIPLINK_0_B2C_DATA_2

A1G CHIPLINK_0_B2C_DATA_2

A1G CHIPLINK_0_B2C_DATA_3

A1G CHIPLINK_0_B2C_DATA_3

A1G CHIPLINK_0_B2C_DATA_3

A1G CHIPLINK_0_B2C_DATA_4

A1G CHIPLINK_0_B2C_DATA_4

A1G CHIPLINK_0_B2C_DATA_5

A1G CHIPLINK_0_B2C_DATA_6

A1G CHIPLINK_0_B2C_DATA_6

A1G CHIPLINK_0_B2C_DATA_6

A1G CHIPLINK_0_B2C_DATA_6

A1G CHIPLINK_0_B2C_DATA_7

A1G CHIPLINK_0_B2C_DATA_7

A1G CHIPLINK_0_B2C_DATA_9

Y16 CHIPLINK_0_B2C_DATA_10

A1G CHIPLINK_0_B2C_DATA_11

A1G CHIPLINK_0_B2C_DATA_12

Y15 CHIPLINK_0_B2C_DATA_12

Y15 CHIPLINK_0_B2C_DATA_12

CHIPLINK_0_C2B_DATA_13

A1G CHIPLINK_0_B2C_DATA_14

A1G CHIPLINK_0_B2C_DATA_15

A1G CHIPLINK_0_B2C_DATA_16

A1G CHIPLINK_0_B2C_DATA_16

A1G CHIPLINK_0_B2C_DATA_16

A1G CHIPLINK_0_B2C_DATA_16

A1G CHIPLINK_0_B2C_DATA_16

A1G CHIPLINK_0_B2C_DATA_16

A1G CHIPLINK_0_B2C_DATA_17

A1G CHIPLINK_0_B2C_DATA_16

A1G CHIPLINK_0_B2C_DATA_17

A1G CHIPLINK_0_B2C_DATA_16

A1G CHIPLINK_0_B2C_DATA_17

A1G CHIPLINK_0_B2C_DATA_17

A1G CHIPLINK_0_B2C_DATA_19

A1G CHIPLINK_0_B2C_DATA_20

A1G CHIPLINK_0_B2C_DATA_21

A1G CHIPLINK_0_B2C_DATA_21

A1G CHIPLINK_0_B2C_DATA_21

A1G CHIPLINK_0_B2C_DATA_21

A1G CHIPLINK_0_B2C_DATA_22

A1G CHIPLINK_0_B2C_DATA_22

A1G CHIPLINK_0_B2C_DATA_23

A1G CHIPLINK_0_B2C_DATA_23

A1G CHIPLINK_0_B2C_DATA_24

A1G CHIPLINK_0_B2C_DATA_25

A1G CHIPLINK_0_B2C_DATA_26

A1G CHIPLINK_0_B2C_DATA_27

A1G CHIPLINK_0_B2C_DATA_29

A1G CHIPLINK_0_C2B_DATA_29

A1G CHIPLINK_0_B2C_DATA_29

A1G CHIPLINK_0_C2B_DATA_29

A1G CHIPLINK_0_C2B_DATA_29

U1B FU740-C000 DDR_MEM_DATA_0 A3
DDR_MEM_DATA_1 A42
DDR_MEM_DATA_2 A44
DDR_MEM_DATA_3 A81
DDR_MEM_DATA_5 Y1
DDR_MEM_DATA_5 W1
DDR_MEM_DATA_6 W1
DDR_MEM_DATA_7 W3
DDR_MEM_DATA_7 Y3
DDR_MEM_DQS_M_0 Y2
DDR_MEM_DQS_M_0 W4 A1 DDR_VDDQ
AA1 DDR_VDDQ
AB6 DDR_VDDQ
AC5 DDR_VDDQ
AD2 DDR_VDDQ
DDR_VDDQ
DDR_VDDQ
GS DDR_VDDQ
J7 DDR_VDDQ
J7 DDR_VDDQ
L1 DDR_VDDQ
L1 DDR_VDDQ
DDR_VDDQ
L1 DDR_VDDQ
DDR_VDDQ
US DDR_VDDQ
US DDR_VDDQ
DDR_VDDQ
DDR_VDDQ
DDR_VDDQ
DDR_VDDQ
US DDR_VDDQ DDR_MEM_DATA_8 V3
DDR_MEM_DATA_9 V1
DDR_MEM_DATA_10 V2
DDR_MEM_DATA_11 T3
DDR_MEM_DATA_12 R2
DDR_MEM_DATA_13 R3
DDR_MEM_DATA_14 R3
DDR_MEM_DATA_15 R1
DDR_MEM_DAS_P_1 U2
DDR_MEM_DOS_P_1 U1 DDR_MEM_DQS_M_1 T2 N8 DDR_VDDPLL DDR_VDDPLL DDR_MEM_DM.1 142
DDR_MEM_DATA_17 AB2
DDR_MEM_DATA_17 AB1
DDR_MEM_DATA_18 AC1
DDR_MEM_DATA_21 AC2
DDR_MEM_DATA_21 AC3
DDR_MEM_DATA_21 AD3
DDR_MEM_DATA_22 AE3
DDR_MEM_DATA_22 AE3
DDR_MEM_DATA_23 AE3
DDR_MEM_DATA_22 AE2
DDR_MEM_DATA_23 AE3 J15 DRR_PLL_AVDD H15 DRR_PLL_AVSS N2
N1
DDR_MEM_ADDRESS_0
N1
DDR_MEM_ADDRESS_1
N7
DDR_MEM_ADDRESS_2
M1
DDR_MEM_ADDRESS_3
M2
DDR_MEM_ADDRESS_4
DDR_MEM_ADDRESS_5
M3
DDR_MEM_ADDRESS_7
L3
DDR_MEM_ADDRESS_8
L3
DDR_MEM_ADDRESS_9
L3
DDR_MEM_ADDRESS_10
M7
DDR_MEM_ADDRESS_11
DDR_MEM_ADDRESS_11
DDR_MEM_ADDRESS_11
DDR_MEM_ADDRESS_13
DDR_MEM_ADDRESS_13
DDR_MEM_ADDRESS_13
DDR_MEM_ADDRESS_14
DDR_MEM_ADDRESS_15 DDR_MEM_DML_24

DDR_MEM_DATA_25

DDR_MEM_DATA_25

AA5

DDR_MEM_DATA_26

AA7

DDR_MEM_DATA_29

DDR_MEM_DATA_29

DDR_MEM_DATA_30

DDR_MEM_DATA_30

DDR_MEM_DATA_31

DDR_MEM_DATA_31

DDR_MEM_DQS_P_3

DDR_MEM_DQS_P_3

DDR_MEM_DDR_M_3

DDR_MEM_DM_3

DR_MEM_DM_3

DR_MEM_DM_3

DR_MEM_DM_3

DR_MEM_DM_3

DR_MEM_DM_3

DR_MEM_DM_3

DR_MEM_DM_3

DR_MEM_DM_3

DR_MEM_DM_3 N4 DDR_MEM_BANK_0
N3 DDR_MEM_BANK_1
N6 DRR_MEM_BANK_2
P5 DRR_MEM_CAS_N
P6 DRR_MEM_CAS_N
P1 DRR_MEM_CAS_N
P1 DRR_MEM_PARITY_IN
DRR_MEM_DARITY_IN
DRR_MEM_DOT_0
C4 DRR_MEM_ODT_0
DRR_MEM_ODT_0
DRR_MEM_CS_N_0
DRR_MEM_CS_N_0
DRR_MEM_CS_N_1
A3 DRR_MEM_RESET_N DDR_MEM_DATA_32 DDR_MEM_DATA_32 J1
DDR_MEM_DATA_33 H1
DDR_MEM_DATA_34 H2
DDR_MEM_DATA_35 H3
DDR_MEM_DATA_36 H3
DDR_MEM_DATA_37 F2
DDR_MEM_DATA_37 F2
DDR_MEM_DATA_39 G4
DDR_MEM_DATA_39 G4
DDR_MEM_DATA_39 G4
DDR_MEM_DATA_39 G4
DDR_MEM_DATA_39 G4
DDR_MEM_DATA_39 G4 DDR_MEM_DM_4 DDR_MEM_DATA_40 F5 DDR_MEM_DATA_41 G7 DDR_MEM_DATA_42 H7 DDR_MEM_DATA_42 H/2
DDR_MEM_DATA_43 H/3
DDR_MEM_DATA_44 K/2
DDR_MEM_DATA_45 J/3
DDR_MEM_DATA_46 J/3
DDR_MEM_DATA_47 J/3
DDR_MEM_DATA_47 J/3
DDR_MEM_DATA_47 J/3
DDR_MEM_DATA_47 J/3
DDR_MEM_DAS_P_5 J/3
DDR_MEM_DAS_P_5 J/3 A2 B1 DRR_MEM_CKE_0 DRR_MEM_CKE_1 P3 DRR_MEM_CLK DRR_MEM_CLK_N DDR_MEM_DATA_58

DDR_MEM_DATA_49

DDR_MEM_DATA_50

E8

DDR_MEM_DATA_51

E8

DDR_MEM_DATA_52

E7

DDR_MEM_DATA_53

E6

DDR_MEM_DATA_54

E7

DDR_MEM_DATA_54

E7

DDR_MEM_DATA_56

E7

DDR_MEM_DATA_56

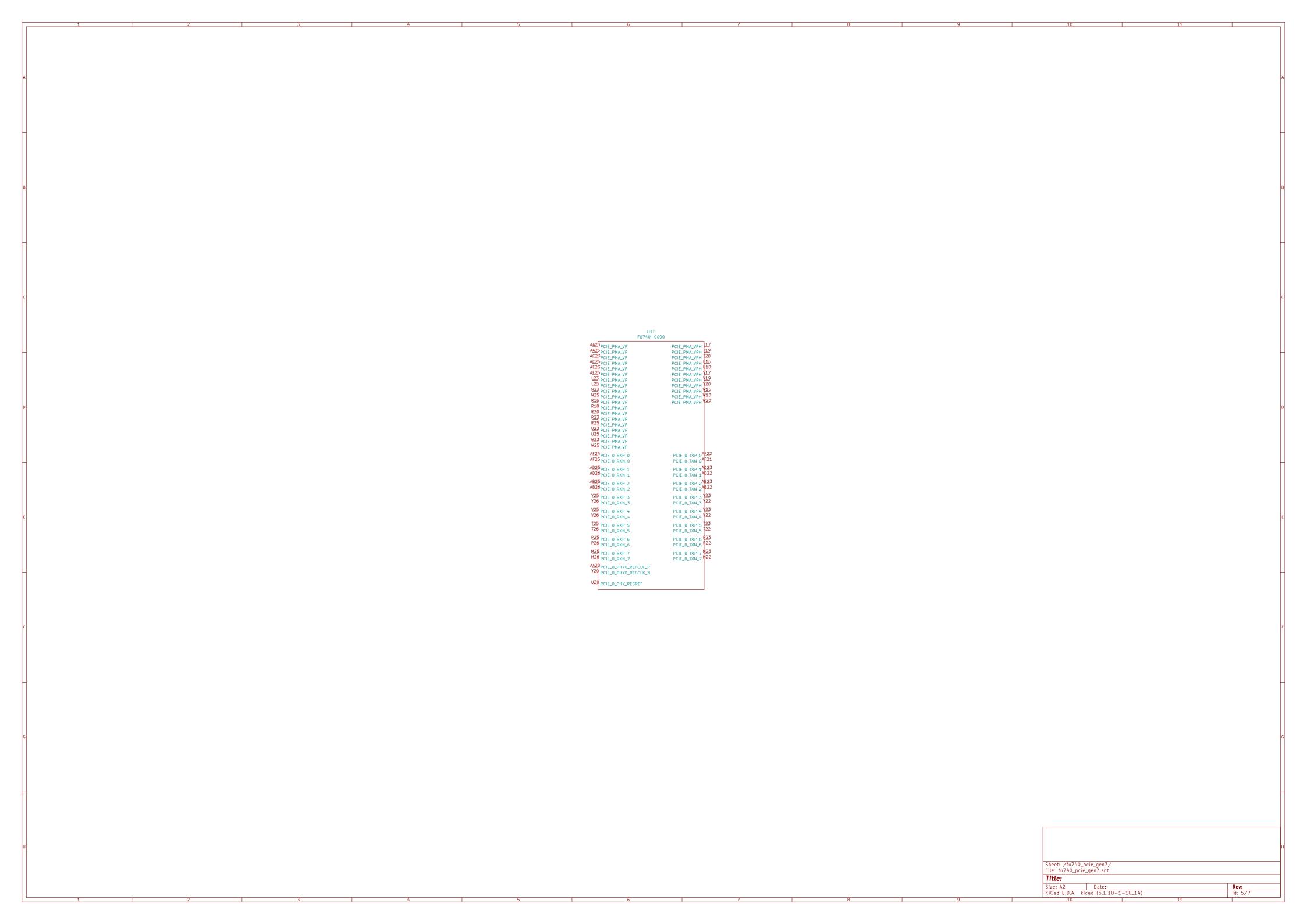
E7

DDR_MEM_DASA_66

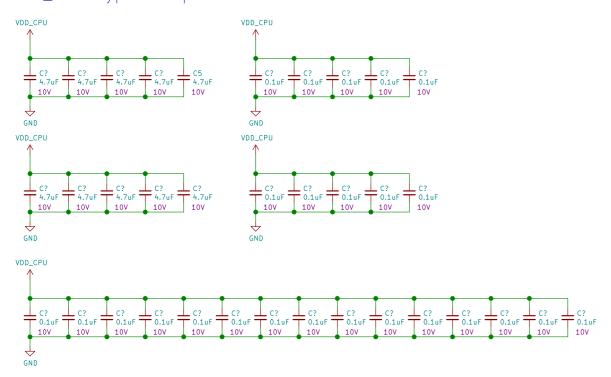
DSS_ DRR_MEM_ECC_DATA_0
U6 DRR.MEM_ECC_DATA_1
V7 DRR_MEM_ECC_DATA_2
U7 DRR_MEM_ECC_DATA_3
R7 DRR_MEM_ECC_DATA_4
I5 DRR_MEM_ECC_DATA_5
R6 DRR_MEM_ECC_DATA_6
R5 DRR_MEM_ECC_DATA_7
U4 DRR_MEM_ECC_DATA_7
U5 DRR_MEM_ECC_DAS_P
U5 DRR_MEM_ECC_DQS_M
DRR_MEM_ECC_DM DDR_MEM_DATA_56 C3
DDR_MEM_DATA_57 C2
DDR_MEM_DATA_58 D4
DDR_MEM_DATA_59 E3
DDR_MEM_DATA_60 D1
DR_MEM_DATA_60 F4 AC6 DDR_PLL_TESTOUT_P AC7 DRR_PLL_TESTOUT_N AF2 DRR_ATB0
AF4 DRR_ATB1
Y9 DRR_CAL_0

U1D FU740-C000 A11 IVDD
A11 IVDD
A21 IVDD
A22 IVDD
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A24 IVDD
A25 IVDD
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A27 IVDD
A28 IVDD
A29 UART_O_TXD 114
GPIO_O_PINS_RXD 415
SPI_O_DC_0 613
SPI_O_DQ_1 613
SPI_O_DQ_2 613
SPI_O_DQ_2 613
SPI_O_DQ_3 612
SPI_O_DQ_3 612
SPI_O_DQ_0 610
QSPI_O_DQ_0 610
QSPI_O_DQ_1 610
QSPI_O_DQ_1 610
QSPI_O_DQ_1 610
QSPI_O_DQ_1 610
QSPI_O_DQ_1 610
QSPI_O_DQ_1 610
QSPI_O_CS_0 69 QSPLO_CS_0 EY
PWM_0_PWM_0 PWM_0 PWM_0 PWM_1 A8
PWM_0_PWM_3 F9
PWM_1_PWM_1 B9
PWM_1_PWM_2 610
PWM_1_PWM_3 A9
JTAG_TDL B7
JTAG_TDL B7
JTAG_TDL G7
JTAG_TDS G11 A14 | I2C_0_SCL A13 | I2C_0_SDA D13 | I2C_1_SCL E13 | I2C_1_SDA

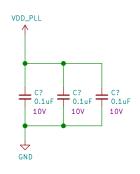
BOOT_MODE_SELECT_0 816 BOOT_MODE_SELECT_1 115 BOOT_MODE_SELECT_2 114 BOOT_MODE_SELECT_3 116 CHIP_ID_SELECT_0 115 PRCI_HFXOSCIN
A17 PRCI_HFXOSCOUT PRCLHFXOSCOUT
D18 PRCLHFXCLKIN
B19 PRCLERESET_N
C18 PRCL_PORESET_N
G16 PRCL_PSD_ASYNCRESET_N
E15 PRCL_PSD_TEST_MODE_RESET
C17 PRCL_PSD_RESETEN_N PRCI_CORECLKPLLSEL 119
PRCI_TLCLKSEL 117
PRCI_HFXSEL 117
PRCI_RTCXSEL 115 THERMALDIODE_ANODE B6
THERMALDIODE_CATHODE PRCI_PROCMON 16

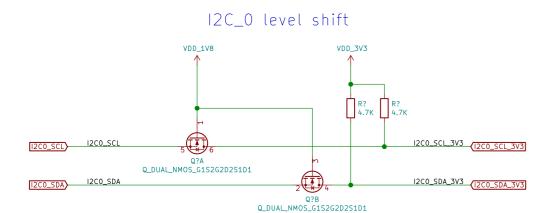


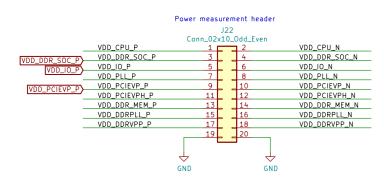
VDD_CPU bypass capacitors



VDD_PLL bypass capacitors

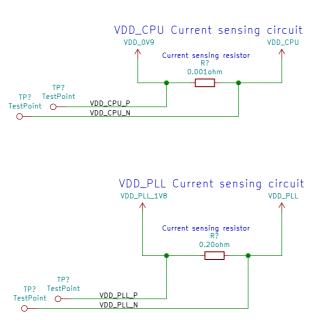


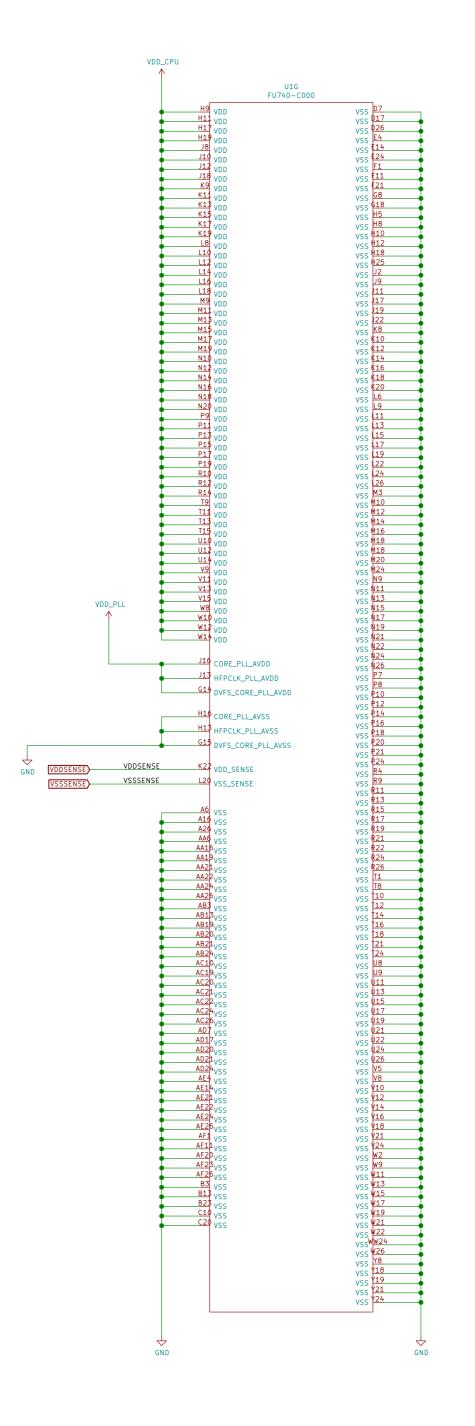


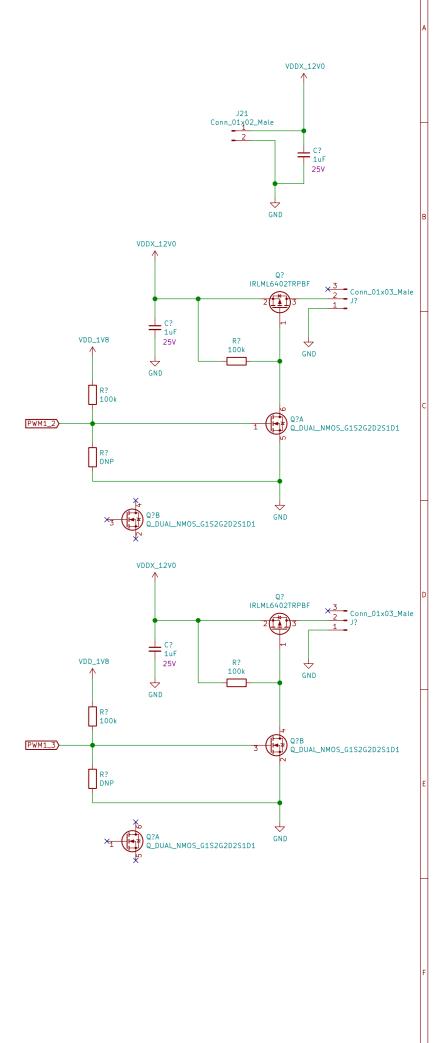


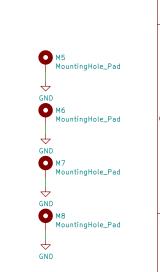
Layout note:

FU740 Power









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