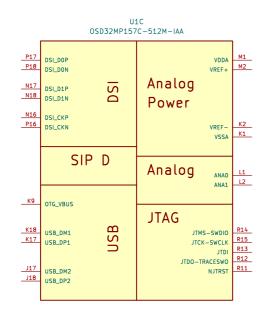
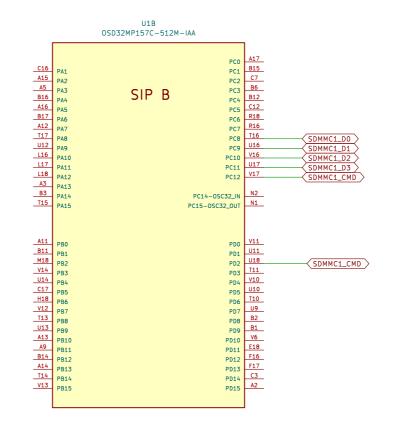
Linux based scout UAV LinuxベースのスカウトUAV

Sheetname: osd32mp1-1-power File: osd32mp1-1-power.kicad_sch Sheetname: osd32mp1-2 File: osd32mp1-2.kicad_sch Sheetname: osd32mp1-3 File: osd32mp1-3.kicad_sch Sheetname: battery-power File: battery-power.kicad_sch Sheetname: power-section-1 File: power-section-1.kicad_sch storage File: storage.kicad_sch long-range-radio-1File: long-range-radio-1.kicad_sch quectel-m65-1 File: quectel-m65-1.kicad_sch File: gnss kicad_sch File: sensors-1.kicad_sch

OSD32MP1 power +VIN U1E OSD32MP157C-512M-IAA +3V3 U1A OSD32MP157C-512M-IAA VSS_36 N11 VSS_37 E12 PMIC_VOUT4_: E4 VSS_2 VSS_3 E5 VSS_4 VSS_5 G5 VSS_5 SIP E PMIC_VOUT4_2 VSS_38 VSS_39 F12 G12 VIN_2 G2 VIN_3 +BST G5 H5 VSS_6 VSS_7 VSS_41 N12 Power Inputs PMIC_BSTOUT_1 VSS_43 VSS_44 F13 G13 VSS_8 VSS_9 PMIC_BSTOUT_2 G13 Power Outputs VSS_45 VSS_46 J13 PMIC_BSTIN_1 +VSW M8 PMIC_BSTIN_2
PMIC_BSTIN_3 connect VSS 3 per pad. VSS_47 K13 M9 PMIC_BSTIN_4 VSS_48 VSS_49 M13 P5 E6 F6 PMIC SWOUT PMIC_SWOUT_2 VSS_50 N13 VSS_15 VSS_16 N6 P6 E7 F7 VSS_51 VSS_52 VSS_53 F14 G14 VSS_17 VSS_18 PMIC_SWIN_1 H9 PMIC_SWIN_2 NOTE: Can use pours to pads instead of a via VSS_19 VSS_20 VSS_54 PMIC_VBUSOTG K8 VSS_55 J14 N7 P7 E8 VSS_22 VSS_23 VSS_57 F8 VSS_58 M14 PMIC_LD025IN NB PB VSS_59 VSS_60 E15 VSS_24 VSS_25 E9 VSS_61 VSS_62 F15 G15 +VLD02 VSS_26 VSS_27 F9 VSS_27 VSS_28 VSS_63 H15 E10 VSS_29 VSS_30 VSS_64 PMIC_LD02 SIP A VSS_65 N10 VSS_30 VSS_31 VSS_32 VSS_33 VSS_34 VSS_35 VSS_66 VSS_67 VSS_68 N15 VSS_69 J16 PMIC_LD05 VSS_70 K16 PONKEY PMIC_PONKEYN PMIC_LD06 VDDI М3 NRST GND Internal Use Only VDD_1 VDD_2 Connect VDD_3 Pull down to enable Together Can be used VDD_5 HSE_OSC_OEN for boot config Internal Use Only VDD_7 VDD_8 GND to program EEPROM Connect VDD_9 Together P4 EEPROM_WP Connect to VDD К3 VBAT VDDI if unused SIP F Connect to GND Boot SW1 SW_DIP_x04 B00T1 B00T2 BYPASS_REG1V8 Config Power Rails R7 10K R5
R8
VDD3V3_USB
VDDA1V1_REG
VDDA1V8_REG
VDD1V2_DSI_REG Do Not Use B3U-1000P EEPROM_W Test Point per RESET signal recommended J2 HSE_OSC_TP OSD32MP157C-512M-IAA Processor Control GND L3 PAO/PMIC_INTN D6 PC13/PMIC_WAKEUP
PWR_ON/PMIC_PWRCTRL K6
M6
L6
PWR_LP
PDR_ON
PDR_ON_CORE Do Not Use Test Point per N4 NRST_CORE signal recommended Sheet: /osd32mp1-1-power/ File: osd32mp1-1-power.kicad_sch Title: Size: A3 Date: KiCad E.D.A. 8.0.3

0SD32MP1





D7
D8
RSVD_1
RSVD_2
P9
RSVD_3
RSVD_4
RSVD_5
RSVD_6
RSVD_6
RSVD_6
RSVD_7
R10
H12
RSVD_9
RSVD_1
RSVD_9
RSVD_10
RSVD_1
RSVD_1
RSVD_1
RSVD_9
RSVD_10
RSVD_1
RSVD

U1G OSD32MP157C-512M-IAA

 Sheet: /osd32mp1-2/

 File: osd32mp1-2.kicad_sch

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 Size: A3
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 KiCad E.D.A. 8.0.3
 Id: 3/11

05D32MP1 U1D OSD32MP157C-512M-IAA SIP C PH2 B13 C13 PH4 U6 PH5 T6 PH6 B5 PH7 C5 PH8 T4 U3 PH10 U2 PH11 U2 PH11 T1 PH15 T2 PIO T3
PI1 R1
PI2 R2
PI3 R3
PI4 P1
PI5 P2
PI6 P3
PI7 N3
PI8 C1
PI9 C2
PI10 B4
PI11 C4 V8 PF0
V8 PF1
T18 PF2
C6 PF3
V7 PF4
B PF5
F18 PF5
G16 G16
G17
G18
PF7
G18
B18 PF1
C11 PF12
A10 PF13
B10 PF14
C10 PF15 C8 PG0

B8 PG1

A8 PG2

C9 PG3

B9 PG4

C15 PG5

H17 PG7

D16 PG8

M17 PG9

C14 PG11

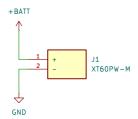
A4 PG12

B7 PG14

V9 PG15 Sheet: /osd32mp1-3/ File: osd32mp1-3.kicad_sch Title: Size: A3 Date: KiCad E.D.A. 8.0.3 Rev: Id: 4/11

Battery Power バッテリー電源

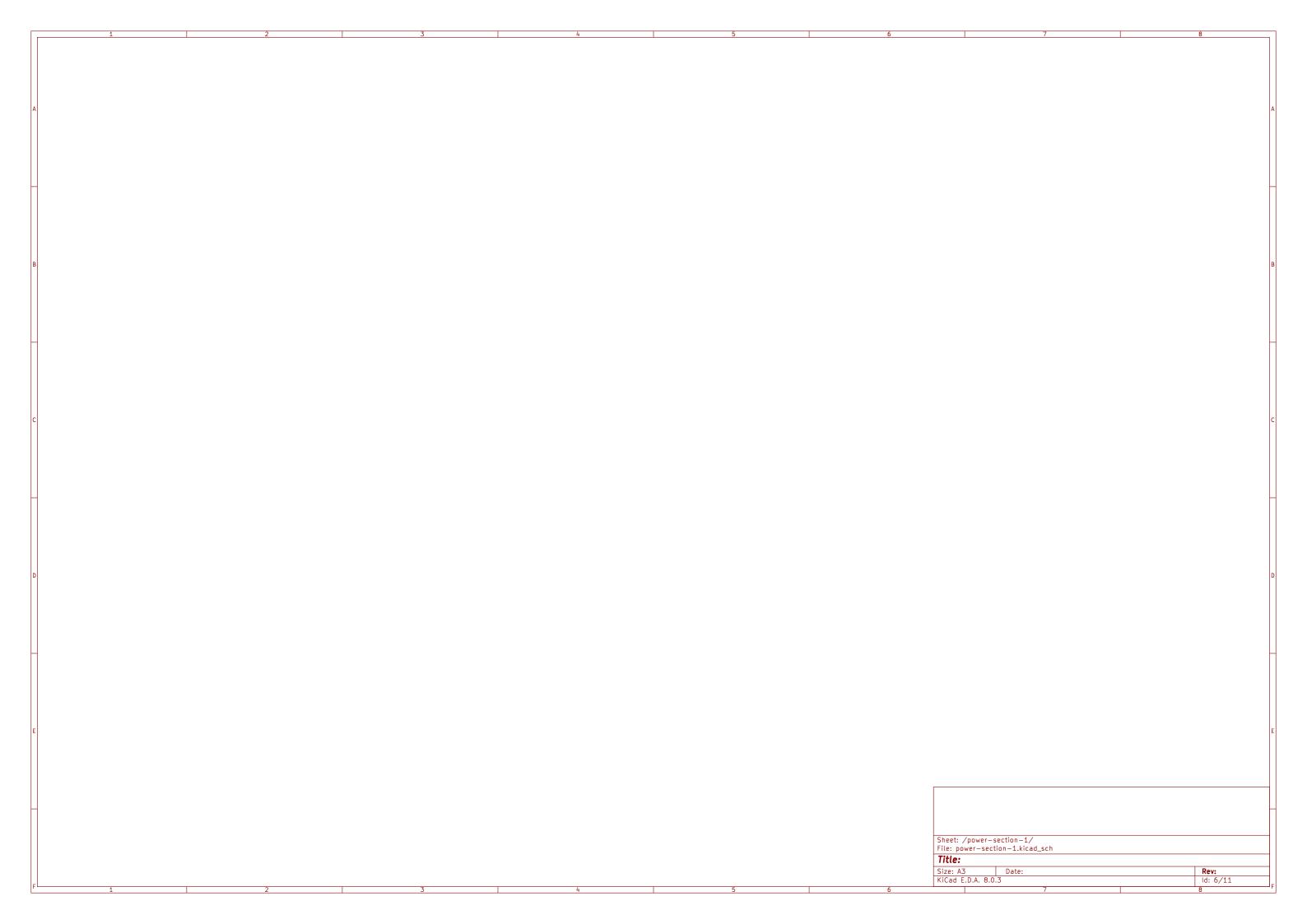
2S-6S LiPo battery





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Storage 財産

