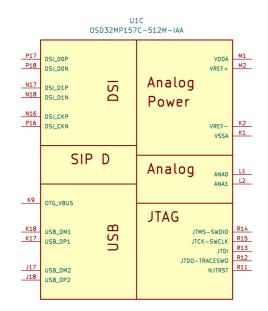
## Linux based scout UAV LinuxベースのスカウトUAV

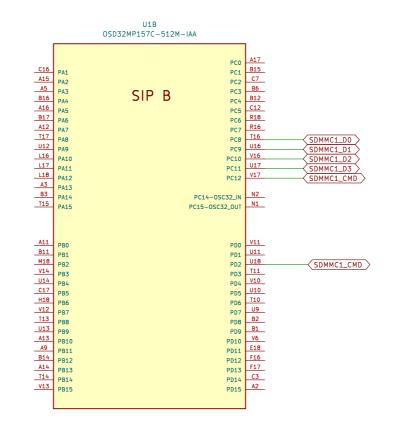
Sheetname: osd32mp1-1-power File: osd32mp1-1-power.kicad\_sch Sheetname: osd32mp1-2 File: osd32mp1-2.kicad\_sch Sheetname: osd32mp1-3 File: osd32mp1-3.kicad\_sch Sheetname: battery-power File: battery-power.kicad\_sch Sheetname: power-section-1 File: power-section-1.kicad\_sch storage File: storage.kicad\_sch long-range-radio-1 File: long-range-radio-1.kicad\_sch quectel-m65-1 File: quectel-m65-1.kicad\_sch File: gnss.kicad\_sch

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OSD32MP1 power +VIN U1E OSD32MP157C-512M-IAA +3V3 U1A OSD32MP157C-512M-IAA VSS\_36 N11 VSS\_37 E12 PMIC\_VOUT4\_: E4 VSS\_2 VSS\_3 E5 VSS\_4 VSS\_5 G5 VSS\_5 SIP E PMIC\_VOUT4\_2 VSS\_38 VSS\_39 F12 G12 VIN\_2 G2 VIN\_3 +BST G5 H5 VSS\_6 VSS\_7 VSS\_41 N12 Power Inputs PMIC\_BSTOUT\_1 VSS\_43 VSS\_44 F13 G13 VSS\_8 VSS\_9 PMIC\_BSTOUT\_2 G13 Power Outputs VSS\_45 VSS\_46 J13 PMIC\_BSTIN\_1 +VSW M8 PMIC\_BSTIN\_2
PMIC\_BSTIN\_3 connect VSS 3 per pad. VSS\_47 K13 M9 PMIC\_BSTIN\_4 VSS\_48 VSS\_49 M13 P5 E6 F6 PMIC SWOUT PMIC\_SWOUT\_2 VSS\_50 N13 VSS\_15 VSS\_16 N6 P6 E7 F7 VSS\_51 VSS\_52 VSS\_53 F14 G14 VSS\_17 VSS\_18 PMIC\_SWIN\_1 H9 PMIC\_SWIN\_2 NOTE: Can use pours to pads instead of a via VSS\_19 VSS\_20 VSS\_54 PMIC\_VBUSOTG K8 VSS\_55 J14 N7 P7 E8 VSS\_22 VSS\_23 VSS\_57 F8 VSS\_58 M14 PMIC\_LD025IN NB PB VSS\_59 VSS\_60 E15 VSS\_24 VSS\_25 E9 VSS\_61 VSS\_62 F15 G15 +VLD02 VSS\_26 VSS\_27 F9 VSS\_27 VSS\_28 VSS\_63 H15 E10 VSS\_29 VSS\_30 VSS\_64 PMIC\_LD02 SIP A VSS\_65 N10 VSS\_30 VSS\_31 VSS\_32 VSS\_33 VSS\_34 VSS\_35 VSS\_66 VSS\_67 VSS\_68 N15 VSS\_69 J16 PMIC\_LD05 VSS\_70 K16 PONKEY PMIC\_PONKEYN PMIC\_LD06 VDDI М3 NRST GND Internal Use Only VDD\_1 VDD\_2 Connect VDD\_3 Pull down to enable Together Can be used VDD\_5 HSE\_OSC\_OEN for boot config Internal Use Only VDD\_7 VDD\_8 GND to program EEPROM Connect VDD\_9 Together P4 EEPROM\_WP Connect to VDD К3 VBAT VDDI if unused SIP F Connect to GND Boot SW1 SW\_DIP\_x04 B00T1 B00T2 BYPASS\_REG1V8 Config Power Rails R7 10K R5
R8
VDD3V3\_USB
VDDA1V1\_REG
VDDA1V8\_REG
VDD1V2\_DSI\_REG Do Not Use B3U-1000P EEPROM\_W Test Point per RESET signal recommended J2 HSE\_OSC\_TP OSD32MP157C-512M-IAA Processor Control GND L3 PAO/PMIC\_INTN D6 PC13/PMIC\_WAKEUP
PWR\_ON/PMIC\_PWRCTRL K6
M6
L6
PWR\_LP
PDR\_ON
PDR\_ON\_CORE Do Not Use Test Point per NA NRST\_CORE signal recommended Sheet: /osd32mp1-1-power/ File: osd32mp1-1-power.kicad\_sch Title: Size: A3 Date: KiCad E.D.A. 8.0.3

# 0SD32MP1





D7
D8
RSVD\_1
RSVD\_2
P9
RSVD\_3
RSVD\_4
RSVD\_5
RSVD\_6
RSVD\_6
RSVD\_6
RSVD\_7
R10
H12
RSVD\_9
RSVD\_1
RSVD\_9
RSVD\_10
RSVD\_1
RSVD\_1
RSVD\_1
RSVD\_9
RSVD\_10
RSVD\_1
RSVD

U1G OSD32MP157C-512M-IAA

05D32MP1 U1D OSD32MP157C-512M-IAA SIP C PH2 B13 C13 PH4 U6 PH5 T6 PH6 B5 PH7 C5 PH8 T4 U3 PH10 U2 PH11 U2 PH11 T1 PH15 T2 PIO T3
PI1 R1
PI2 R2
PI3 R3
PI4 P1
PI5 P2
PI6 P3
PI7 N3
PI8 C1
PI9 C2
PI10 B4
PI11 C4 V8 PF0
V8 PF1
T18 PF2
C6 PF3
V7 PF4
B PF5
F18 PF5
G16 G16
G17
G18
PF7
G18
B18 PF1
C11 PF12
A10 PF13
B10 PF14
C10 PF15 C8 PG0

B8 PG1

A8 PG2

C9 PG3

B9 PG4

C15 PG5

H17 PG7

D16 PG8

M17 PG9

C14 PG11

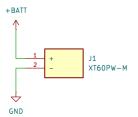
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B7 PG14

V9 PG15 Sheet: /osd32mp1-3/ File: osd32mp1-3.kicad\_sch Title: Size: A3 Date: KiCad E.D.A. 8.0.3 Rev: Id: 4/10

### Battery Power バッテリー電源

#### 2S-6S LiPo battery

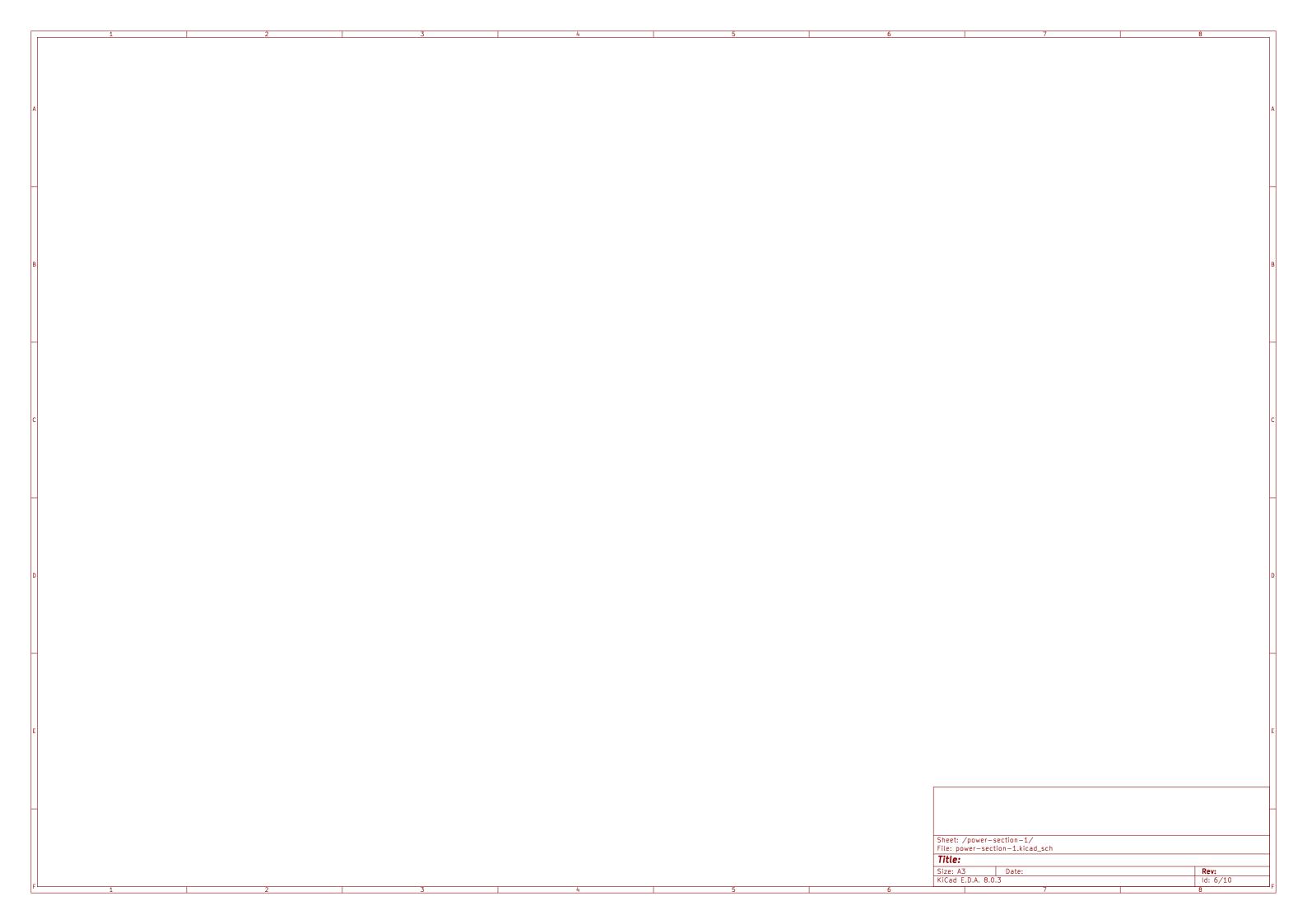




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# Storage 財産

