+5V

+3.3V

P1

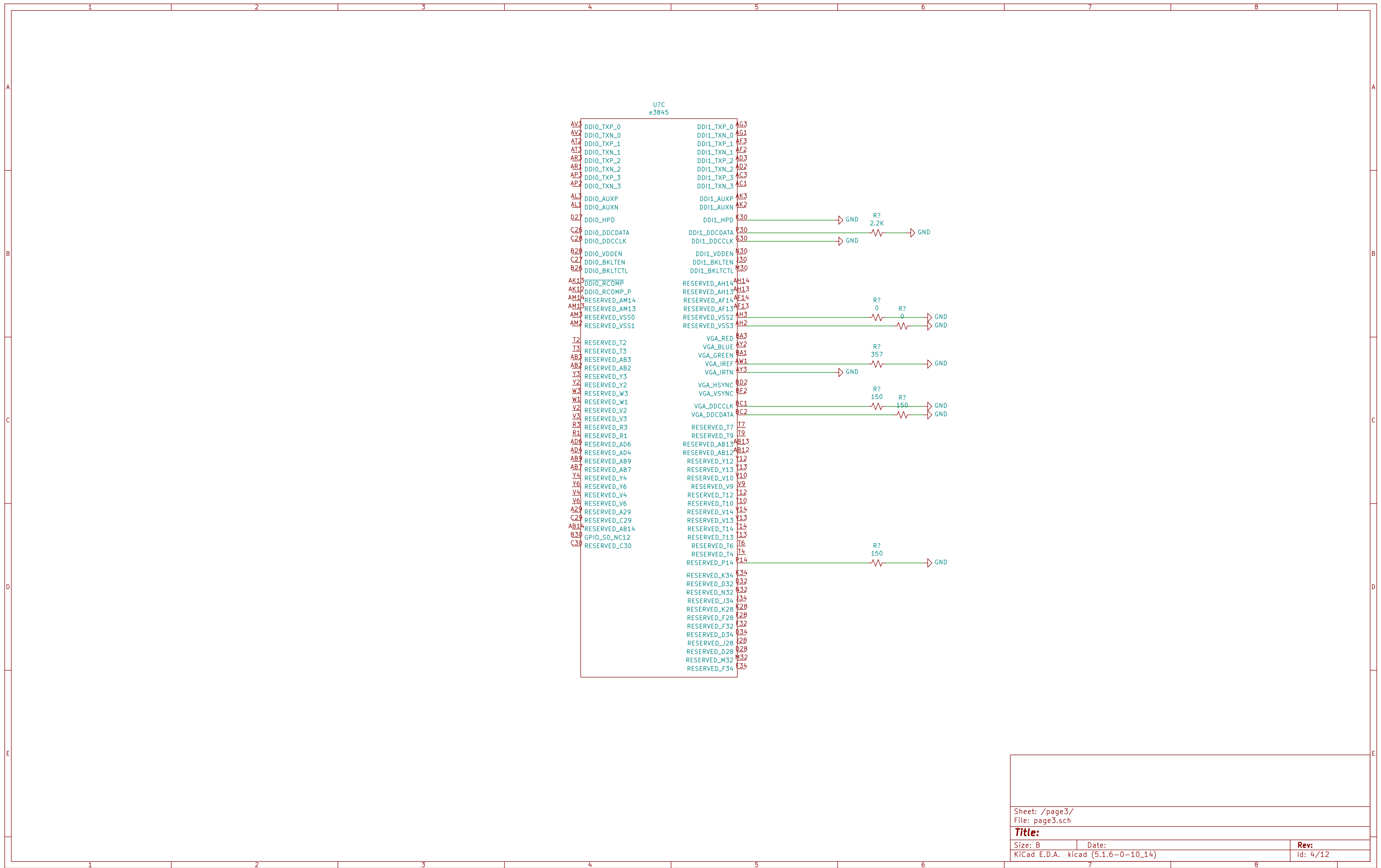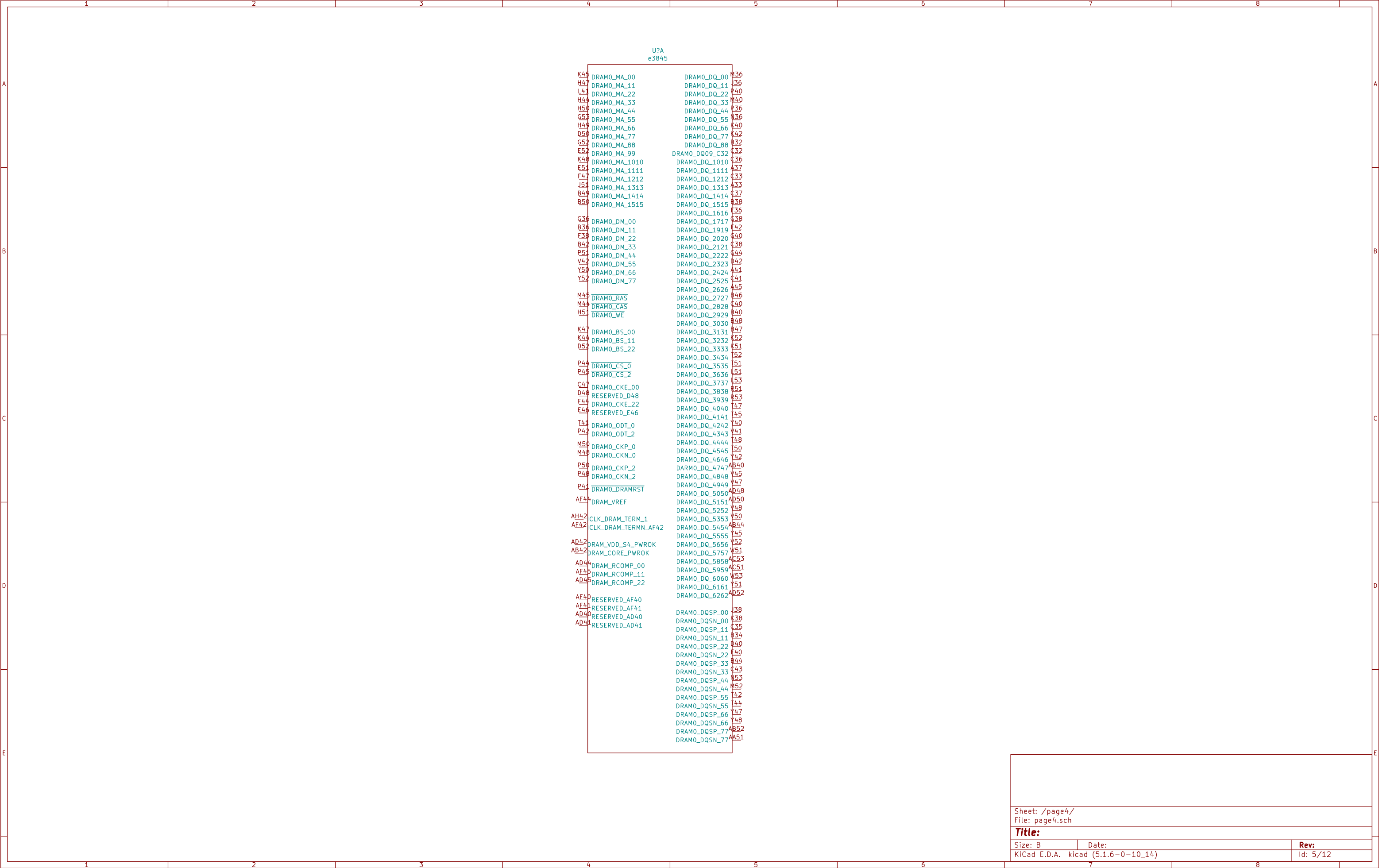|		
GPIO_SPI_CS#	1	2
GPIO_SPI_MISO	3	4
GPIO_SPI_MOSI	5	6	GPIO_UART1_TXD
GPIO_SPI_CLK	7	8	GPIO_UART1_RXD
GPIO_I2C_SCL	9	10	GPIO_UART1_CTS
GPIO_I2C_SDA	11	12	GPIO_UART1_RTS
GPIO_UART2_TXD	13	14	GPIO_I2S_CLK
GPIO_UART2_RXD	15	16	GPIO_I2S_FRM
GPIO_S5_0	17	18	GPIO_I2S_DO
GPIO_S5_1	19	20	GPIO_I2S_DI
GPIO_S5_2	21	22	GPIO_PWM0
	23	24	GPIO_PWM1
	25	26	I2SMCLK_GPIO

CONN_02X13

GND

GND

Sheet: page1
File: page1.sch

Sheet: page2
File: page2.sch

Sheet: page3
File: page3.sch

Sheet: page4
File: page4.sch

Sheet: page5
File: page5.sch

Sheet: page6
File: page6.sch

Sheet: page7
File: page7.sch

Sheet: page8
File: page8.sch

Sheet: page9
File: page9.sch

Sheet: page10
File: page10.sch

Sheet: page11
File: page11.sch

Sheet: /
File: MinnowBoard_port.sch

Title: MinnowBoard Turbot Quad KiCad port.

Size: B | Date: | Rev:
KiCad E.D.A. kicad (5.1.6-0-10_14) | Id: 1/12

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e3845

Left pins		Right pins

AV3 DDI0_TXP_0   DDI1_TXP_0 AG3
AV2 DDI0_TXN_0   DDI1_TXN_0 AG1
AT2 DDI0_TXP_1   DDI1_TXP_1 AF3
AT3 DDI0_TXN_1   DDI1_TXN_1 AF2
AR3 DDI0_TXP_2   DDI1_TXP_2 AD3
AR1 DDI0_TXN_2   DDI1_TXN_2 AD2
AP3 DDI0_TXP_3   DDI1_TXP_3 AC3
AP2 DDI0_TXN_3   DDI1_TXN_3 AC1

AL3 DDI0_AUXP   DDI1_AUXP AK3
AL1 DDI0_AUXN   DDI1_AUXN AK2
D27 DDI0_HPD   DDI1_HPD K30
C26 DDI0_DDCDATA   DDI1_DDCDATA P30
C28 DDI0_DDCCLK   DDI1_DDCCLK G30

B28 DDI0_VDDEN   DDI1_VDDEN N30
C27 DDI0_BKLTEN   DDI1_BKLTEN J30
B26 DDI0_BKLTCTL   DDI1_BKLTCTL M30

AK13 DDI0_RCOMP   RESERVED_AH14 AH14
AK12 DDI0_RCOMP_P   RESERVED_AH13 AH13
AM14 RESERVED_AM14   RESERVED_AF14 AF14
AM13 RESERVED_AM13   RESERVED_AF13 AF13
AM3 RESERVED_VSS0   RESERVED_VSS2 AH3
AM2 RESERVED_VSS1   RESERVED_VSS3 AH2

T2 RESERVED_T2   VGA_RED BA3
T3 RESERVED_T3   VGA_BLUE AY2
AB3 RESERVED_AB3   VGA_GREEN BA1
AB2 RESERVED_AB2   VGA_IREF AW1
Y3 RESERVED_Y3   VGA_IRTN AY3
Y2 RESERVED_Y2
W3 RESERVED_W3   VGA_HSYNC BD2
W1 RESERVED_W1   VGA_VSYNC BE2
V2 RESERVED_V2
V3 RESERVED_V3   VGA_DDCCLK BC1
R3 RESERVED_R3   VGA_DDCDATA BC2
R1 RESERVED_R1
AD6 RESERVED_AD6   RESERVED_T7 T7
AD4 RESERVED_AD4   RESERVED_T9 T9
AB9 RESERVED_AB9   RESERVED_AB13 AB13
AB7 RESERVED_AB7   RESERVED_AB12 AB12
Y4 RESERVED_Y4   RESERVED_Y12 Y12
Y6 RESERVED_Y6   RESERVED_Y13 Y13
V4 RESERVED_V4   RESERVED_V10 V10
V6 RESERVED_V6   RESERVED_V9 V9
A29 RESERVED_A29   RESERVED_T12 T12
C29 RESERVED_C29   RESERVED_T10 T10
AB14 RESERVED_AB14   RESERVED_V14 V14
B30 GPIO_S0_NC12   RESERVED_V13 V13
C30 RESERVED_C30   RESERVED_T14 T14
                        RESERVED_T13 T13
                        RESERVED_T6 T6
                        RESERVED_T4 T4
                        RESERVED_P14 P14

                        RESERVED_K34 K34
                        RESERVED_D32 D32
                        RESERVED_N32 N32
                        RESERVED_J34 J34
                        RESERVED_K28 K28
                        RESERVED_F28 F28
                        RESERVED_F32 F32
                        RESERVED_D34 D34
                        RESERVED_J28 J28
                        RESERVED_D28 D28
                        RESERVED_M32 M32
                        RESERVED_F34 F34

GND   R? 2.2K
GND   R? (DDI1_DDCDATA P30) GND
GND

R? 0   R? 0
        GND
        GND

R? 357
        GND
GND

R? 150   R? 150
         GND
         GND

R? 150
         GND

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Pin	Left signal
K45	DRAM0_MA_00
H47	DRAM0_MA_11
L41	DRAM0_MA_22
H44	DRAM0_MA_33
H50	DRAM0_MA_44
G51	DRAM0_MA_55
H49	DRAM0_MA_66
D50	DRAM0_MA_77
G52	DRAM0_MA_88
E52	DRAM0_MA_99
K48	DRAM0_MA_1010
E51	DRAM0_MA_1111
F47	DRAM0_MA_1212
J51	DRAM0_MA_1313
B49	DRAM0_MA_1414
B50	DRAM0_MA_1515
G36	DRAM0_DM_00
B36	DRAM0_DM_11
F38	DRAM0_DM_22
B42	DRAM0_DM_33
P51	DRAM0_DM_44
V42	DRAM0_DM_55
Y50	DRAM0_DM_66
Y52	DRAM0_DM_77
M45	DRAM0_RAS
M44	DRAM0_CAS
H51	DRAM0_WE
K47	DRAM0_BS_00
K44	DRAM0_BS_11
D52	DRAM0_BS_22
P44	DRAM0_CS_0
P45	DRAM0_CS_2
C47	DRAM0_CKE_00
D48	RESERVED_D48
F44	DRAM0_CKE_22
E46	RESERVED_E46
T41	DRAM0_ODT_0
P42	DRAM0_ODT_2
M50	DRAM0_CKP_0
M48	DRAM0_CKN_0
P50	DRAM0_CKP_2
P48	DRAM0_CKN_2
P41	DRAM0_DRAMRST
AF44	DRAM_VREF
AH42	CLK_DRAM_TERM_1
AF42	CLK_DRAM_TERMN_AF42
AD42	DRAM_VDD_S4_PWROK
AB42	DRAM_CORE_PWROK
AD44	DRAM_RCOMP_00
AF46	DRAM_RCOMP_11
AD46	DRAM_RCOMP_22
AF40	RESERVED_AF40
AF41	RESERVED_AF41
AD40	RESERVED_AD40
AD41	RESERVED_AD41

Right signal	Pin
DRAM0_DQ_00	M36
DRAM0_DQ_11	J36
DRAM0_DQ_22	P40
DRAM0_DQ_33	M40
DRAM0_DQ_44	P36
DRAM0_DQ_55	N36
DRAM0_DQ_66	K40
DRAM0_DQ_77	K42
DRAM0_DQ_88	B32
DRAM0_DQ09_C32	C32
DRAM0_DQ_1010	C36
DRAM0_DQ_1111	A37
DRAM0_DQ_1212	A33
DRAM0_DQ_1313	A33
DRAM0_DQ_1414	C37
DRAM0_DQ_1515	B38
DRAM0_DQ_1616	F36
DRAM0_DQ_1717	E38
DRAM0_DQ_1919	F42
DRAM0_DQ_2020	G40
DRAM0_DQ_2121	C38
DRAM0_DQ_2222	G44
DRAM0_DQ_2323	D42
DRAM0_DQ_2424	A41
DRAM0_DQ_2525	C41
DRAM0_DQ_2626	A45
DRAM0_DQ_2727	B46
DRAM0_DQ_2828	C40
DRAM0_DQ_2929	B40
DRAM0_DQ_3030	B48
DRAM0_DQ_3131	B47
DRAM0_DQ_3232	K52
DRAM0_DQ_3333	K51
DRAM0_DQ_3434	T52
DRAM0_DQ_3535	T51
DRAM0_DQ_3636	L51
DRAM0_DQ_3737	L53
DRAM0_DQ_3838	R51
DRAM0_DQ_3939	R53
DRAM0_DQ_4040	T47
DRAM0_DQ_4141	T45
DRAM0_DQ_4242	Y40
DRAM0_DQ_4343	Y41
DRAM0_DQ_4444	T48
DRAM0_DQ_4545	T50
DRAM0_DQ_4646	T42
DARM0_DQ_4747	AB40
DRAM0_DQ_4848	Y45
DRAM0_DQ_4949	Y47
DRAM0_DQ_5050	AD48
DRAM0_DQ_5151	AD50
DRAM0_DQ_5252	Y48
DRAM0_DQ_5353	Y50
DRAM0_DQ_5454	AB44
DRAM0_DQ_5555	Y45
DRAM0_DQ_5656	Y52
DRAM0_DQ_5757	V51
DRAM0_DQ_5858	AC53
DRAM0_DQ_5959	AC51
DRAM0_DQ_6060	V53
DRAM0_DQ_6161	Y51
DRAM0_DQ_6262	AD52
DRAM0_DQSP_00	J38
DRAM0_DQSN_00	K38
DRAM0_DQSP_11	C35
DRAM0_DQSN_11	B34
DRAM0_DQSP_22	D40
DRAM0_DQSN_22	E40
DRAM0_DQSP_33	B44
DRAM0_DQSN_33	C43
DRAM0_DQSP_44	N53
DRAM0_DQSN_44	M52
DRAM0_DQSP_55	T42
DRAM0_DQSN_55	T44
DRAM0_DQSP_66	Y47
DRAM0_DQSN_66	Y48
DRAM0_DQSP_77	AB52
DRAM0_DQSN_77	AA51

Sheet: /page4/
File: page4.sch
Title:
Size: B   Date:
KiCad E.D.A. kicad (5.1.6-0-10_14)
Rev:
Id: 5/12

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e3845

Pin	Signal		Signal	Pin
AY45	DRAM1_MA_00		DRAM1_DQ_00	BG38
BB47	DRAM1_MA_11		DRAM1_DQ_11	BC40
AW41	DRAM1_MA_22		DRAM1_DQ_22	BA42
BB44	DRAM1_MA_33		DRAM1_DQ_33	BD42
BB50	DRAM1_MA_44		DRAM1_DQ_44	BC38
BC53	DRAM1_MA_55		DRAM1_DQ_55	BD36
BB49	DRAM1_MA_66		DRAM1_DQ_66	BF42
BF50	DRAM1_MA_77		DRAM1_DQ_77	BC44
BC52	DRAM1_MA_88		DRAM1_DQ_88	BH32
BE52	DRAM1_MA_99		DRAM1_DQ_99	BG32
AY48	DRAM1_MA_1010		DRAM1_DQ_1010	BG36
BE51	DRAM1_MA_1111		DRAM1_DQ_1111	BJ37
BD47	DRAM1_MA_1212		DRAM1_DQ_1212	BG33
BA51	DRAM1_MA_1313		DRAM1_DQ_1313	BJ33
BH49	DRAM1_MA_1414		DRAM1_DQ_1414	BG37
BH50	DRAM1_MA_1515		DRAM1_DQ_1515	BH38
			DRAM1_DQ_1616	AJ36
BD38	DRAM1_DM_00		DRAM1_DQ_1717	AT36
BH36	DRAM1_DM_11		DRAM1_DQ_1818	AV40
BC36	DRAM1_DM_22		DRAM1_DQ_1919	AT40
BH42	DRAM1_DM_33		DRAM1_DQ_2020	BA36
AT51	DRAM1_DM_44		DRAM1_DQ_2121	AV36
AM42	DRAM1_DM_55		DRAM1_DQ_2222	AY42
AK50	DRAM1_DM_66		DRAM1_DQ_2323	AY40
AK52	DRAM1_DM_77		DRAM1_DQ_2424	BJ41
			DRAM1_DQ_2525	BG41
AV45	DRAM1_RAS		DRAM1_DQ_2626	BJ45
AV44	DRAM1_CAS		DRAM1_DQ_2727	BH46
BB51	DRAM1_WE		DRAM1_DQ_2828	BG40
			DRAM1_DQ_2929	BH40
AY47	DRAM1_BS_00		DRAM1_DQ_3030	BH48
AY44	DRAM1_BS_11		DRAM1_DQ_3131	BH47
BF52	DRAM1_BS_22		DRAM1_DQ_3232	AY52
			DRAM1_DQ_3333	AY51
AT44	DRAM1_CS_0		DRAM1_DQ_3434	AP52
			DRAM1_DQ_3535	AP51
AT45	DRAM1_CS_2		DRAM1_DQ_3636	AW51
			DRAM1_DQ_3737	AW53
BG47	DRAM1_CKE_00		DRAM1_DQ_3838	AR51
BE46	RESERVED_BE46		DRAM1_DQ_3939	AR53
BD44	DRAM1_CKE_22		DRAM1_DQ_4040	AP47
BF48	RESERVED_BF48		DRAM1_DQ_4141	AP45
			DRAM1_DQ_4242	AK40
AP41	DRAM1_ODT_0		DRAM1_DQ_4343	AM41
AT42	DRAM1_ODT_2		DRAM1_DQ_4444	AP48
			DRAM1_DQ_4545	AP50
AV50	DRAM1_CKP_0		DRAM1_DQ_4646	AK42
AV48	DRAM1_CKN_0		DRAM1_DQ_4747	AH40
			DRAM1_DQ_4848	AM45
AT50	DRAM1_CKP_2		DRAM1_DQ_4949	AM47
AT48	DRAM1_CKN_2		DRAM1_DQ_5050	AF48
			DRAM1_DQ_5151	AF50
AT41	DRAM1_DRAMRST		DRAM1_DQ_5252	AM48
			DRAM1_DQ_5353	AM50
			DRAM1_DQ_5454	AH44
			DRAM1_DQ_5555	AK45
			DRAM1_DQ_5656	AM52
			DRAM1_DQ_5757	AL51
			DRAM1_DQ_5858	AG53
			DRAM1_DQ_5959	AG51
			DRAM1_DQ_6060	AL53
			DRAM1_DQ_6161	AK51
			DRAM1_DQ_6262	AF52
			DRAM1_DQ_6363	AF51
			DRAM1_DQSP_00	BF40
			DRAM1_DQSN_00	BD40
			DRAM1_DQSP_11	BG35
			DRAM1_DQSN_11	BH34
			DRAM1_DQSP_22	BA38
			DRAM1_DQSN_22	AY38
			DRAM1_DQSP_33	BH44
			DRAM1_DQSN_33	BG43
			DRAM1_DQSP_44	AJ53
			DRAM1_DQSN_44	AV53
			DRAM1_DQSP_55	AP42
			DRAM1_DQSN_55	AP44
			DRAM1_DQSP_66	AK47
			DRAM1_DQSN_66	AK48
			DRAM1_DQSP_77	AH52
			DRAM1_DQSN_77	AJ51

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e3845

Left pins	Signal	Right signal	Right pins
BF6	SATA_TXP_0	PCIE_TXP_0	AY7
BG7	SATA_TXN_0	PCIE_TXN_0	AY6
AU16	SATA_RXP_0	PCIE_RXP_0	AT14
AV16	SATA_RXN_0	PCIE_RXN_0	AT13
BD10	SATA_TXP_1	PCIE_TXP_1	AV6
BF10	SATA_TXN_1	PCIE_TXN_1	AV4
AY15	SATA_RXP_1	PCIE_RXP_1	AT10
BA16	SATA_RXN_1	PCIE_RXN_1	AT9
BB10	RESERVED_VSS4	PCIE_TXP_2	AT7
BC10	RESERVED_VSS5	PCIE_TXN_2	AT6
BA12	SATA_GP0	PCIE_RXP_2	AP12
AY14	SATA_GP1	PCIE_RXN_2	AP10
AY12	SATA_LED	PCIE_TXP_3	AP6
AT22	MMC1_CLK	PCIE_TXN_3	AP7
AV20	MCC1_D0	PCIE_RXP_3	AP9
AU22	MMC1_D1	PCIE_RXN_3	AP7
AV22	MMC1_D2		
AT20	MMC1_D3	RESERVED_VSS6	BB5
AY24	MMC1_D4	RESERVED_VSS7	BB7
AU26	MCC1_D5	PCIE_CLKREQ_0	BG3
AT26	MCC1_D6	PCIE_CLKREQ_1	BD7
AU20	MMC1_D7	PCIE_CLKREQ_2	BG5
AV26	MMC1_CMD	PCIE_CLKREQ_3	BE3
BA24	MMC1_RST	SD3_WP_BD5	BD5
AY18	MMC1_RCOMP	PCIE_RCOMP_P_AP14_AP14	AP14
BA18	SD2_CLK	PCIE_RCOMP_N_AP13_AP13	AP13
AY20	SD2_D0	RESERVED_BB4	BB4
BD20	SD2_D1	RESERVED_BB3	BB3
BA20	SD2_D2	RESERVED_AV10	AV10
BD18	SD2_D3_CD	RESERVED_AV9	AV9
BC18	SD2_CMD	HDA_LPE_RCOMP	BF20
AY26	SD3_CLK	HDA_RST	BG22
AT28	SD3_D0	HDA_SYNC	BH20
BD26	SD3_D1	HDA_CLK	BJ21
AU28	SD3_D2	HDA_SD0	BG20
BA26	SD3_D3	HDA_SDI0	BG19
BC24	SD3_CD#	HDA_SDI1	BG21
AV28	SD3_CMD	GPIO_S0_SC_14	BH18
BF22	SD3_1P8EN	GPIO_S0_SC_15	BG18
BD22	SD3_PWREN	LPE_I2S2_CLK	BF28
BF26	SD3_RCOMP	LPE_I2S2_FRM	BA30
		LPE_I2S2_DATAOUT	BC30
		LPE_I2S2_DATAIN	BD28
		RESERVED_P34	P34
		RESERVED_N34	N34
		RESERVED_AK9	AK9
		RESERVED_AK7	AK7
		PROCHOT	C24

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e3845

Pin	Signal		Signal	Pin
AH12	ICLK_OSCIN		SIO_UART1_RXD	AU34
AH10	ICLK_OSCOUT		SIO_UART1_TXD	AV34
AD9	RESERVED_AD9		SIO_UART1_RTS	BA34
			SIO_UART1_CTS	AY34
AD14	ICLK_ICOMP			
AD13	ICLK_RCOMP		SIO_UART2_RXD	BE34
AD10	RESERVED_AD10		SIO_UART2_TXD	BD34
AD12	RESERVED_AD12		SIO_UART2_RTS	BD32
			SIO_UART2_CTS	BF32
AF6	PCIE_CLKN_00			
AF4	PCIE_CLKP_00		PMC_SUSPWRDNACK	D26
AF9	PCIE_CLKN_11		PMC_SUSCLK0_G24	G24
AF7	PCIE_CLKP_11		PCM_SLP_S0IX	F18
AK4	PCIE_CLKN_22		PCM_SLP_S4	C22
AK6	PCIE_CLKP_22		PCM_SLP_S3	D22
AM4	PCIE_CLKN_33		GPIO_S5_14	J20
AM6	PCIE_CLKP_33		PMC_ACPRESENT	D20
			PMC_AWAKE_PCIE_0	C26
AM10	RESERVED_AM10		PMC_BATLOW	K26
AM9	RESERVED_AM9		PMC_PWRBTN	J26
			PMC_RSTBTN	BG9
BH7	PCM_PLT_CLK_00		PMC_PLTRST	C20
BH5	PCM_PLT_CLK_11		GPIO_S5_17	J24
BH4	PCM_PLT_CLK_22		PMC_SUS_STAT	G18
BH8	PCM_PLT_CLK_33			
BH6	PCM_PLT_CLK_44		ILB_RTC_TEST	C11
BJ9	PCM_PLT_CLK_55			
C12	ILB_RTC_RST		PMC_RSMRST	B10
			PMC_CORE_PWROK	B7
D14	TAP_TCK			
G12	TAP_TRST		ILB_RTC_X1	C9
F14	TAP_TMS		ILB_RTC_X2	A9
F12	TAP_TDI		ILB_RTC_EXTPAD	B8
G16	TAP_TDO			
D18	TAP_PRDY		SVID_ALERT	B24
F16	TAP_PREQ		SVID_DATA	A25
AT34	RESERVED_AT34		SVID_CLK	C25
C23	PCU_SPI_CS_00		SIO_PWM_00	AU32
C21	PCU_SPI_CS_11		SIO_PWM_11	AT32
B22	PCU_SPI_MISO			
A21	PCU_SPI_MOSI		GPIO_S5_22	K24
C22	PCU_SPI_CLK		GPIO_S5_23	N24
			GPIO_S5_24	M20
B19	GPIO_S5_0		GPIO_S5_25	J18
B16	GPIO_S5_1		GPIO_S5_26	M18
C18	GPIO_S5_2		GPIO_S5_27	K18
A17	GPIO_S5_3		GPIO_S5_28	K20
C17	GPIO_S5_4		GPIO_S5_29	M22
C16	GPIO_S5_5		GPIO_S5_30	M24
C15	GPIO_S5_7			
C13	GPIO_S5_8		SIO_SPI_CS	AV32
A13	GPIO_S5_9		SIO_SPI_MISO	BA28
C19	GPIO_S5_10		SIO_SPI_MOSI	AY28
			SIO_SPI_CLK	AY30
N26	GPIO_RCOMP			

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e3845

Pin	Left Signal		Right Signal	Pin

Left side pins and signals:
- G2 — GPIO_S5_31
- M3 — GPIO_S5_32
- L1 — GPIO_S5_33
- K2 — GPIO_S5_34
- K3 — GPIO_S5_35
- M2 — GPIO_S5_36
- N3 — GPIO_S5_37
- P2 — GPIO_S5_38
- L3 — GPIO_S5_39
- J3 — GPIO_S5_40
- P3 — GPIO_S5_41
- B12 — GPIO_S5_43
- M16 — USB_DP0
- K16 — USB_DN0
- J14 — USB_DP1
- G14 — USB_DN1
- K12 — USB_DP2
- J12 — USB_DN2
- K10 — USB_DP3
- H10 — USB_DN3
- D10 — ICLK_USB_TERM_1
- F10 — ICLK_USB_TERMN
- D6 — USB_RCOMPO
- C7 — USB?RCOMPI
- M13 — USB_PLL_MON
- B4 — USB_HSIC0_DATA
- B5 — USB_HSIC0_STROBE
- E2 — USB_HSIC1_DATA
- D2 — USB_HSIC1_STROBE
- A7 — USB_HSIC_RCOMP
- BF18 — LPC_RCOMP
- BH16 — ILB_LPC_AD_00
- BJ17 — ILB_LPC_AD_11
- BJ13 — ILB_LPC_AD_22
- BG14 — ILB_LPC_AD_33
- BG17 — ILB_LPC_FRAME
- BG15 — ILB_LPC_CLK_00
- BH14 — ILB_LPC_CLK_11
- BG16 — ILB_LPC_CLKRUN
- BG13 — ILB_LPC_SERIRQ
- BG12 — PCU_SMB_DATA
- BH10 — PCU_SMB_CLK
- BG11 — PCU_SMB_ALERT

Right side signals and pins:
- RESERVED_M10 — M10
- RESERVED_M9 — M9
- RESERVED_P7 — P7
- RESERVED_P6 — P6
- RESERVED_M7 — M7
- RESERVED_P10 — P10
- RESERVED_P12 — P12
- RESERVED_M4 — M4
- RESERVED_M6 — M6
- RESERVED_H8 — H8
- RESERVED_H7 — H7
- RESERVED_H5 — H5
- RESERVED_H4 — H4
- USB3_REXT0 — M12
- USB3_RXP0 — D4
- USB3_RXN0 — E3
- USB3_TXP0 — K6
- USB3_TXN0 — K7
- GPIO_S0_SC_55 — D12
- GPIO_S0_SC_56 — C12
- GPIO_S0_SC_57 — D14
- GPIO_S0_SC_58 — C14
- GPIO_S0_SC_59 — F14
- GPIO_S0_SC_60 — D16
- GPIO_S0_SC_61 — C16
- ILB_8254_SPKR — H12
- SIO_I2C0_DATA — H22
- SIO_I2C0_CLK — G23
- SIO_I2C1_DATA — G24
- SIO_I2C1_CLK — H24
- SIO_I2C2_DATA — G25
- SIO_I2C2_CLK — J25
- SIO_I2C3_DATA — G26
- SIO_I2C3_CLK — H26
- SIO_I2C4_DATA — F27
- SIO_I2C4_CLK — G27
- SIO_I2C5_DATA — H28
- SIO_I2C5_CLK — G28
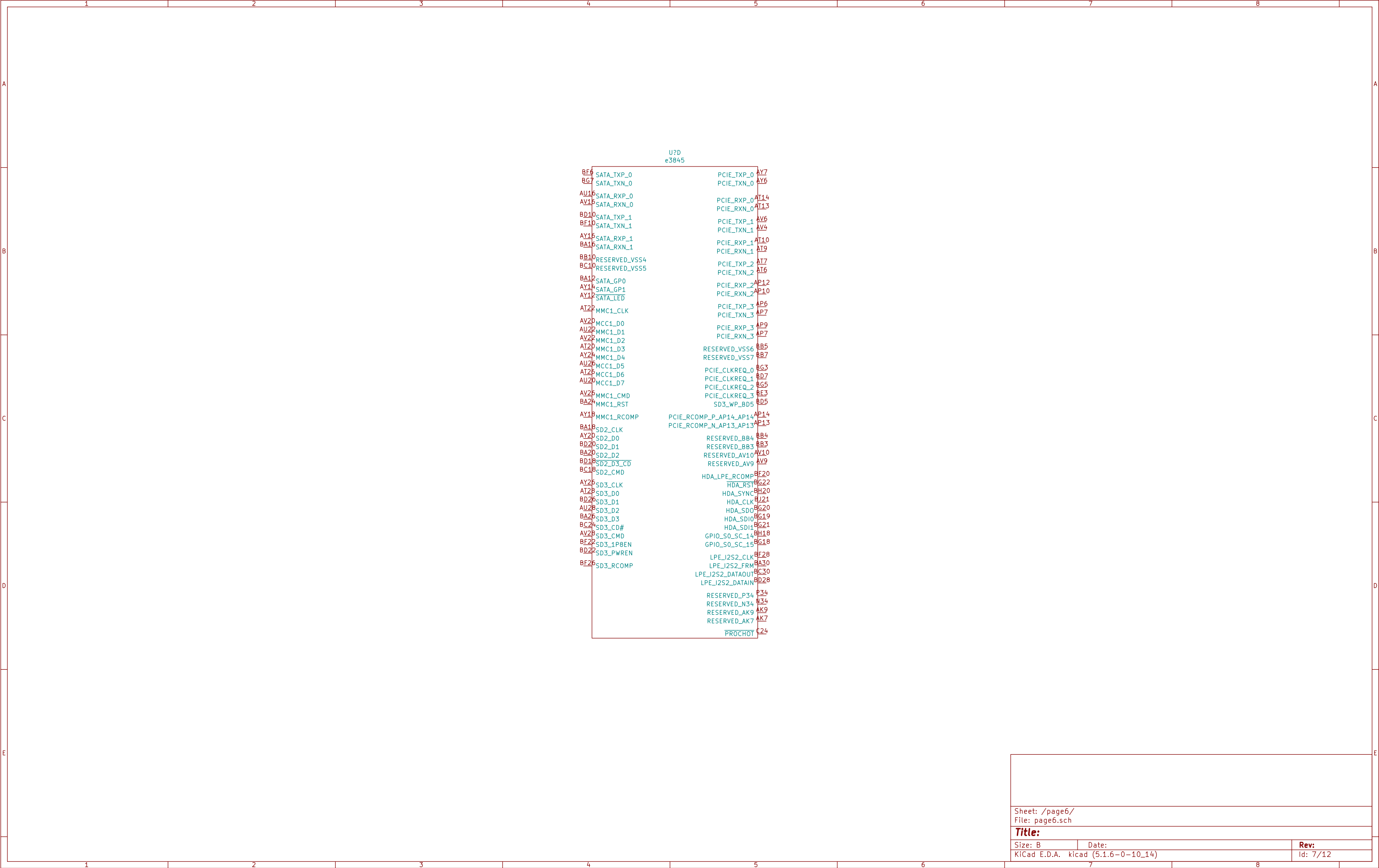- SIO_I2C6_DATA — J29
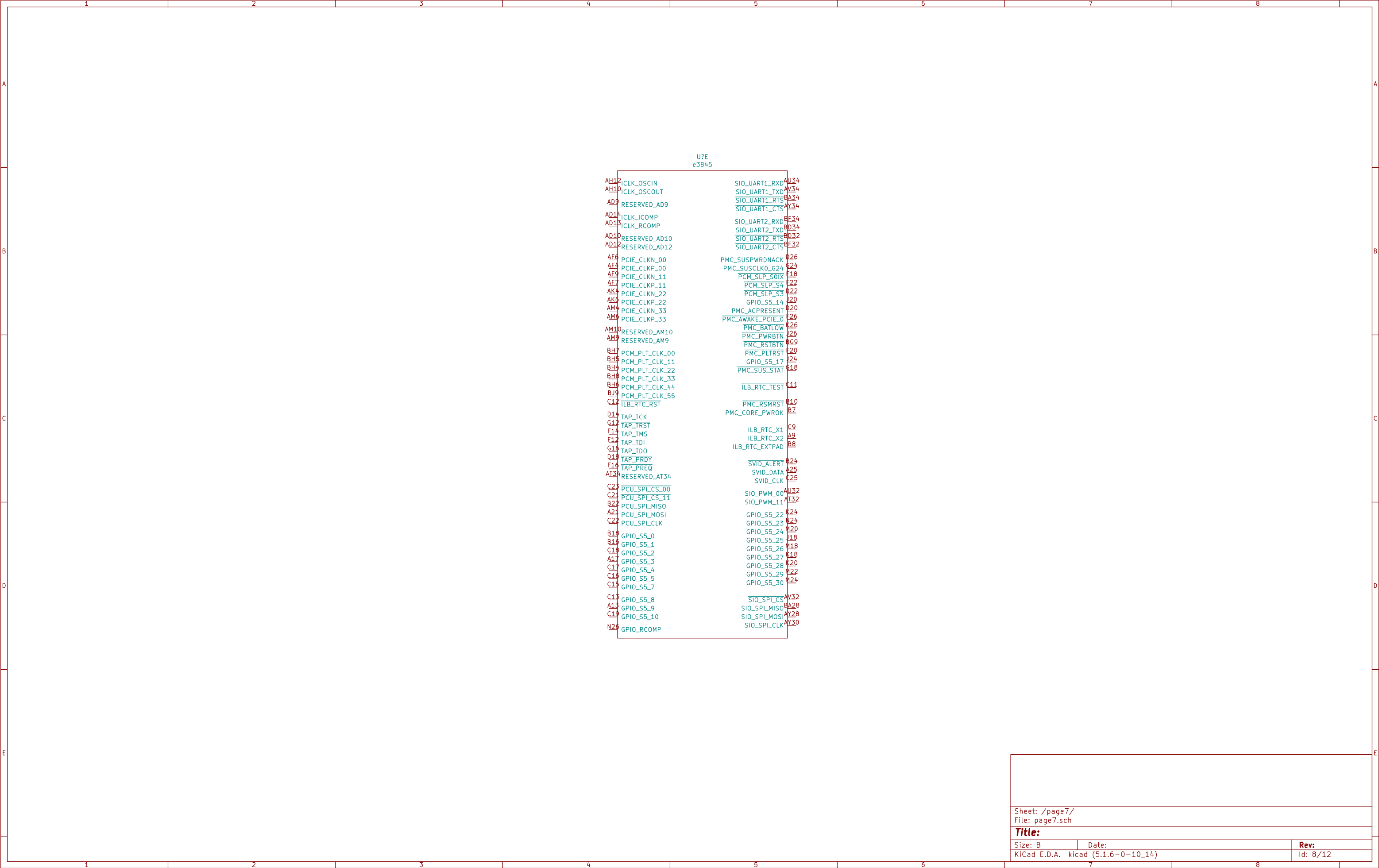- SIO_I2C6_CLK — G29
- GPIO_S0_SC_092 — H30
- GPIO_S0_SC_093 — G30

Sheet: /page8/
File: page8.sch
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Size: B   Date:
KiCad E.D.A.   kicad (5.1.6-0-10_14)
Rev:
Id: 9/12

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Pin	Left signal	Right signal	Pin

CORE_VCC_SENSE_P28 — P28
UNCORE_VNN_SENSE — BB8
CORE_VSS_SENSE_N28 — N28

DRAM_VDD_S4_AD38 — AD38
DRAM_VDD_S4_AF38 — AF38
DRAM_VDD_S4 — A48
DRAM_VDD_S4_AK38 — AK38
DRAM_VDD_S4_AM38 — AM38
DRAM_VDD_S4_AV41 — AV41
DRAM_VDD_S4_AV42 — AV42
DRAM_VDD_S4_BB46 — BB46

CORE_VCC_SOIX_AA27 — AA27
CORE_VCC_SOIX_AA29 — AA29
CORE_VCC_SOIX_AA30 — AA30
CORE_VCC_SOIX_AC27 — AC27
CORE_VCC_SOIX_AC29 — AC29
CORE_VCC_SOIX_AC30 — AC30
CORE_VCC_SOIX_AD27 — AD27
CORE_VCC_SOIX_AD30 — AD30
CORE_VCC_SOIX_AF27 — AF27
CORE_VCC_SOIX_AF29 — AF29
CORE_VCC_SOIX_AG27 — AG27
CORE_VCC_SOIX_AG29 — AG29
CORE_VCC_SOIX_AG30 — AG30
CORE_VCC_SOIX_P26 — P26
CORE_VCC_SOIX_P27 — P27
CORE_VCC_SOIX_U27 — U27
CORE_VCC_SOIX_U29 — U29
CORE_VCC_SOIX_V27 — V27
CORE_VCC_SOIX_V29 — V29
CORE_VCC_SOIX_V30 — V30
CORE_VCC_SOIX_Y27 — Y27
CORE_VCC_SOIX_Y29 — Y29
CORE_VCC_SOIX_Y30 — Y30

TP_CORE_V1P05_S4

DRAM_VDD_S4_BD49 — BD49
DRAM_VDD_S4_BD52 — BD52
DRAM_VDD_S4_BD53 — BD53
DRAM_VDD_S4_BF44 — BF44
DRAM_VDD_S4_BG51 — BG51
DRAM_VDD_S4_BJ48 — BJ48
DRAM_VDD_S4_C51 — C51
DRAM_VDD_S4_D44 — D44
DRAM_VDD_S4_F49 — F49
DRAM_VDD_S4_F52 — F52
DRAM_VDD_S4_F53 — F53
DRAM_VDD_S4_H46 — H46
DRAM_VDD_S4_M41 — M41
DRAM_VDD_S4_M42 — M42
DRAM_VDD_S4_V38 — V38
DRAM_VDD_S4_Y38 — Y38

UNCORE_VNN_S3_AA24 — AA24
UNCORE_VNN_S3_AC22 — AC22
UNCORE_VNN_S3_AC24 — AC24
UNCORE_VNN_S3_AD22 — AD22
UNCORE_VNN_S3_AD24 — AD24
UNCORE_VNN_S3_AF22 — AF22
UNCORE_VNN_S3_AF24 — AF24
UNCORE_VNN_S3_AG22 — AG22
UNCORE_VNN_S3_AG24 — AG24
UNCORE_VNN_S3_AJ22 — AJ22
UNCORE_VNN_S3_AJ24 — AJ24
UNCORE_VNN_S3_AK22 — AK22
UNCORE_VNN_S3_AK24 — AK24
UNCORE_VNN_S3_AK25 — AK25
UNCORE_VNN_S3_AK27 — AK27
UNCORE_VNN_S3_AK29 — AK29
UNCORE_VNN_S3_AK30 — AK30
UNCORE_VNN_S3_AK32 — AK32
UNCORE_VNN_S3_AM22 — AM22

TP2_CORE_VCC_SOIX — AA22

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Pin	Left signal		Right signal	Pin

Left pins:
- V32 SVID_V1P0_S3_V32
- BJ6 VGA_V1P0_S3_BJ6
- AD35 DRAM_V1P0_SOIX_AD35
- AF35 DRAM_V1P0_SOIX_AF35
- AF36 DRAM_V1P0_SOIX_AF36
- AA36 DRAM_V1P0_SOIX_AA36
- AJ36 DRAM_V1P0_SOIX_AJ36
- AK35 DRAM_V1P0_SOIX_AK35
- AK36 DRAM_V1P0_SOIX_AK36
- Y35 DRAM_V1P0_SOIX_Y35
- Y36 DRAM_V1P0_SOIX_Y36
- AK19 DDI_V1P0_SOIX_AK19
- AK21 DDI_V1P0_SOIX_AK21
- AJ18 DDI_V1P0_SOIX_AJ18
- AM16 DDI_V1P0_SOIX_AM16
- U22 UNCORE_V1P0_G3_U22
- V22 UNCORE_V1P0_G3_V22
- AN29 UNCORE_V1P0_SOIX_AN29
- AN30 UNCORE_V1P0_SOIX_AN30
- AF16 UNCORE_V1P0_S3_AF16
- AF18 UNCORE_V1P0_S3_AF18
- Y18 UNCORE_V1P0_S3_Y18
- G1 UNCORE_V1P0_S3_G1
- AM21 PCIE_V1P0_S3_AM21
- AN21 PCIE_V1P0_S3_AN21
- AN18 PCIE_SATA_V1P0_S3_AN18
- AN19 SATA_V1P0_S3_AN19
- AA33 CORE_V1P05_S3_AA33
- AF21 UNCORE_V1P0_SOIX_AF21
- AG21 UNCORE_V1P0_SOIX_AG21
- V24 UNCORE_V1P0_SOIX_V24
- Y22 UNCORE_V1P0_SOIX_Y22
- Y24 UNCORE_V1P0_SOIX_Y24
- M14 USB_V1P0_S3_M14
- U18 USB_V1P0_S3_U18
- U19 USB_V1P0_S3_U19
- AN25 GPIO_V1P0_S3_AN25
- Y19 USB3_V1P0_G3_Y19
- C3 USB3_V1P0_G3_C3
- C5 UNCORE_V1P0_G3_C5
- B6 UNCORE_V1P0_G3_B6
- AC32 CORE_V1P05_S3_AC32
- Y32 CORE_V1P05_S3_Y32
- U36 UNCORE_V1P35_SOIX_F4_U36
- AA25 UNCORE_V1P35_SOIX_F5_AA25
- AG32 UNCORE_V1P35_SOIX_F2_AG32
- V36 UNCORE_V1P35_SOIX_F3_V36
- BD1 VGA_V1P35_S3_F1_BD1
- AF19 UNCORE_V1P35_SOIX_F6
- AG19 UNCORE_V1P35_SOIX_F1_AG19
- AJ19 ICLK_V1P35_S3_F1_AJ19
- AG18 ICLK_V1P35_S3_F2
- AN16 VSSA_AN16
- U16 USB_VSSA_U16

Right pins:
- DRAM_V1P35_SOIX_F1_AD36 AD36
- HDA_LPE_V1P5V1P8_S3_AM32 AM32
- UNCORE_V1P8_S3_AM30 AM30
- UNCORE_V1P8_S3_AN32 AN32
- LPC_V1P8V3P3_S3_AM27 AM27
- UNCORE_V1P8_G3_U24 U24
- USB_V3P3_G3_N18 N18
- USB_V3P3_G3_P18 P18
- UNCORE_V1P8_S3_U38 U38
- VGA_V3P3_S3_AN24 AN24
- PCU_V1P8_S3_AN24 AN25
- PCU_V3P3_G3_N22 N22
- SD3_V1P8V3P3_S3_AN27 AN27
- VSS_AD16 AD16
- VSS_AD18 AD18
- USB_HSIC_V1P24_G3_V18 V18
- UNCORE_V1P8_G3_AA18 AA18
- RTC_VCC_P22 P22
- USB_V1P8_G3_N20 N20
- PMU_V1P8_G3_U25 U25
- CORE_V1P05_S3_AF33 AF33
- CORE_V1P05_S3_AG33 AG33
- CORE_V1P05_S3_AG35 AG35
- CORE_V1P05_S3_U33 U33
- CORE_V1P05_S3_U35 U35
- CORE_V1P05_S3_V33 V33
- VSS_A3_A3 A3
- VSS_A49_A49 A49
- VSS_A5_A5 A5
- VSS_A51_A51 A51
- VSS_A52_A52 A52
- VSS_A6_A6 A6
- VSS_B2_B2 B2
- VSS_B52_B52 B52
- VSS_B53_B53 B53
- VSS_BE1_BE1 BE1
- VSS_BE53_BE53 BE53
- VSS_BG1_BG1 BG1
- VSS_BG53_BG53 BG53
- VSS_BH1_BH1 BH1
- VSS_BH2_BH2 BH2
- VSS_BH52_BH52 BH52
- VSS_BH53_BH53 BH53
- VSS_BJ2_BJ2 BJ2
- VSS_BJ3_BJ3 BJ3
- VSS_BJ5_BJ5 BJ5
- VSS_BJ49_BJ49 BJ49
- VSS_BJ51_BJ51 BJ51
- VSS_BJ52_BJ52 BJ52
- VSS_C1_C1 C1
- VSS_C53_C53 C53
- VSS_E1_E1 E1
- VSS_E53_E53 E53
- RESERVED_F1 F1
- PCIE_V1P0_S3_AK18 AK18
- PCIE_V1P0_S3_AM18 AM18

Sheet: /page10/
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KiCad E.D.A. kicad (5.1.6-0-10_14)
Rev:
Id: 11/12

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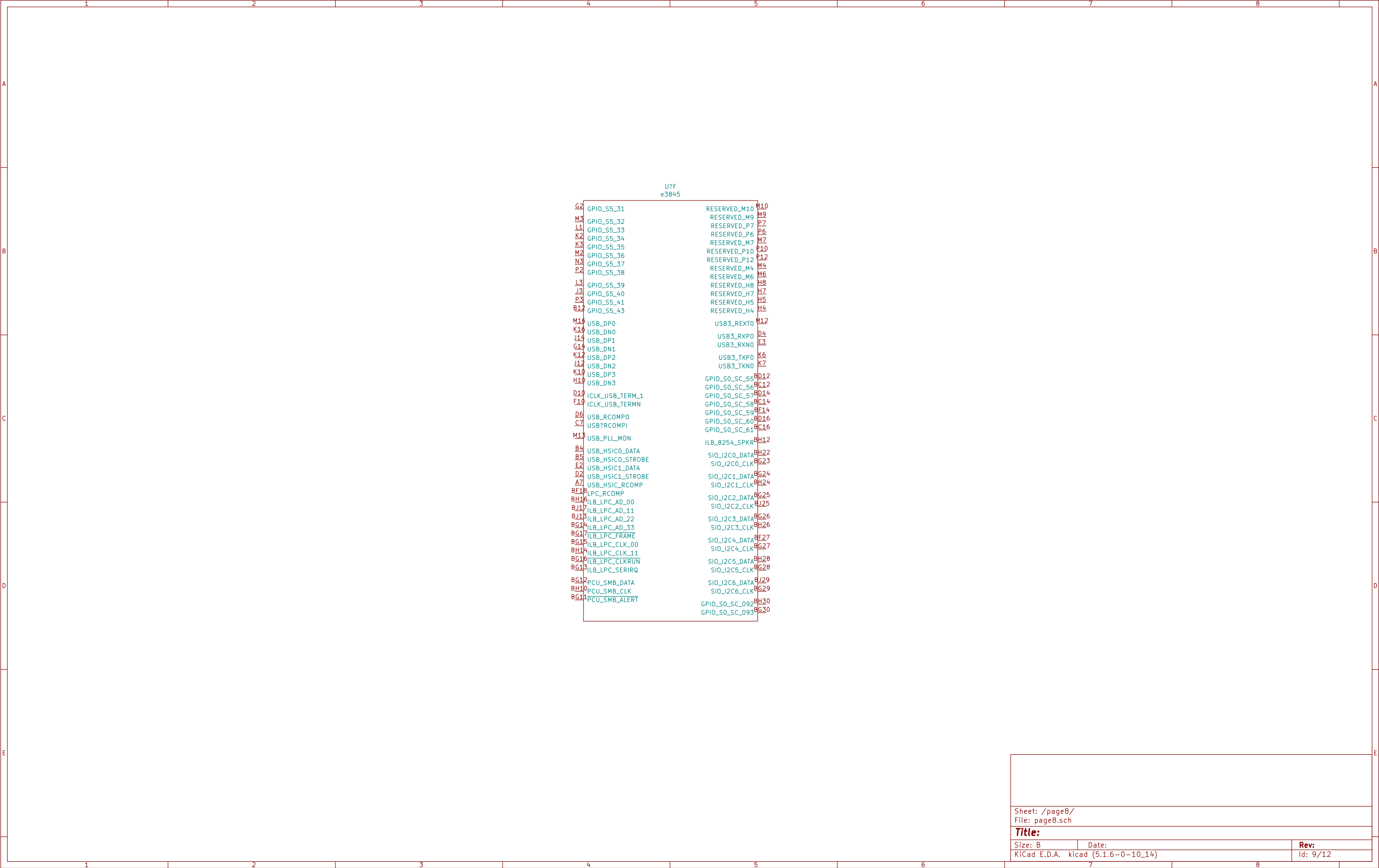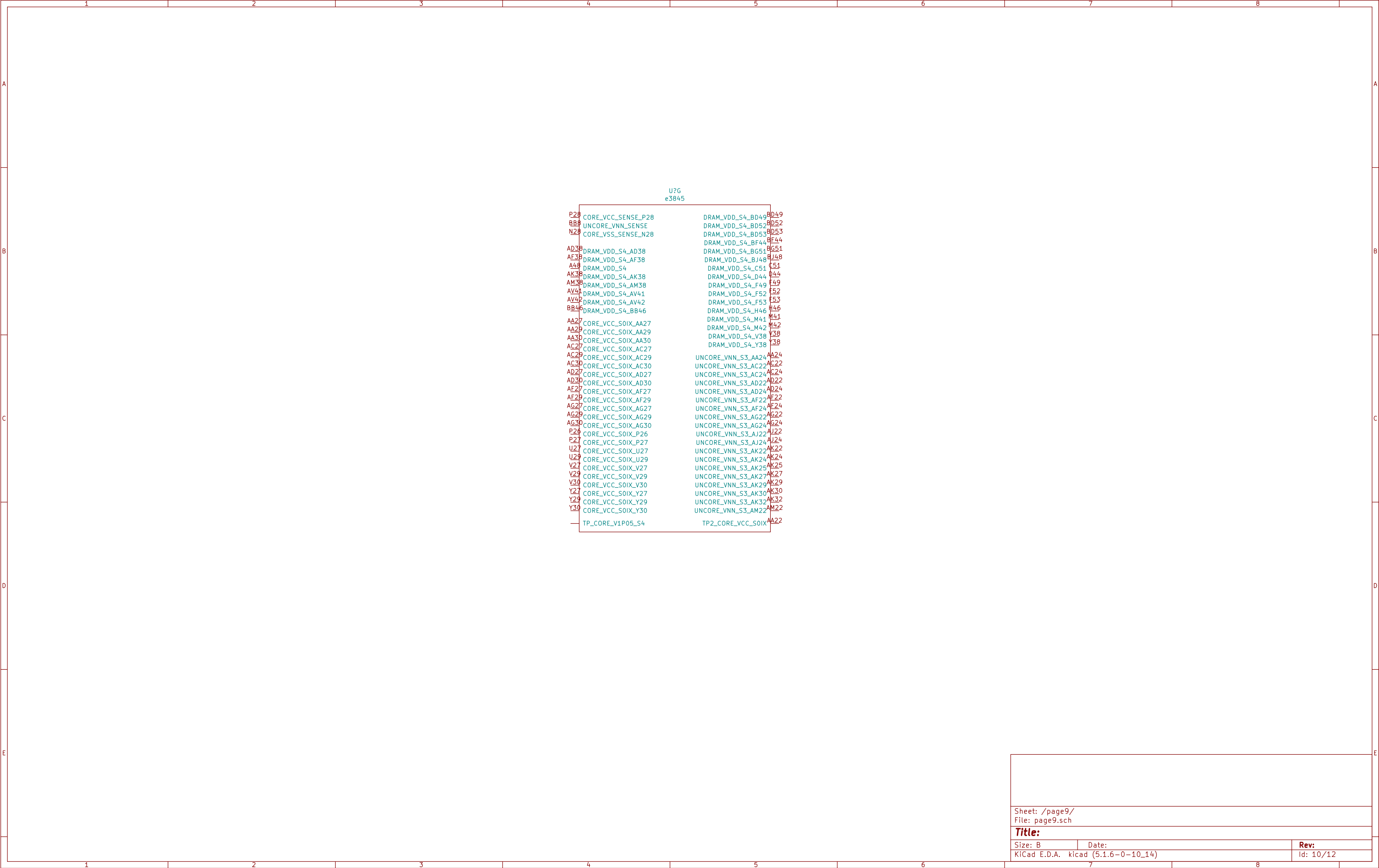e3845

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Id: 12/12