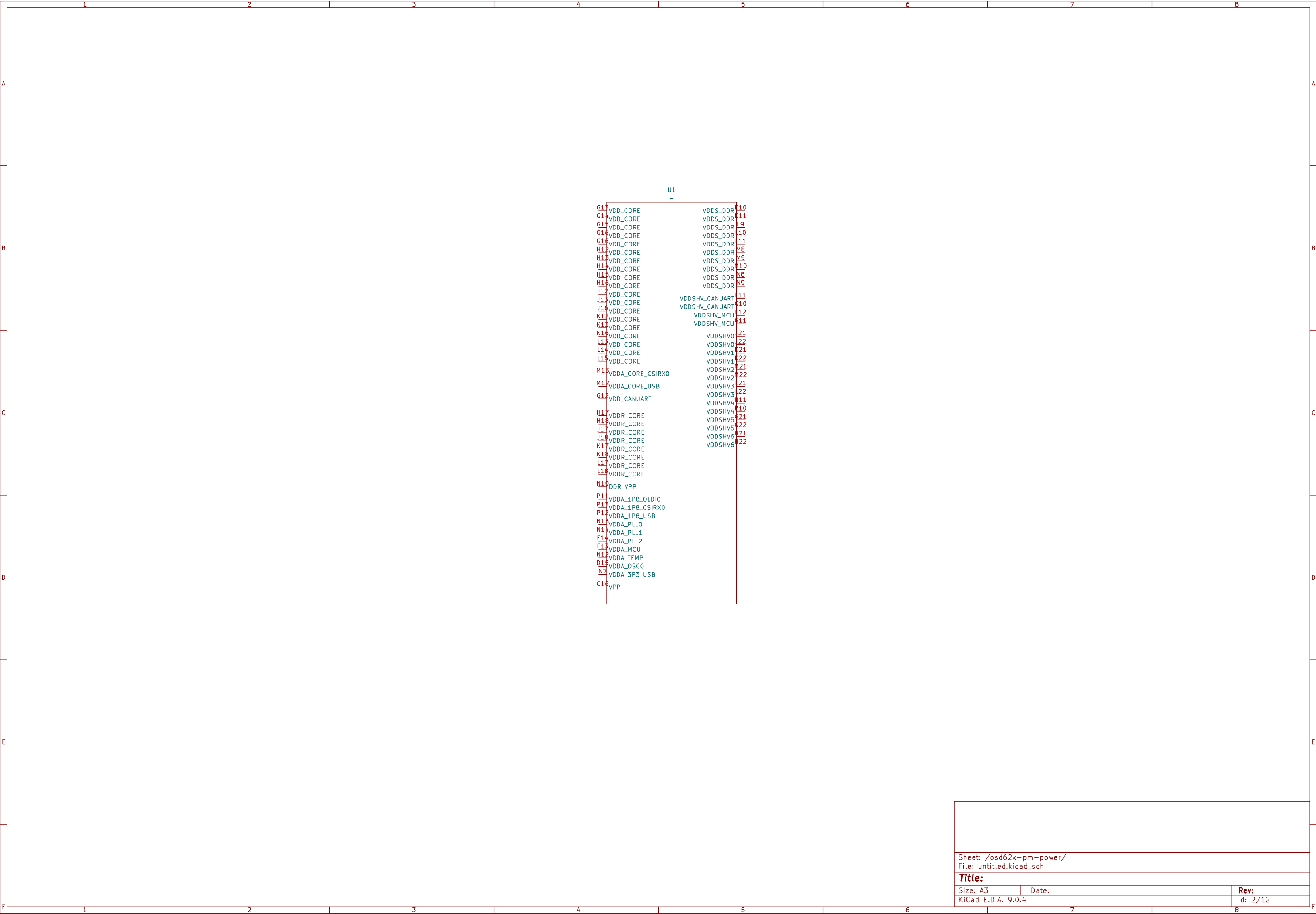


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	clocks & reset							
	File: clocksresets.kicad_sch							
	ddr & gpmc interface							
	File: ddr-gpmc.kicad_sch							
	peripherals							
	File: peripherals.kicad_sch							
B	rgmii-mdio							
	File: rgmii-mdio.kicad_sch							
	usb0-usb1							
	File: usb0-usb1.kicad_sch							
	display							
	File: display.kicad_sch							
	csi							
	File: csi.kicad_sch							
C	OSPI							
	File: ospi.kicad_sch							
	debug_control							
	File: debug_control.kicad_sch							
	misc_reserved_nc							
	File: misc_reserved_nc.kicad_sch							
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Sheet: / File: osd62x-pm-base.kicad_sch		
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KiCad E.D.A. 9.0.4		Id: 1/12



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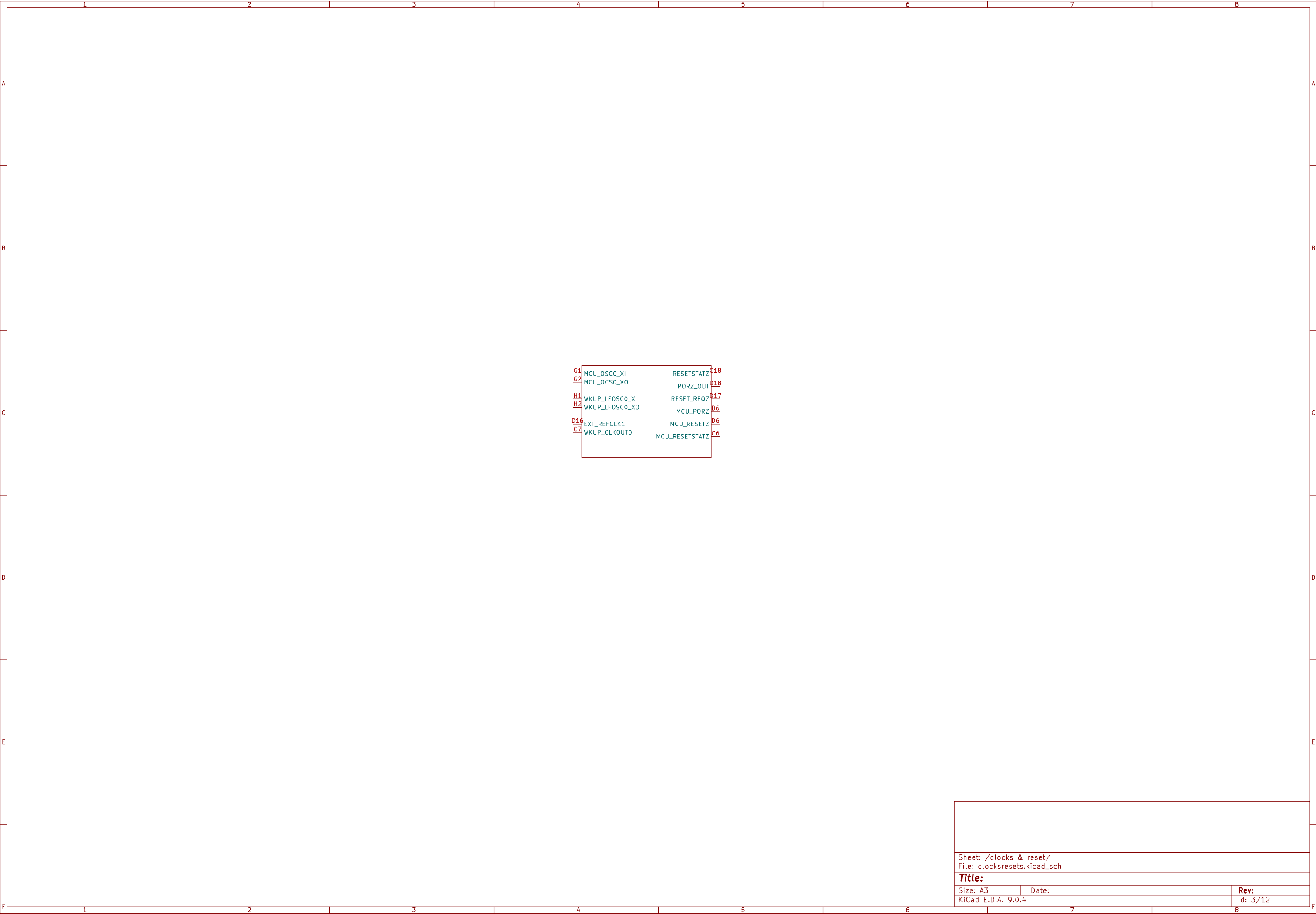
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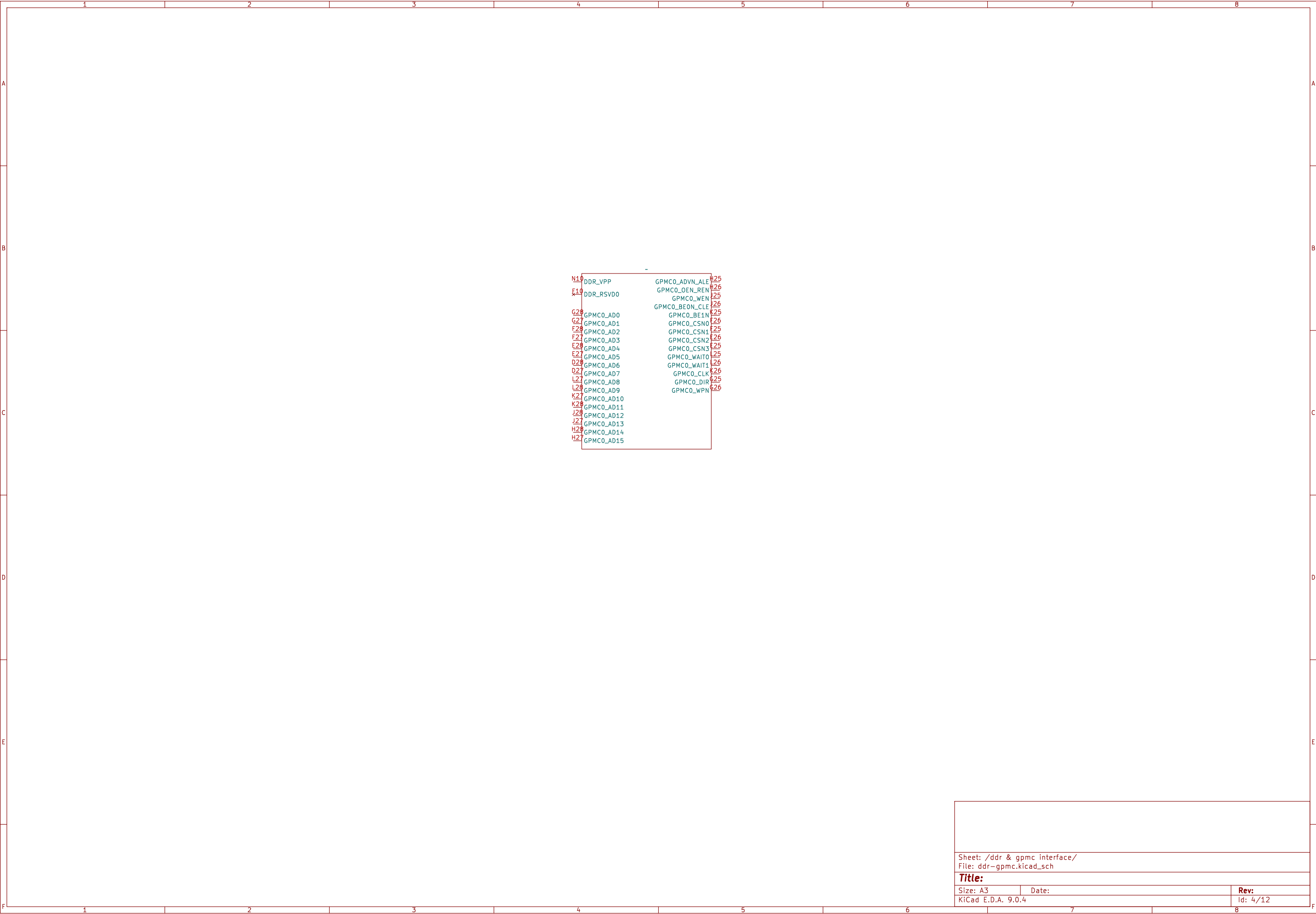
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KiCad E.D.A. 9.0.4

Id: 2/12





Sheet: /peripherals/ File: peripherals.kicad_sch	
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KiCad E.D.A. 9.0.4	Rev: 5/12



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F	1	2	3	4	5	6	7	8

T26	VOUT0_PCLK	OLDIO_A0N	P22
R25	VOUT0_DE	OLDIO_A0P	R1
P25	VOUT0_HSYNC	OLDIO_A1N	R2
R26	VOUT0_VSYNC	OLDIO_A1P	T1
P26	VOUT0_DATA0	ALDIO_A2N	T2
N25	VOUT0_DATA1	ALDIO_A2P	U2
N26	VOUT0_DATA2	ALDIO_A3N	U3
M25	VOUT0_DATA3	ALDIO_A3P	V3
M26	VOUT0_DATA4	ALDIO_A4N	U4
U27	VOUT0_DATA5	ALDIO_A4P	V4
T27	VOUT0_DATA6	ALDIO_A5N	U5
T28	VOUT0_DATA7	ALDIO_A5P	V5
R27	VOUT0_DATA8	ALDIO_A6N	U6
R28	VOUT0_DATA9	ALDIO_A6P	V6
P27	VOUT0_DATA10	ALDIO_A7N	U7
P28	VOUT0_DATA11	ALDIO_A7P	V7
N27	VOUT0_DATA12	ALDIO_CLK0N	U8
N28	VOUT0_DATA13	ALDIO_CLK0P	V8
M27	VOUT0_DATA14	ALDIO_CLK1N	U9
M28	VOUT0_DATA15	ALDIO_CLK1P	V9

Sheet: /display/ File: display.kicad_sch		
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KiCad E.D.A. 9.0.4	Id: 8/12	

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F	1	2	3	4	5	6	7	8

V17	CSIO_RXCLKN
U17	CSIO_RXCLKP
V16	CSIO_RX0N
U16	CSIO_RX0P
V15	CSIO_RX1N
U15	CSIO_RX1P
U14	CSIO_RX2N
V14	CSIO_RX2P
U13	CSIO_RX3N
V13	CSIO_RX3P

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Size: A3	Date:	Rev:
KiCad E.D.A. 9.0.4	Id: 9/12	

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A								
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C								
D								
E								
F								

A24	OSPI0_CLK	OSPI0_D7	C28
C26	OSPI0_LBCLK0	OSPI0_D6	C27
D26	OSPI0_DQS	OSPI0_D5	B27
D24	OSPI0_CSNO	OSPI0_D4	B26
C28	OSPI0_CSN1	OSPI0_D3	A26
D24	OSPI0_CSN2	OSPI0_D2	A25
C24	OSPI0_CSN3	OSPI0_D1	A25
		OSPI0_D0	B24

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KiCad E.D.A. 9.0.4	Id: 10/12	

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Sheet: /debug_control/ File: debug_control.kicad_sch		
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KiCad E.D.A. 9.0.4	Id: 11/12	

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Sheet: /misc_reserved_nc/ File: misc_reserved_nc.kicad_sch		
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Size: A3	Date:	Rev:
KiCad E.D.A. 9.0.4	Id: 12/12	