

	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

osd62x-pm-power

File: untitled.kicad_sch

clocks & reset

File: clocksresets.kicad_sch

ddr & gpmc interface

File: ddr-gpmc.kicad_sch

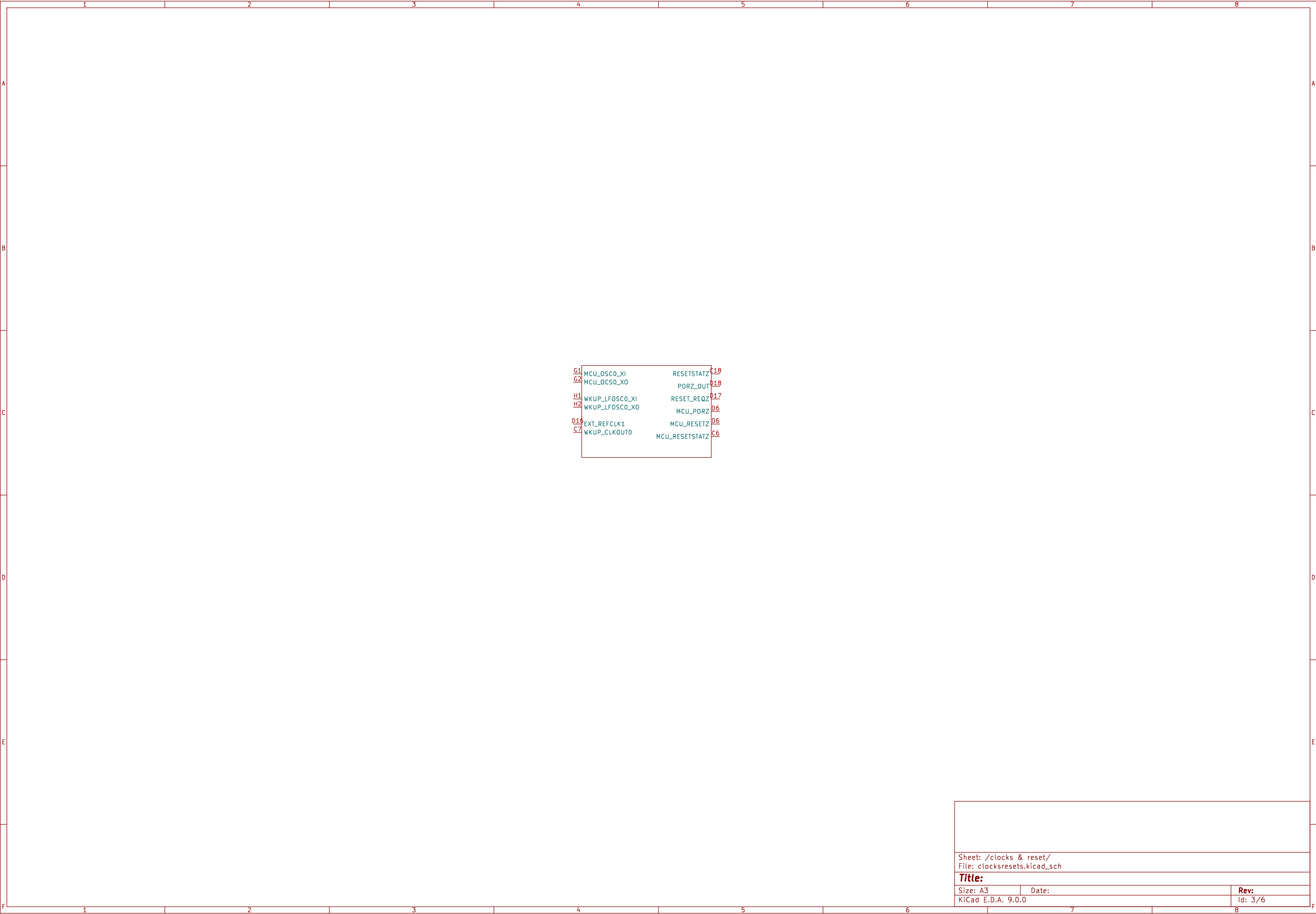
peripherals

File: peripherals.kicad_sch

rgmii-mdio

File: rgmii-mdio.kicad_sch

Sheet: /		
File: osd62x-pm-base.kicad_sch		
Title: osd62x-pm-base		
Size: A3	Date:	Rev: 0.1
KiCad E.D.A. 9.0.0	Id: 1/6	



	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

N10	DDR_VPP	GPMC0_ADVN_ALE	H25
F10	DDR_RSVD0	GPMC0_OEN_REN	H26
		GPMC0_WEN	J25
		GPMC0_BE0N_CLE	J26
G28	GPMC0_AD0	GPMC0_BE1N	K25
G27	GPMC0_AD1	GPMC0_CSN0	F26
F28	GPMC0_AD2	GPMC0_CSN1	F25
F27	GPMC0_AD3	GPMC0_CSN2	F26
E28	GPMC0_AD4	GPMC0_CSN3	E25
E27	GPMC0_AD5	GPMC0_WAIT0	L25
D28	GPMC0_AD6	GPMC0_WAIT1	L26
D27	GPMC0_AD7	GPMC0_CLK	K26
L27	GPMC0_AD8	GPMC0_DIR	E25
L28	GPMC0_AD9	GPMC0_WPN	E26
K27	GPMC0_AD10		
K28	GPMC0_AD11		
J28	GPMC0_AD12		
J27	GPMC0_AD13		
H28	GPMC0_AD14		
H27	GPMC0_AD15		

Sheet: /ddr & gpmc interface/ File: ddr-gpmc.kicad_sch		
Title:		
Size: A3	Date:	Rev:
KiCad E.D.A. 9.0.0	Id: 4/6	

1	2	3	4	5	6	7	8
A							
B							
C							
D							
E							
F	1	2	3	4	5	6	8

B10	UART0_TXD	MCAN0_RX	A11
A10	UART0_RXD	MCAN0_TX	B11
A9	UART0_CTSN		
B9	UART0_RTSN	MCU_MCAN0_RX	E1
		MCU_MCAN0_TX	E2
B3	MCU_UART0_TXD		
B2	MCU_UART0_RXD	MCU_MCAN1_RX	F1
B4	MCU_UART0_CTSN	MCU_MCAN1_TX	F2
A3	MCU_UART0_RTSN		
C1	WKUP_UART0_TXD	MCU_ERRORN	D7
C2	WKUP_UART0_RXD	MCU_PORZ	D6
D1	WKUP_UART0_CTSN	MCU_RESETZ	C5
A3	WKUP_UART0_RTSN	MCU_RESETSTATZ	C6
A12	I2C0_SCL		
B12	I2C0_SDA		
A13	I2C1_SCL		
B13	I2C1_SDA		
A7	MCU_I2C0_SCL		
B7	MCU_I2C0_SDA		
A8	WKUP_I2C0_SCL		
B8	WKUP_I2C0_SDA		
C11	SPI0_CLK		
D12	SPI0_D0		
C12	SPI0_D1		
D13	SPI0_CS0		
C13	SPI0_CS1		
A4	MCU_SPI0_CLK		
B5	MCU_SPI0_D0		
A5	MCU_SPI0_D1		
B6	MCU_SPI0_CS0		
A6	MCU_SPI0_CS1		

Sheet: /peripherals/ File: peripherals.kicad_sch		
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Size: A3	Date:	Rev:
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