

UVM Connect

Part 3 – Converters

Adam Erickson Verification Technologist

academy@mentor.com www.verificationacademy.com



UVM Connect Presentation Series

Part 1 – UVMC Introduction

- Learn what UVMC is and why you need it
- Review the principles behind the TLM1 and TLM2 standards
- Review basic port/export/interface connections in both SC and SV

Part 2 – UVMC Connections

 Learn how to establish connections between TLM-based components in SC and SV

Part 3 – UVMC Converters

 Learn how to write the converters that are needed to transfer transaction data across the language boundary

Part 4 – UVMC Command API

 Learn how to access and control key aspects of UVM simulation from SystemC

Agenda

Behind the scenes

A look at how the converters are used

Default Converters

For SC and SV; Built in support for tlm_generic_payload

SV Converter Options

Implement inside or outside transaction class

SC Converter Options

- Implement inside or outside transaction class
- · Adapting while converting

Agenda

Behind the scenes

A look at how the converters are used

Default Converters

For SC and SV; Built in support for tlm_generic_payload

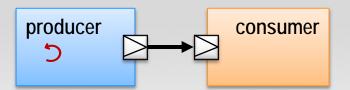
SV Converter Options

Implement inside or outside transaction class

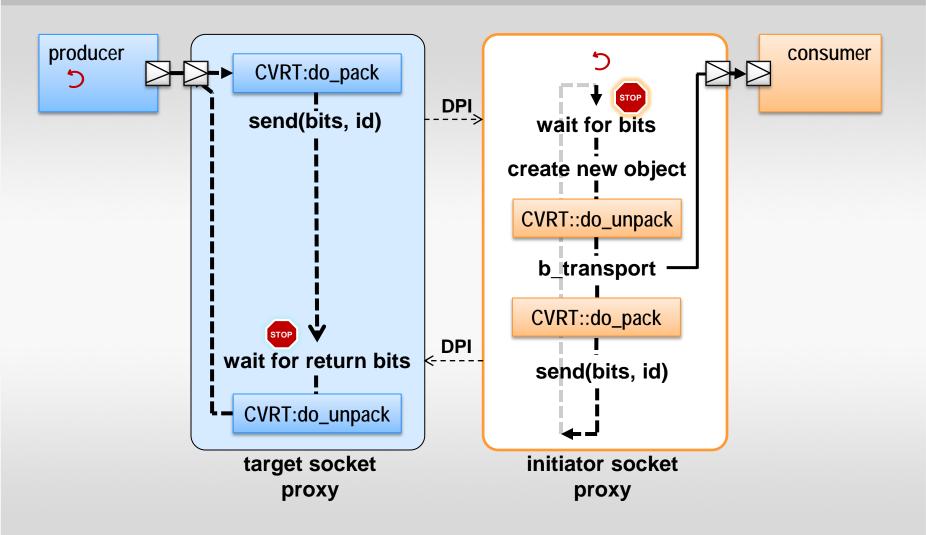
SC Converter Options

- Implement inside or outside transaction class
- Adapting while converting

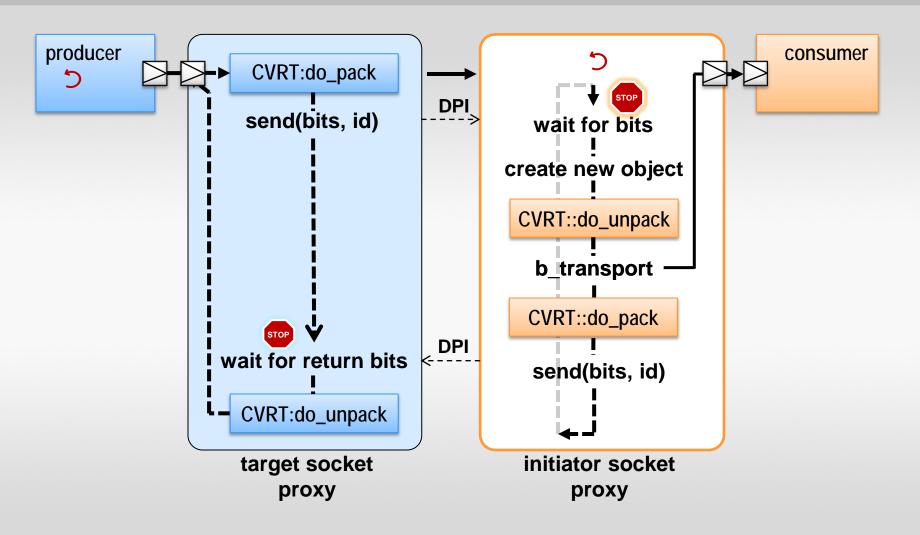
Behind the Scenes



Behind the Scenes



Behind the Scenes



Agenda =

Behind the scenes

A look at how the converters are used

Default Converters & Type Support

- For SC and SV
- Built in support for tlm_generic_payload

SV Converter Options

Implement inside or outside transaction class

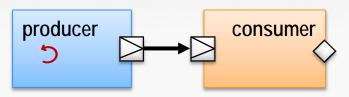
SC Converter Options

- Implement inside or outside transaction class
- Adapting while converting

Built in Support for Generic Payload

Full TLM GP support is built into the libraries

- Components using TLM GP with base protocol are easist to integrate, most interoperable
- In most cases TLM GP is all you should ever need and use



```
import uvm pkq::*;
                                    #include "uvmc.h"
import uvmc_pkg::*;
                                    using namespace uvmc;
                                    #include "consumer.h"
`include "producer.sv"
                                    int sc_main(int argc, char* argv[])
module sv main;
producer prod = new("prod");
 initial begin
                                      consumer cons("cons");
  uvmc_tlm #()::connect(prod.out,
                                      uvmc connect(cons.in, "foo");
                          "foo");
                                      sc start();
                                      return 0;
  run test()
                    Uses default
 end
               uvm_tlm_generic_payload
endmodule
```

Type Support

Category	SV	SC
Signed integrals	longint int shortint byte bit	long long* int short char bool
Unsigned integrals	longint unsigned int unsigned shortint unsigned byte unsigned bit unsigned	unsigned long long* unsigned int unsigned short unsigned char bool
Misc	shortreal real string time enum	float double string sc_time enum
Arrays	T arr[N] T q[\$] T da[] T aa[KEY]	T arr[N] vector <t> list<t> map<key,t></key,t></t></t>
Bit vectors	bit logic bit [L:R] logic [L:R]	sc_bit sc_logic sc_bv <n> sc_lv<n></n></n>
SC integers	bit [N-1:0] bit [N-1:0] bit [N-1:0] bit [N-1:0]	sc_int <n> sc_uint<n> sc_bigint<n> sc_biguint<n></n></n></n></n>

The UVM uvm_packer and UVMC uvmc_packer support these types directly.

Other types can be accommodated in your converters

Sub-objects supported.

Just call their
converter methods directly

On 32-bit machines, SC long long is 32 bits. SV longints are always 64 bits regardless of architecture

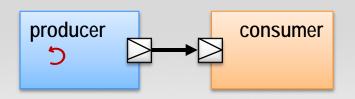
Transaction Used in non-GP Examples

3 Data Members:

- Command
 - 32-bit enum: { READ, WRITE, NOOP }
- Address
 - 32-bit Integer
- Data
 - Variable array of bytes

Default Converters

Both delegate to T.pack/unpack



The SC default is rarely appropriate.

```
template <typename T>
class uvmc_default_converter
                                           class uvmc_converter {
  #(type T=int) extends
                                             public:
     uvmc_converter #(T);
                                             static void
static function
                                                do pack(const T &t,
 void do pack (T t,
                                                        uvmc_packer &packer) {
                uvm packer packer);
                                                t.do_pack(packer);
    t.pack(packer);
endfunction
                                             static void
static function
                                               do unpack(T &t,
  void do_unpack (T t,
                                                         uvmc packer &packer) {
                  uvm_packer packer);
                                                t.do unpack(packer);
    t.unpack(packer);
endfunction
                                           };
endclass
```

Agenda

- Behind the scenes
 - A look at how the converters are used
- Default Converters
 - For SC and SV; Built in support for tlm_generic_payload
- SV Converter Options
 - Implement inside or outside transaction class



- SC Converter Options
 - Implement inside or outside transaction class
 - Adapting while converting

SV Conversion – do_pack/unpack

```
class packet extends uvm sequence item;
                                                                    extends
  typedef enum { WRITE, READ, NOOP } cmd_t;
                                                               uvm_sequence_item
  rand cmd_t cmd;
  rand int addr;
  rand byte data[$];
                                                                      conversion
                                                                     functions are
  function new(string name="");
                                                                    implemented as
    super.new(name);
                                                                    methods of the
                                                                   transaction class
  endfunction
  `uvm_object_utils(packet)
  virtual function void do pack (uvm packer packer);
                                                                     define do_pack
    `uvm pack enum(cmd)
                                                                   using `uvm_pack_*
    `uvm pack int(addr)
                                                                        macros
    `uvm_pack_queue(data)
  endfunction
  virtual function void do_unpack (uvm_packer packer);
                                                                    define do_unpack
    `uvm_unpack_enum(cmd,cmd_t)
                                                                         using
    `uvm_unpack_int(addr)
                                                                     `uvm_unpack_*
    `uvm unpack queue(data)
                                                                        macros
  endfunction
endclass
```

SV Conversion - Field Macros

```
class packet extends uvm sequence item; ←
  typedef enum { WRITE, READ, NOOP } cmd_t;
 rand cmd t cmd;
 rand int addr;
 rand byte data[$];
 function new(string name="");
    super.new(name);
  endfunction
  `uvm_object_utils_begin(packet)
    `uvm field enum(cmd t,cmd,UVM ALL ON)
    `uvm field int(addr,UVM ALL ON)
    `uvm field queue int(data,UVM ALL ON)
  `uvm_object_utils_end
```

extends uvm_sequence_item

conversion functions are implemented via `uvm_field macros

`uvm_field macros degrade overall performance

> `uvm_field macros hinder debug

SV Conversion – External Converter

```
class packet;
                                                                  no base class
  typedef enum { WRITE, READ, NOOP } cmd_t;
                                                                (or doesn't extend
  rand cmd t cmd;
                                                                  uvm_object)
  rand int addr;
  rand byte data[$];
                                                                   custom converter
endclass
                                                                     must extend
                                                                  uvmc_converter #(T)
class convert_packet extends uvmc_converter #(packet);
  static function void do_pack(packet t, uvm_packer packer); 
                                                                       define do_pack
    `uvm pack enum(t.cmd)
    `uvm_pack_int(t.addr)
                                                        using same `uvm pack * macros
    `uvm pack queue(t.data)
                                                         as for in-transaction do_pack
  endfunction
  static function void do_unpack(packet t, uvm_packer packer);
                                                                      define do_unpack
    `uvm unpack enum(t.cmd,packet::cmd t)
    `uvm_unpack_int(t.addr)
    `uvm unpack queue(t.data)
  endfunction
                                                      using same `uvm_unpack_* macros
endclass
                                                       as for in-transaction do_unpack
```

Agenda

- Behind the scenes
 - A look at how the converters are used
- Default Converters
 - For SC and SV; Built in support for tlm_generic_payload
- SV Converter Options
 - Implement inside or outside transaction class
- SC Converter Options
 - Implement inside or outside transaction class
 - · Adapting while converting



SC Converter Specialization

```
class packet {
                                                                       no base class.
  enum cmd t { WRITE=0, READ, NOOP };
                                                                     no dependencies.
  cmd t cmd;
  unsigned int addr;
  vector<unsigned char> data;
                                                                      Define template
};
                                                                      specialization of
                                                                    uvmc_converter<T>
template <> ◆
struct uvmc_converter<packet_base> {
                                                                 Define do_pack.
  static void do pack (const packet_base &t,
                                                           Stream from data members into
                         uvmc packer &packer) {
                                                              packer using operator <<
    packer << t.cmd << t.addr << t.data;</pre>
                                                                Define do_unpack.
  static void do unpack (packet base &t,
                                                           Stream into data members from
                           uvmc_packer &packer) {
                                                             packer using operator >>
    packer >> t.cmd >> t.addr >> t.data;
};
```

Conversion is simple: just "stream" all your fields to/from the packer.

SC Converter Specialization - Macros

```
class packet {
  enum cmd_t { WRITE=0, READ, NOOP };
  cmd t cmd;
  unsigned int addr;
  vector<unsigned char> data;
};
template <> struct uvmc_converter<packet> {
  static void do pack(const packet &t, uvmc packer &packer) {
   packer << t.cmd << t.addr << t.data;</pre>
  static void do_unpack(packet &t, uvmc_packer &packer) {
   packer >> t.cmd >> t.addr >> t.data;
};
UVMC_UTILS_3(packet,cmd,addr,data)
```

These are "good" macros—they expand into code that <u>you</u> would write

define this exact template specialization of uvmc_converter<T> by invoking one UVMC_UTILS_x macro (x = 1 to 20)

SC Converter – In Class

```
class packet {
  enum cmd_t { WRITE=0, READ, NOOP };
  cmd t cmd;
  unsigned int addr;
 vector<unsigned char> data;
 virtual void do pack(uvmc packer &packer) const {
   packer << cmd << addr << data;</pre>
 virtual void do_unpack(uvmc_packer &packer) {
   packer >> cmd >> addr >> data;
};
```

Although easy with UVMC, this is uncommon, as C++ affords better decoupling through separate converter classes.

 Question: What if your SC and SV transactions are already fixed (and <u>very</u> different)?

SV

```
class packet extends uvm_sequence;
  typedef enum { WRITE, READ, NOOP } cmd_t;
  rand cmd_t cmd;
  rand int addr;
  rand byte data[$];
endclass
```

SC

```
class packet {
   short addr_hi;
   short addr_lo;
   unsigned int payload[4];
   char len;
   bool write;
};
```

- Answer: Leverage the SC converter to convert AND adapt
 - Define SV converter "normally"
 - Options are limited
 - Define custom SC converter
 - that derives from uvmc_converter<T>

Step 1: Do SV conversion as usual

```
class packet extends uvm sequence item;
   typedef enum { WRITE, READ, NOOP } cmd_t;
  rand cmd t cmd;
  rand int addr;
  rand byte data[$];
   `uvm object utils(packet)
   function new(string name="");
     super.new(name);
  endfunction
  virtual function void do pack(uvm_packer pack
     `uvm pack enum(cmd)
     `uvm_pack_int(addr)
     `uvm pack queue(data)
  endfunction
  virtual function void do unpack(uvm packer pa
     `uvm unpack enum(cmd,cmd t)
     `uvm unpack int(addr)
     `uvm unpack queue(data)
  endfunction
 endclass
```

 Step 2: SC converter converts and adapts

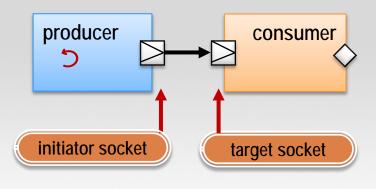
SV cmd enum → SC bool

SV byte queue → SC int [4]

Adjust different streaming order

```
struct custom packet cvrt : public uvmc converter<packet> {
  static void do pack(const packet &t, uvmc packer &packer) {
    int cmd tmp;
    cmd_tmp = write ? 0 : 1;
   packer << cmd_tmp << t.addr_lo << t.addr_hi</pre>
           << (int)(t.len) << t.payload << (int)0;
  static void do_unpack(packet &t, uvmc_packer &packer) {
    int cmd tmp;
   vector<unsigned char> data tmp;
   packer >> cmd_tmp >> t.addr_lo >> t.addr_hi >> data_tmp;
   t.len = data_tmp.size();
    t.write = (cmd_tmp == 0) ? 1 : 0;
    for (int i=0;i<4;i++) {
     t.payload[i]=0;
      for (int j=0; j<4; j++) {
        if ((i*4+j)<t.len) {
          int_tmp = data_tmp[i*4+j] << (8*j);</pre>
          t.payload[i] = t.payload[i] | int_tmp;
        else break;
};
```

Step 3:
 Specify
 custom
 converter
 when calling
 connect on
 SC side



```
import uvm pkq::*;
                                   #include "uvmc.h"
import uvmc_pkg::*;
                                   using namespace uvmc;
                                   #include "consumer.h"
`include "producer.sv"
                                    int sc_main(int argc, char* argv[])
module sv main;
producer prod = new("prod");
                                      consumer cons("consumer");
 initial begin
  uvmc tlm #()::
                                      uvmc_connect<custom_packet_cvrt>
                                          (cons->in, "foo");
      connect(prod.out, "foo");
  run test();
                                      sc start();
 end
                                      return 0;
endmodule
```

UVM Connect Presentation Series

Part 1 – UVMC Introduction

- Learn what UVMC is and why you need it
- Review the principles behind the TLM1 and TLM2 standards
- Review basic port/export/interface connections in both SC and SV

Part 2 – UVMC Connections

 Learn how to estblish connections between TLM-based components in SC and SV

Part 3 – UVMC Converters

 Learn how to write the converters that are needed to transfer transaction data across the language boundary

Part 4 – UVMC Command API

 Learn how to access and control key aspects of UVM simulation from SystemC



UVM Connect

Part 3 – Converters

Adam Erickson Verification Technologist

academy@mentor.com www.verificationacademy.com

