Josh Noble

Hardware-savvy AI Engineer

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Summary

Hardware-oriented AI engineer with five years at AMD delivering high-volume GPU silicon and reducing wafer-test debug time from 3h to 30min through LLM automation. I build production Python/C++ data pipelines, deploy FastAPI services, and launch products—most recently a rewards tool that drew 5,000 users on day one. Seeking senior software or AI-engineering roles where deep systems intuition meets modern ML infrastructure.

Experience

2021–Present **Senior Product Development Engineer**, *Advanced Micro Devices (AMD)*, Austin, TX

- Integrated retrieval-augmented LLM agents into Advantest 93K ATE workflows, reducing typical wafer-test debug cycles from 3h to 30min.
- Vectorized 13K+ engineering documents and exposed a FastAPI + ChromaDB semantic-search service now used across product development teams.
- Built an automated document validation and generation pipeline that improves LLM ingestion quality and consistency.

2023–May 2025 **Co-Founder & COO**, *OneCard (inactive since May 2025)*, Austin, TX / Remote

- Shipped public rewards-ranking tool (rewards.getonecard.io) → 5K sign-ups in the first 24h; now serves 1K+ API calls/mo.
- Led product roadmap and growth experiments; secured support from Antler Residency and RIT Venture Creations incubator.

2025–Present **Founder**, *SentarAI (stealth)*, Remote

- Developing a Python agent-orchestration framework (LangGraph, FastAPI) to automate complex personal and enterprise workflows.
- Maintain an on-prem Mistral LLM stack in Docker; experimenting with retrieval-augmented generation and fine-tuning.

Early Technical Experience Exelon Generation (Electrical Eng. Intern '17–'18) • Council Rock (HW Eng. Intern '19) • AMD (Reliability Eng. Intern '20)

Selected Projects

Smart Shed (IoT/Edge) Designed off-grid composting system with MCU sensor network, solar PMICs, Python/MQTT telemetry, and lightweight anomaly-detection model for automated feed control.

Equipment Technician Serviced and repaired centrifuges, autoclaves, and microscopes in a rural Guatemala hospital; restored critical devices to service and trained local staff.

Nand2Tetris Completed logic-to-OS course projects, reinforcing computer-systems design fundamentals.

Education

2016–2021 **B.S. Electrical Engineering**, *Rochester Institute of Technology — Honors College, Presidential Scholar, Sigma Chi Fraternity*, Rochester, NY

Skills

Languages: Python, C++, Ruby, SQL, Typescript **AI/ML**: OpenAI & Mistral APIs, RAG, LangGraph, ChromaDB **Systems**: Linux, Git, FastAPI, Redis, PostgreSQL, MongoDB, **Hardware**: Advantest 93K ATE, FPGAs (Verilog/VHDL), Embedded C, PCB design (KiCad), SPI/I²C/UART, high-speed digital debug