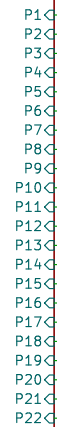
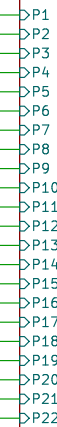


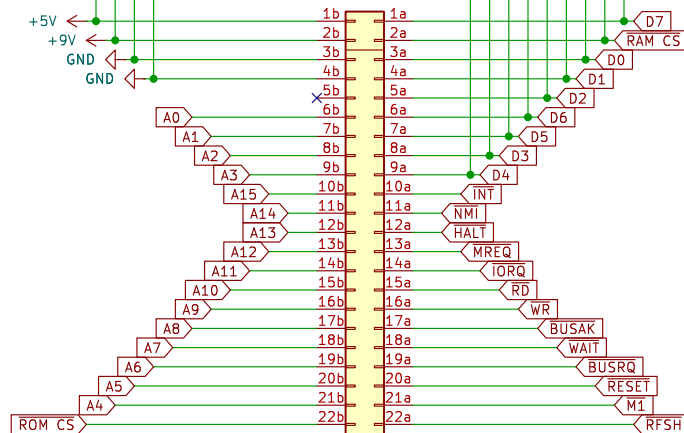
File: Logic.kicad_sch



Memory Board



File: Memory Array.kicad_sch



J1

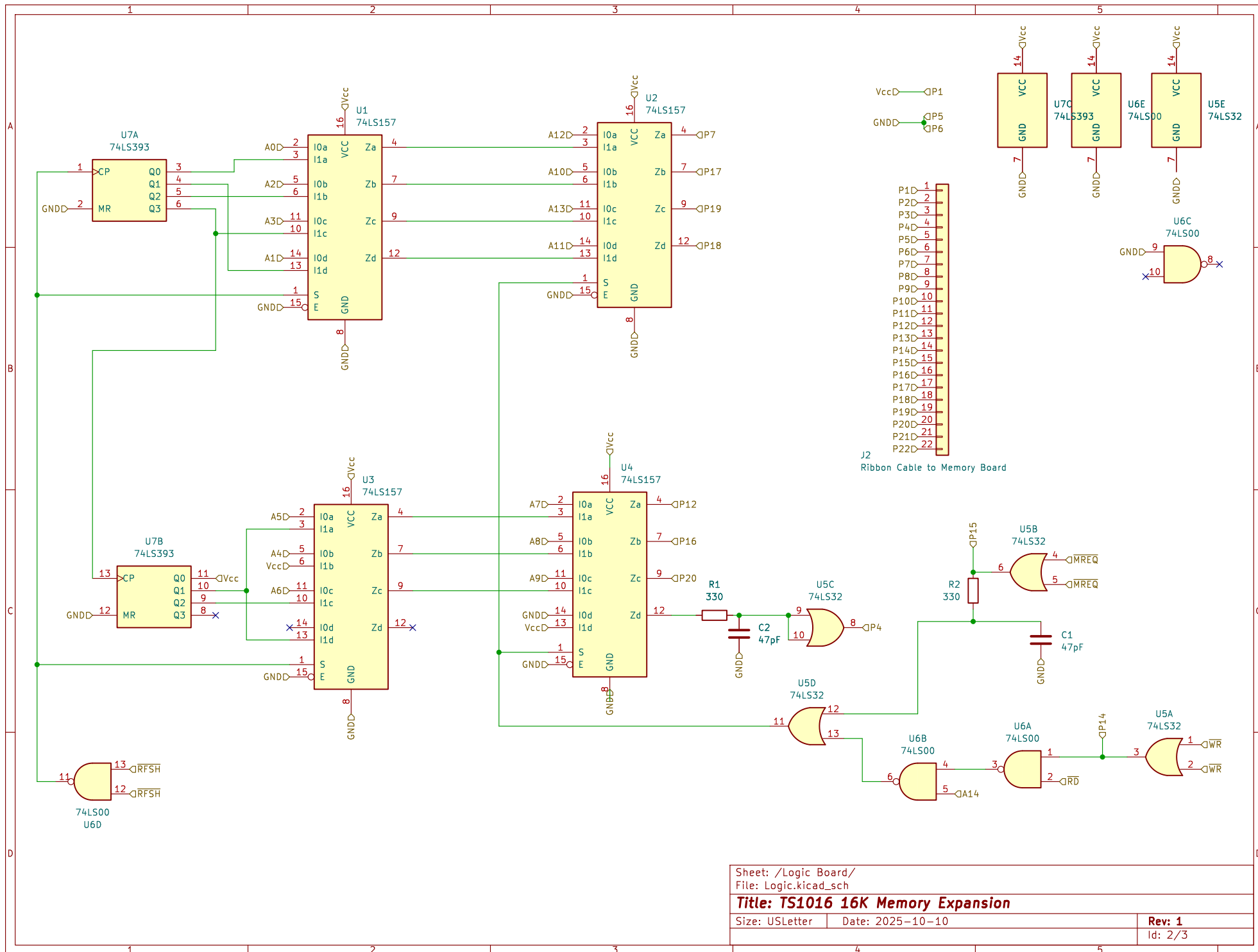
Card Edge Connector to TS 1000

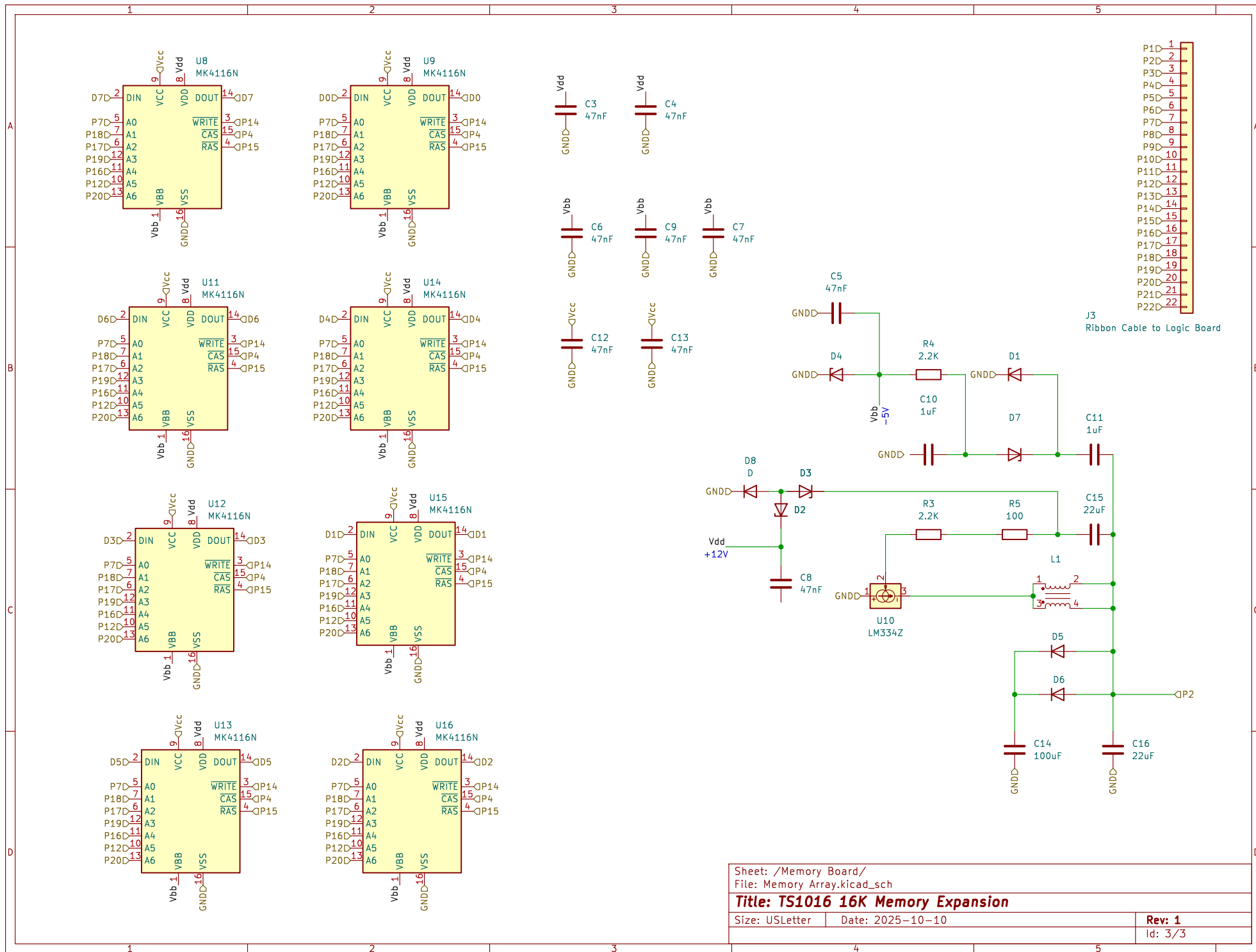
Sheet: /
File: TS1016.kicad_sch

Title: TS1016 16K Memory Expansion

Size: USLetter Date: 2025-10-10

Rev: 1
Id: 1/3





Sheet: /Memory Board/ File: Memory Array.kicad_sch		
Title: TS1016 16K Memory Expansion		
Size: USLetter	Date: 2025-10-10	Rev: 1
		Id: 3/3