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1 // https://github.com/joachim4/3096S-Assignments/blob/main/3096-Pracs-MDLSAY006-
  GNGJOA003/Prac3/main.c
2
3
4 /* USER CODE BEGIN Header */
5 /**
6  *****
7  * @file      : main.c
8  * @brief     : Main program body
9  *****
10 * @attention
11 *
12 * Copyright (c) 2023 STMicroelectronics.
13 * All rights reserved.
14 *
15 * This software is licensed under terms that can be found in the LICENSE file
16 * in the root directory of this software component.
17 * If no LICENSE file comes with this software, it is provided AS-IS.
18 *
19 *****
20 */
21 /* USER CODE END Header */
22 /* Includes -----*/
23 #include "main.h"
24
25 /* Private includes -----*/
26 /* USER CODE BEGIN Includes */
27 #include <stdio.h>
28 #include "stm32f0xx.h"
29 #include <lcd_stm32f0.c>
30 /* USER CODE END Includes */
31
32 /* Private typedef -----*/
33 /* USER CODE BEGIN PTD */
34
35 /* USER CODE END PTD */
36
37 /* Private define -----*/
38 /* USER CODE BEGIN PD */
39
40 /* USER CODE END PD */
41
42 /* Private macro -----*/
43 /* USER CODE BEGIN PM */
44
45 /* USER CODE END PM */
46
47 /* Private variables -----*/
48 ADC_HandleTypeDef hadc;
49 TIM_HandleTypeDef htim3;
50
51 /* USER CODE BEGIN PV */
52 uint32_t prev_millis = 0;
53 uint32_t curr_millis = 0;
54 uint32_t delay_t = 500; // Initialise delay to 500ms
55 uint32_t adc_val;
56 /* USER CODE END PV */

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57
58 /* Private function prototypes -----*/
59 void SystemClock_Config(void);
60 static void MX_GPIO_Init(void);
61 static void MX_ADC_Init(void);
62 static void MX_TIM3_Init(void);
63
64 /* USER CODE BEGIN PFP */
65 void EXTI0_1_IRQHandler(void);
66 void writeLCD(char *char_in);
67 uint32_t pollADC(void);
68 uint32_t ADCToCCR(uint32_t adc_val);
69 /* USER CODE END PFP */
70
71 /* Private user code -----*/
72 /* USER CODE BEGIN 0 */
73
74 /* USER CODE END 0 */
75
76 /**
77  * @brief The application entry point.
78  * @retval int
79  */
80 int main(void)
81 {
82     /* USER CODE BEGIN 1 */
83     /* USER CODE END 1 */
84
85     /* MCU Configuration-----*/
86
87     /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
88     HAL_Init();
89
90     /* USER CODE BEGIN Init */
91     /* USER CODE END Init */
92
93     /* Configure the system clock */
94     SystemClock_Config();
95
96     /* USER CODE BEGIN SysInit */
97     /* USER CODE END SysInit */
98
99     /* Initialize all configured peripherals */
100     MX_GPIO_Init();
101     MX_ADC_Init();
102     MX_TIM3_Init();
103
104     /* USER CODE BEGIN 2 */
105     init_LCD();
106
107     // PWM setup
108     uint32_t CCR = 0;
109     HAL_TIM_PWM_Start(&htim3, TIM_CHANNEL_3); // Start PWM on TIM3 Channel 3
110     /* USER CODE END 2 */
111
112     /* Infinite loop */
113     /* USER CODE BEGIN WHILE */

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114 while (1)
115 {
116     curr_millis = HAL_GetTick();
117     // Get the time as soon as the button is clicked
118     if (curr_millis - prev_millis >= 100){
119         HAL_GPIO_TogglePin(GPIOB, LED7_Pin);
120         prev_millis = curr_millis;
121     }
122
123     // ADC to LCD; TODO: Read POT1 value and write to LCD
124
125     // Read ADC value using the given function
126     adc_val = pollADC();
127
128     // Get string of ADC value to print to LCD
129     char adc_line[16];
130     snprintf(adc_line, sizeof(adc_line), "ADC Value: %lu", adc_val);
131
132     // Display ADC value on the LCD
133     writeLCD(adc_line);
134
135     // Update PWM value; TODO: Get CRR
136     uint32_t CCR = ADCtoCCR(adc_val);
137
138     __HAL_TIM_SetCompare(&htim3, TIM_CHANNEL_3, CCR);
139
140     // Wait for delay ms
141     HAL_Delay (delay_t);
142     /* USER CODE END WHILE */
143
144     /* USER CODE BEGIN 3 */
145 }
146 /* USER CODE END 3 */
147 }
148
149 /**
150  * @brief System Clock Configuration
151  * @retval None
152  */
153 void SystemClock_Config(void)
154 {
155     LL_FLASH_SetLatency(LL_FLASH_LATENCY_0);
156     while(LL_FLASH_GetLatency() != LL_FLASH_LATENCY_0)
157     {
158     }
159     LL_RCC_HSI_Enable();
160
161     /* Wait till HSI is ready */
162     while(LL_RCC_HSI_IsReady() != 1)
163     {
164     }
165     LL_RCC_HSI_SetCalibTrimming(16);
166     LL_RCC_HSI14_Enable();
167
168     /* Wait till HSI14 is ready */
169     while(LL_RCC_HSI14_IsReady() != 1)
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171 {
172
173 }
174 LL_RCC_HSI14_SetCalibTrimming(16);
175 LL_RCC_SetAHBPrescaler(LL_RCC_SYSCLK_DIV_1);
176 LL_RCC_SetAPB1Prescaler(LL_RCC_APB1_DIV_1);
177 LL_RCC_SetSysClkSource(LL_RCC_SYS_CLKSOURCE_HSI);
178
179 /* Wait till System clock is ready */
180 while(LL_RCC_GetSysClkSource() != LL_RCC_SYS_CLKSOURCE_STATUS_HSI)
181 {
182
183 }
184 LL_SetSystemCoreClock(8000000);
185
186 /* Update the time base */
187 if (HAL_InitTick (TICK_INT_PRIORITY) != HAL_OK)
188 {
189     Error_Handler();
190 }
191 LL_RCC_HSI14_EnableADCControl();
192 }
193
194 /**
195  * @brief ADC Initialization Function
196  * @param None
197  * @retval None
198  */
199 static void MX_ADC_Init(void)
200 {
201
202     /* USER CODE BEGIN ADC_Init 0 */
203     /* USER CODE END ADC_Init 0 */
204
205     ADC_ChannelConfTypeDef sConfig = {0};
206
207     /* USER CODE BEGIN ADC_Init 1 */
208
209     /* USER CODE END ADC_Init 1 */
210
211     /** Configure the global features of the ADC (Clock, Resolution, Data Alignment and number
    of conversion)
212     */
213     hadc.Instance = ADC1;
214     hadc.Init.ClockPrescaler = ADC_CLOCK_ASYNC_DIV1;
215     hadc.Init.Resolution = ADC_RESOLUTION_12B;
216     hadc.Init.DataAlign = ADC_DATAALIGN_RIGHT;
217     hadc.Init.ScanConvMode = ADC_SCAN_DIRECTION_FORWARD;
218     hadc.Init.EOCSelection = ADC_EOC_SINGLE_CONV;
219     hadc.Init.LowPowerAutoWait = DISABLE;
220     hadc.Init.LowPowerAutoPowerOff = DISABLE;
221     hadc.Init.ContinuousConvMode = DISABLE;
222     hadc.Init.DiscontinuousConvMode = DISABLE;
223     hadc.Init.ExternalTrigConv = ADC_SOFTWARE_START;
224     hadc.Init.ExternalTrigConvEdge = ADC_EXTERNALTRIGCONVEDGE_NONE;
225     hadc.Init.DMAContinuousRequests = DISABLE;
226     hadc.Init.Overrun = ADC_OVR_DATA_PRESERVED;

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227 if (HAL_ADC_Init(&hadc) != HAL_OK)
228 {
229     Error_Handler();
230 }
231
232 /** Configure for the selected ADC regular channel to be converted.
233 */
234 sConfig.Channel = ADC_CHANNEL_6;
235 sConfig.Rank = ADC_RANK_CHANNEL_NUMBER;
236 sConfig.SamplingTime = ADC_SAMPLETIME_1CYCLE_5;
237 if (HAL_ADC_ConfigChannel(&hadc, &sConfig) != HAL_OK)
238 {
239     Error_Handler();
240 }
241 /* USER CODE BEGIN ADC_Init 2 */
242 ADC1->CR |= ADC_CR_ADCAL;
243 while(ADC1->CR & ADC_CR_ADCAL);           // Calibrate the ADC
244 ADC1->CR |= (1 << 0);                     // Enable ADC
245 while((ADC1->ISR & (1 << 0)) == 0);        // Wait for ADC ready
246 /* USER CODE END ADC_Init 2 */
247
248 }
249
250 /**
251  * @brief TIM3 Initialization Function
252  * @param None
253  * @retval None
254  */
255 static void MX_TIM3_Init(void)
256 {
257
258     /* USER CODE BEGIN TIM3_Init 0 */
259
260     /* USER CODE END TIM3_Init 0 */
261
262     TIM_ClockConfigTypeDef sClockSourceConfig = {0};
263     TIM_MasterConfigTypeDef sMasterConfig = {0};
264     TIM_OC_InitTypeDef sConfigOC = {0};
265
266     /* USER CODE BEGIN TIM3_Init 1 */
267
268     /* USER CODE END TIM3_Init 1 */
269     htim3.Instance = TIM3;
270     htim3.Init.Prescaler = 0;
271     htim3.Init.CounterMode = TIM_COUNTERMODE_UP;
272     htim3.Init.Period = 47999;
273     htim3.Init.ClockDivision = TIM_CLOCKDIVISION_DIV1;
274     htim3.Init.AutoReloadPreload = TIM_AUTORELOAD_PRELOAD_DISABLE;
275     if (HAL_TIM_Base_Init(&htim3) != HAL_OK)
276     {
277         Error_Handler();
278     }
279     sClockSourceConfig.ClockSource = TIM_CLOCKSOURCE_INTERNAL;
280     if (HAL_TIM_ConfigClockSource(&htim3, &sClockSourceConfig) != HAL_OK)
281     {
282         Error_Handler();
283     }
284 }
```

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284 if (HAL_TIM_PWM_Init(&htim3) != HAL_OK)
285 {
286     Error_Handler();
287 }
288 sMasterConfig.MasterOutputTrigger = TIM_TRGO_RESET;
289 sMasterConfig.MasterSlaveMode = TIM_MASTERSLAVEMODE_DISABLE;
290 if (HAL_TIMEx_MasterConfigSynchronization(&htim3, &sMasterConfig) != HAL_OK)
291 {
292     Error_Handler();
293 }
294 sConfigOC.OCMode = TIM_OCMODE_PWM1;
295 sConfigOC.Pulse = 0;
296 sConfigOC.OCpolarity = TIM_OCPOLARITY_HIGH;
297 sConfigOC.OCFastMode = TIM_OCFAST_DISABLE;
298 if (HAL_TIM_PWM_ConfigChannel(&htim3, &sConfigOC, TIM_CHANNEL_3) != HAL_OK)
299 {
300     Error_Handler();
301 }
302 /* USER CODE BEGIN TIM3_Init 2 */
303
304 /* USER CODE END TIM3_Init 2 */
305 HAL_TIM_MspPostInit(&htim3);
306
307 }
308
309 /**
310  * @brief GPIO Initialization Function
311  * @param None
312  * @retval None
313  */
314 static void MX_GPIO_Init(void)
315 {
316     LL_EXTI_InitTypeDef EXTI_InitStruct = {0};
317     LL_GPIO_InitTypeDef GPIO_InitStruct = {0};
318 /* USER CODE BEGIN MX_GPIO_Init_1 */
319 /* USER CODE END MX_GPIO_Init_1 */
320
321 /* GPIO Ports Clock Enable */
322 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOF);
323 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOA);
324 LL_AHB1_GRP1_EnableClock(LL_AHB1_GRP1_PERIPH_GPIOB);
325
326 /**/
327 LL_GPIO_ResetOutputPin(LED7_GPIO_Port, LED7_Pin);
328
329 /**/
330 LL_SYSCFG_SetEXTISource(LL_SYSCFG_EXTI_PORTA, LL_SYSCFG_EXTI_LINE0);
331
332 /**/
333 LL_GPIO_SetPinPull(Button0_GPIO_Port, Button0_Pin, LL_GPIO_PULL_UP);
334
335 /**/
336 LL_GPIO_SetPinMode(Button0_GPIO_Port, Button0_Pin, LL_GPIO_MODE_INPUT);
337
338 /**/
339 EXTI_InitStruct.Line_0_31 = LL_EXTI_LINE_0;
340 EXTI_InitStruct.LineCommand = ENABLE;

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341 EXTI_InitStruct.Mode = LL_EXTI_MODE_IT;
342 EXTI_InitStruct.Trigger = LL_EXTI_TRIGGER_RISING;
343 LL_EXTI_Init(&EXTI_InitStruct);
344
345 /**/
346 GPIO_InitStruct.Pin = LED7_Pin;
347 GPIO_InitStruct.Mode = LL_GPIO_MODE_OUTPUT;
348 GPIO_InitStruct.Speed = LL_GPIO_SPEED_FREQ_LOW;
349 GPIO_InitStruct.OutputType = LL_GPIO_OUTPUT_PUSH_PULL;
350 GPIO_InitStruct.Pull = LL_GPIO_PULL_NO;
351 LL_GPIO_Init(LED7_GPIO_Port, &GPIO_InitStruct);
352
353 /* USER CODE BEGIN MX_GPIO_Init_2 */
354 HAL_NVIC_SetPriority(EXTI0_1_IRQn, 0, 0);
355 HAL_NVIC_EnableIRQ(EXTI0_1_IRQn);
356 /* USER CODE END MX_GPIO_Init_2 */
357 }
358
359 /* USER CODE BEGIN 4 */
360 void EXTI0_1_IRQHandler(void)
361 {
362     // TODO: Add code to switch LED7 delay frequency
363
364     curr_millis = HAL_GetTick(); // GetTick fn gives us the current system time elapsed
365
366     if (__HAL_GPIO_EXTI_GET_IT(Button0_Pin) != 0)
367     {
368         if (curr_millis - prev_millis >= 150) // Checked to see if enough time has passed
369             since the previous time the button was clicked
370             {
371                 // Toggle the LED frequency between 1 Hz and 2 Hz
372                 if (delay_t == 500)
373                 {
374                     delay_t = 1000; // 1000 delay equates to 1 Hz
375                 }
376                 else
377                 {
378                     delay_t = 500; // 500 delay equates to 2 Hz
379                 }
380
381                 prev_millis = curr_millis; // Save that old system time to be compared later
382             }
383     }
384
385     HAL_GPIO_EXTI_IRQHandler(Button0_Pin); // Clear interrupt flags
386 }
387
388 // TODO: Complete the writeLCD function
389 void writeLCD(char *char_in){
390     delay(3000);
391     lcd_command(CLEAR);
392     lcd_putstring(char_in);
393 }
394
395 // Get ADC value
396 uint32_t pollADC(void){

```

```
397 // TODO: Complete function body to get ADC val
398
399     // We used the HAL Adc functions to start, convert and stop the adc
400
401     HAL_ADC_Start(&hadc);
402     HAL_ADC_PollForConversion(&hadc, HAL_MAX_DELAY);
403     uint32_t val = HAL_ADC_GetValue(&hadc);
404     HAL_ADC_Stop(&hadc);
405
406     return val;
407 }
408
409 // Calculate PWM CCR value - Capture/Compare Register
410 uint32_t ADCToCCR(uint32_t adc_val){
411     // TODO: Calculate CCR val using an appropriate equation
412
413     // Since the ADC configured to 12-bit mode, input ADC integer is between 0-4095
414     // While the Capture/Compare Register requires value in range 0-ARR(max)
415     // to have control on the PWM duty cycle
416
417     uint32_t ADCvalueRange = 4095;
418     uint32_t ARRvalueRange = 47999;
419
420     // Corresponds to 1kHz frequency for PWM signal
421     // With Duty cycle = CCR/ARR
422
423     // Equation to calculate appropriate CCR value
424     uint32_t val = (adc_val * ARRvalueRange) / ADCvalueRange;
425
426     return val;
427 }
428
429 void ADC1_COMP_IRQHandler(void)
430 {
431     adc_val = HAL_ADC_GetValue(&hadc); // read adc value
432     HAL_ADC_IRQHandler(&hadc); //Clear flags
433 }
434 /* USER CODE END 4 */
435
436 /**
437  * @brief This function is executed in case of error occurrence.
438  * @retval None
439  */
440 void Error_Handler(void)
441 {
442     /* USER CODE BEGIN Error_Handler_Debug */
443     /* User can add his own implementation to report the HAL error return state */
444     __disable_irq();
445     while (1)
446     {
447     }
448     /* USER CODE END Error_Handler_Debug */
449 }
450
451 #ifndef USE_FULL_ASSERT
452 /**
453  * @brief Reports the name of the source file and the source line number
```



```
454 *           where the assert_param error has occurred.
455 * @param   file: pointer to the source file name
456 * @param   line: assert_param error line source number
457 * @retval  None
458 */
459 void assert_failed(uint8_t *file, uint32_t line)
460 {
461     /* USER CODE BEGIN 6 */
462     /* User can add his own implementation to report the file name and line number,
463        ex: printf("Wrong parameters value: file %s on line %d\r\n", file, line) */
464     /* USER CODE END 6 */
465 }
466 #endif /* USE_FULL_ASSERT */
467
```