

# Winter semester 2016/2017

## Group plan 7TCS1

|           | 08:15 09:00   | 09:15 10:00   | 10:15 11:00   | 11:15 12:00  | 12:15 13:00 | 13:15 14:00 | 14:15 15:00  | 15:15 16:00 | 16:15 17:00   | 17:15 18:00 | 18:15 19:00 | 19:15 20:00 |
|-----------|---|---|---|--|-------------|-------------|--|-------------|---|-------------|-------------|-------------|
| Mo        |   |   |   |  |             |             |  |             |   |             |             |             |
| Tuesday   |   |   |   |  |             |             | laboratory<br><b>Wireless Communication</b><br><i>Institute of Electronics,<br/>Building B9, room 320a</i> |             | seminar<br><b>Final Project Seminar</b><br>dr hab. inż. Sławomir Hausman<br><i>Institute of Electronics,<br/>Building B9, room 321a</i> |             |             |             |
| Wednesday | seminar<br><b>Final Project Seminar</b><br>prof. dr hab. Andrzej Napieralski<br><i>Department of Microelectronics and<br/>Computer Science, Building B18, room A2</i> | lecture<br><b>Computer Aided Design of Electronic Circuits</b><br>prof. dr hab inż. Zygmunt Ciota<br><i>Building B18, room A1</i> | laboratory<br><b>Computer Aided Design of Electronic Circuits</b><br><i>Building B18, lab M</i> | lecture<br><b>Wireless Communication</b><br>dr hab. inż. Sławomir Hausman<br><i>Institute of Electronics, Building B9, room 321a</i> |             |             |  |             |   |             |             |             |
| Thu       |   |   |   |  |             |             |  |             |   |             |             |             |
| Fri       |   |   |   |  |             |             |  |             |   |             |             |             |